



## Product Statement of Volatility

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Arista Networks, Inc.

## Revision Tracking

Date	Revision	Changes/Additions
8/9/2011	1.0	Document created
8/17/2011	1.1	Document review comments incorporated.
11/8/2011	1.2	Added DCS-7050T, DCS-7124FX and DCS-7050Q
4/21/2012	1.3	Updated DCS-7124FX and fixed typos
9/14/2012	1.4	Added 7140T and 7120T
1/12/2013	1.5	Added removable optics
4/15/2013	1.6	Added DCS-7050QX-32
6/10/2013	1.7	Added DCS7150S-XX, DCS-7500E Supervisor Module, DCS-7504E Fabric Module, DCS-7508E Fabric Module, DCS-7500E-36Q Linecard, and DCS-7500E-xxS Linecard
2/3/2014	1.8	Added 2-RU switch CPU card, DCS-7050SX-128
2/6/2014	1.9	Added DCS-7250QX-64 cards
2/7/2014	1.10	Added PCA-00459-0x Crow CPU card
3/17/2014	1.11	Added 7050TX and 7010T-48
3/24/2014	1.12	Added DCS-7050SX-96 and DCS-7050SX-72
3/26/2013	1.13	Added DCS-7050QX-32S
4/7/2014	1.14	Added DCS-7500E-6C2-LC Linecard
4/28/2014	1.15	Added DCS-7050SX-64
5/14/2014	1.16	Added Jenner section and DCS-7280SE-64
5/30/2014	1.17	Added DCS-7316 Fabric Module
6/16/2014	1.18	Added photos of 7010T-48
6/18/2014	1.19	Added section 7.4. Updated 7.1
6/18/2014	1.20	Fix typo in revision tracking table (DCS-7050X-32 -> DCS-7050QX-32; DCS-7050X-32S -> DCS-7050QX-32S)
6/25/2014	1.21	Updated Section 6.7 to include SSD and front panel memory stick.
7/8/2014	1.22	Corrected typos in 7048T section (was 7148T)
7/16/2014	1.23	Added DCS-7050TX-96/72, changed DCS-7050TX-XX to DCS-7050TX-64/48

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## **1. Introduction**

This document is intended to provide system memory identification, location and use information for the Arista switch product line. This information can be used as a reference document for sanitization procedures prior to release outside of controlled or sensitive environments. Additionally, procedures to remove non-volatile memory containing user data are included to ensure damage does not occur to the equipment during sanitization. Questions regarding memory components or removal procedures can be directed to your Arista account team.

### **1.1. Volatile Memory**

Volatile memory, also known as volatile storage, is that which requires power to retain data. Examples of volatile memory are DRAM and SRAM. Volatile memory will purge retained information when the system is powered off for 10 seconds or more.

### **1.2. Non-Volatile Memory**

Non-volatile memory is that which continues to retain information even when the system power is turned off. This type of memory is used to retain persistent information that must survive a system reboot or loss of power. In Arista switches, the non-volatile memory is in the form of Flash RAM.

Note: A small lithium battery maintains only the time of day and the most recent reset cause in the Real Time Clock chip for a minimum of 10 years. The battery can be removed if desired to clear this information.

## 2. Arista Napa Family Product Memory

### 2.1. DCS-7148SX Memory Map

Table 1-DCS-7148SX Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	SDRAM	CPU Memory	Yes	Yes	2GB
2	SRAM	Ethernet Switch Internal RAM	No	No	
3	SRAM	PHY	No	No	

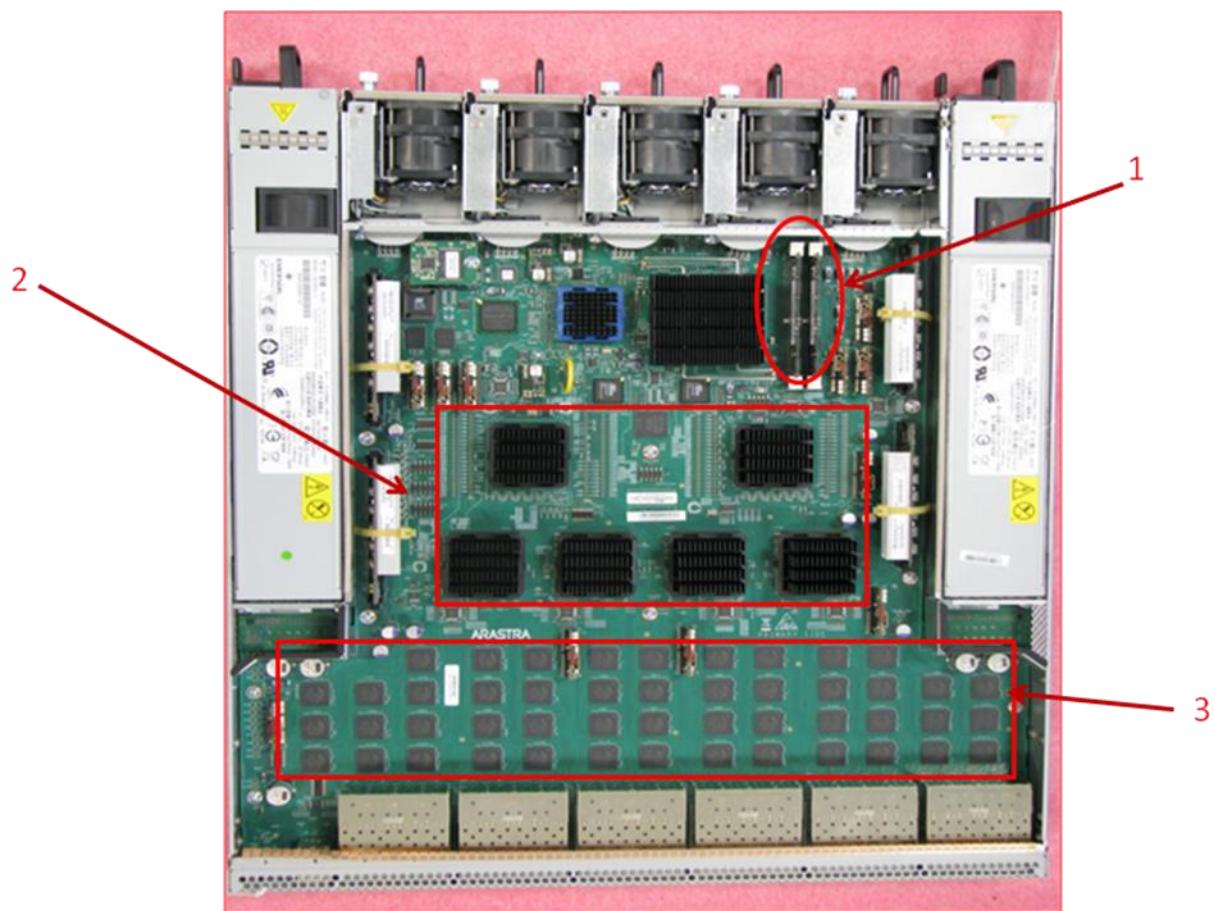


Figure 1-DCS-7148SX Volatile Memory Locations

Table 2-DCS-7148SX Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Power supply manufacturer data	Yes	No	2KB
2	Flash	Power controller	No	No	
3	Flash	PC Board manufacturer data	No	No	64KB
4	Flash	PC Board MAC address	No	No	2KB
5	Flash	SFP Module manufacturer data	Yes	No	2KB
6	Flash	CPU Firmware	Yes	No	2MB
7	Flash	Software and user configuration	Yes	Yes	2GB
8	Microcontroller	Security	No	No	134KB
9	Flash	FPGA configuration	No	No	500KB
10	USB	Front panel (optional)	Yes	Yes	Various
11	Flash	Fan module manufacturer data	Yes	No	2KB
12	Real Time Clock	Stores time of day and date	Yes	No	

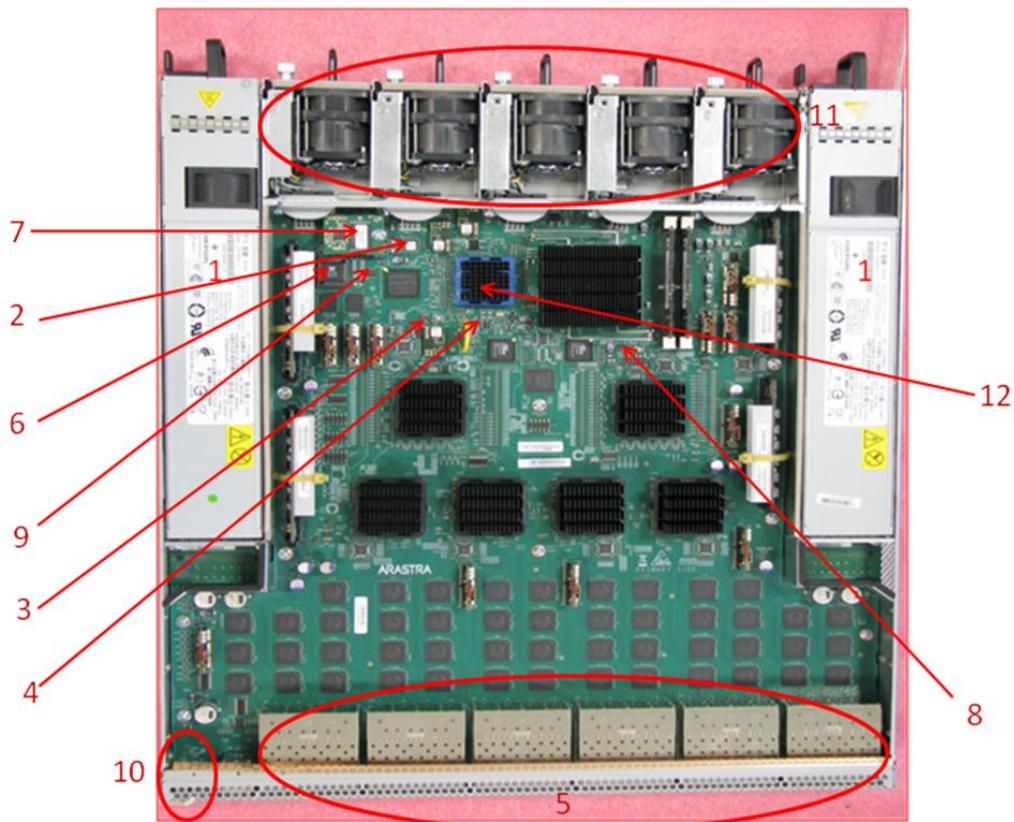


Figure 2-DCS-7148SX Non-volatile Memory Locations

## 2.2. DCS-7048T Memory Map

Table 3-DCS-7048T Volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	SDRAM	CPU Memory	Yes	Yes	2GB
2	DRAM-DDR3	Temporary Packet Storage	No	Yes	512MB
3	SRAM-QDR2	Look-up Tables	No	Yes	36Mb

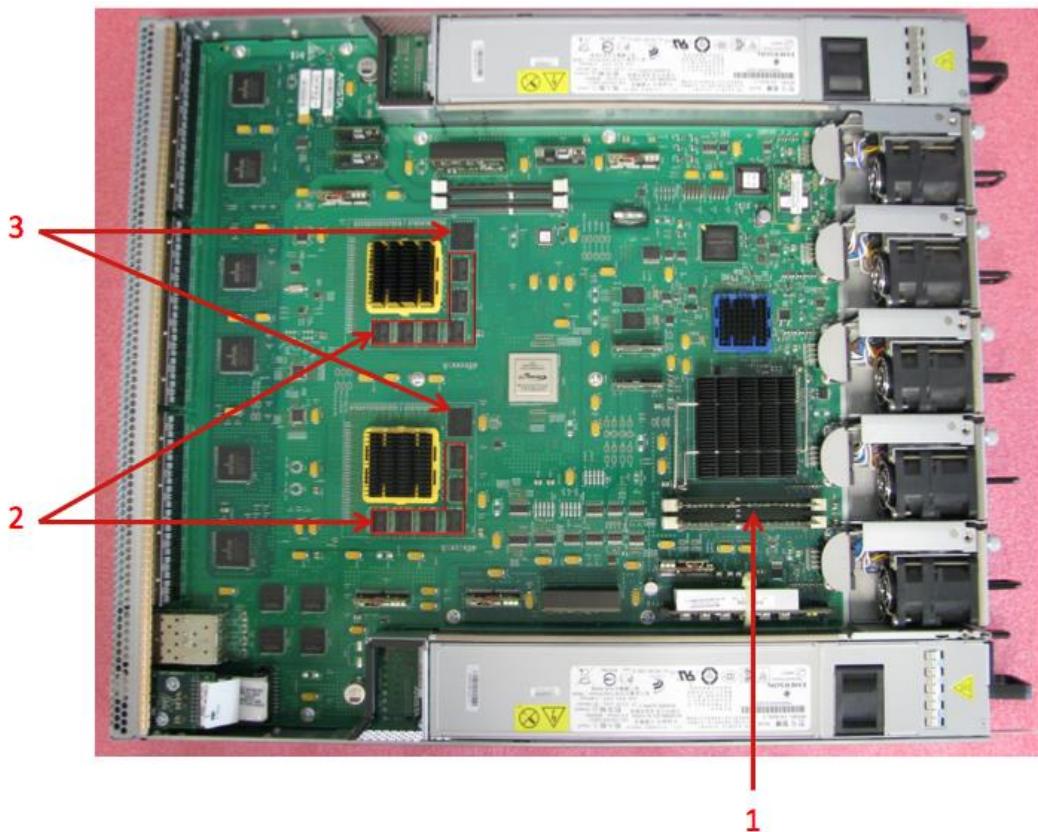


Figure 3-DCS-7048T Volatile Memory Locations

Table 4-DCS-7048T Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	PC Board MAC address	No	No	2KB
2	Microcontroller	Security	No	No	134KB
3	Flash	BIOS flash memory	No	No	2MB
4	Flash	Software and user config	Yes	Yes	2GB
5	Flash	FPGA external memory 1	No	No	512KB
6	Flash	FPGA external memory 2	No	No	512KB
7	Flash	PC Board manufacturer data	No	No	64KB
8	Flash	Power supply manufacturer data	Yes	No	2KB
9	Flash	Fan manufacturer data	Yes	No	2KB
10	USB	Front panel (optional)	Yes	Yes	Various
11	Flash	Power Controller	No	No	

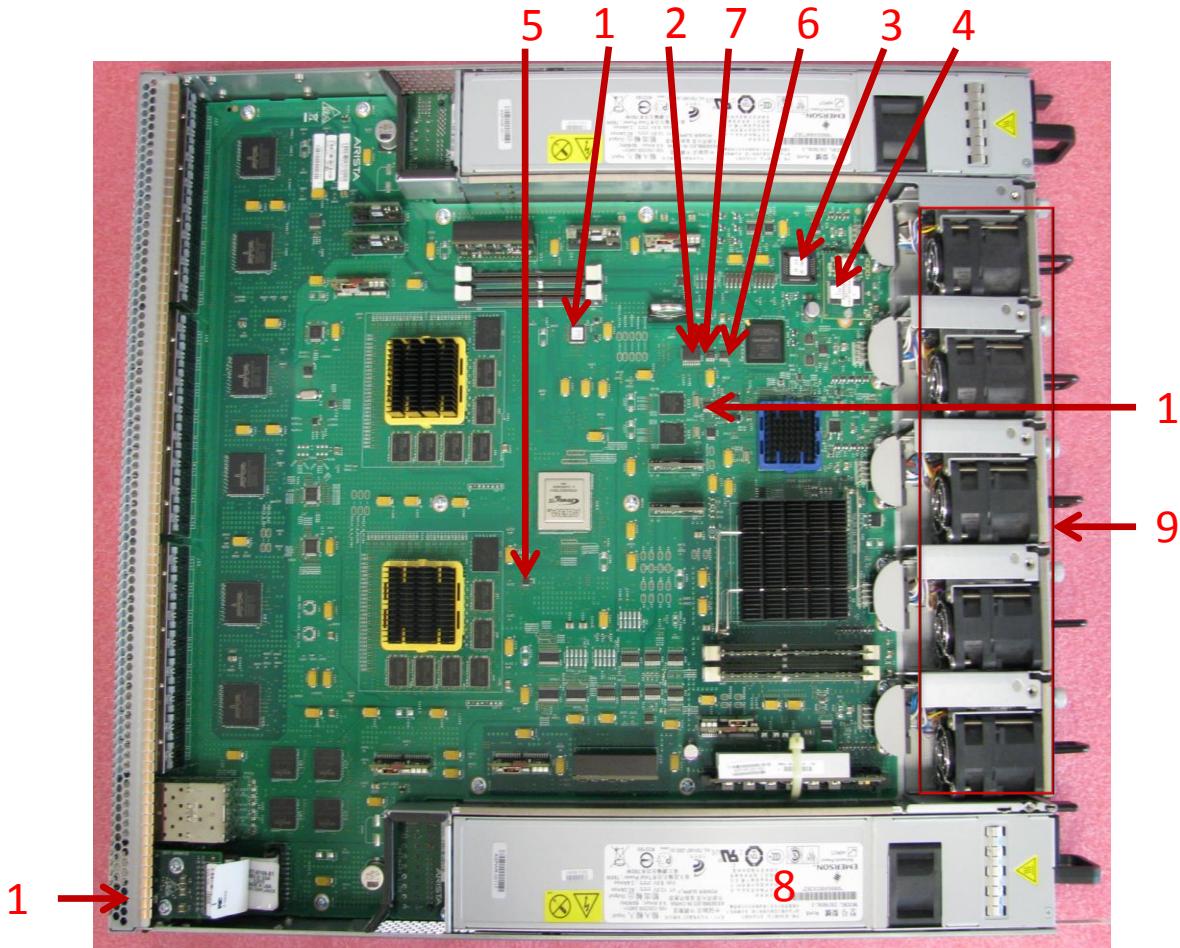


Figure 4-DCS-7048T Non-volatile Memory Location

## 2.3. DCS-7120T Memory Map

Table 5-DCS-7120T Volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	SDRAM	CPU Memory	Yes	Yes	2GB

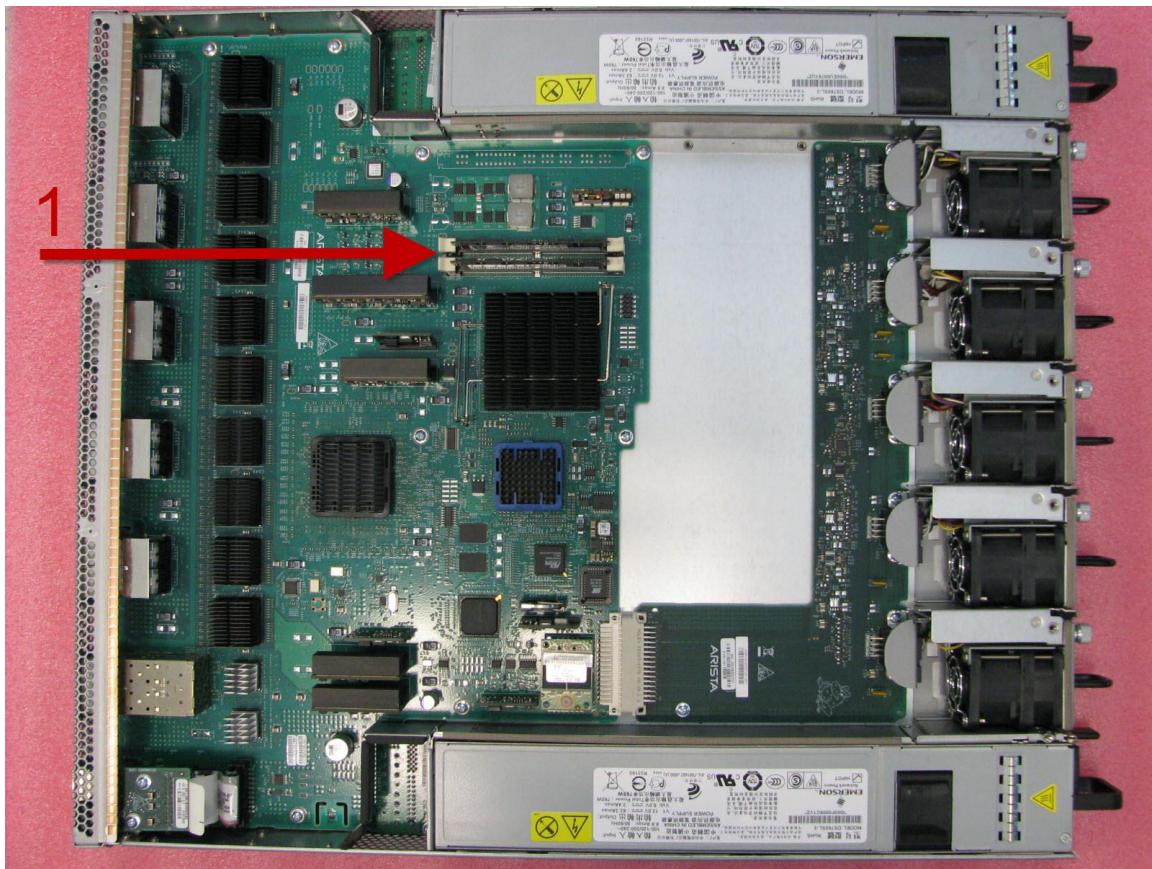


Figure 5-DCS-7120T Volatile Memory Locations

Table 6-DCS-7120T Non-volatile Memory Map

<b>Location</b>	<b>Type</b>	<b>Application</b>	<b>Removable</b>	<b>User Data</b>	<b>Size</b>
1	Flash	PC Board MAC address	No	No	2KB
2	Microcontroller	Security	No	No	134KB
3	Flash	BIOS flash memory	No	No	2MB
4	Flash	Software and user config	Yes	Yes	2GB
5	Flash	FPGA external memory	No	No	512KB
6	Flash	PC Board manufacturer data	No	No	64KB
7	Flash	Power supply manufacturer data	Yes	No	2KB
8	Flash	Fan manufacturer data	Yes	No	2KB
9	USB	Front panel (optional)	Yes	Yes	Various
10	Flash	Power Controller	No	No	very small
11	Flash	PHY firmware storage	No	No	20 x 0.5MB

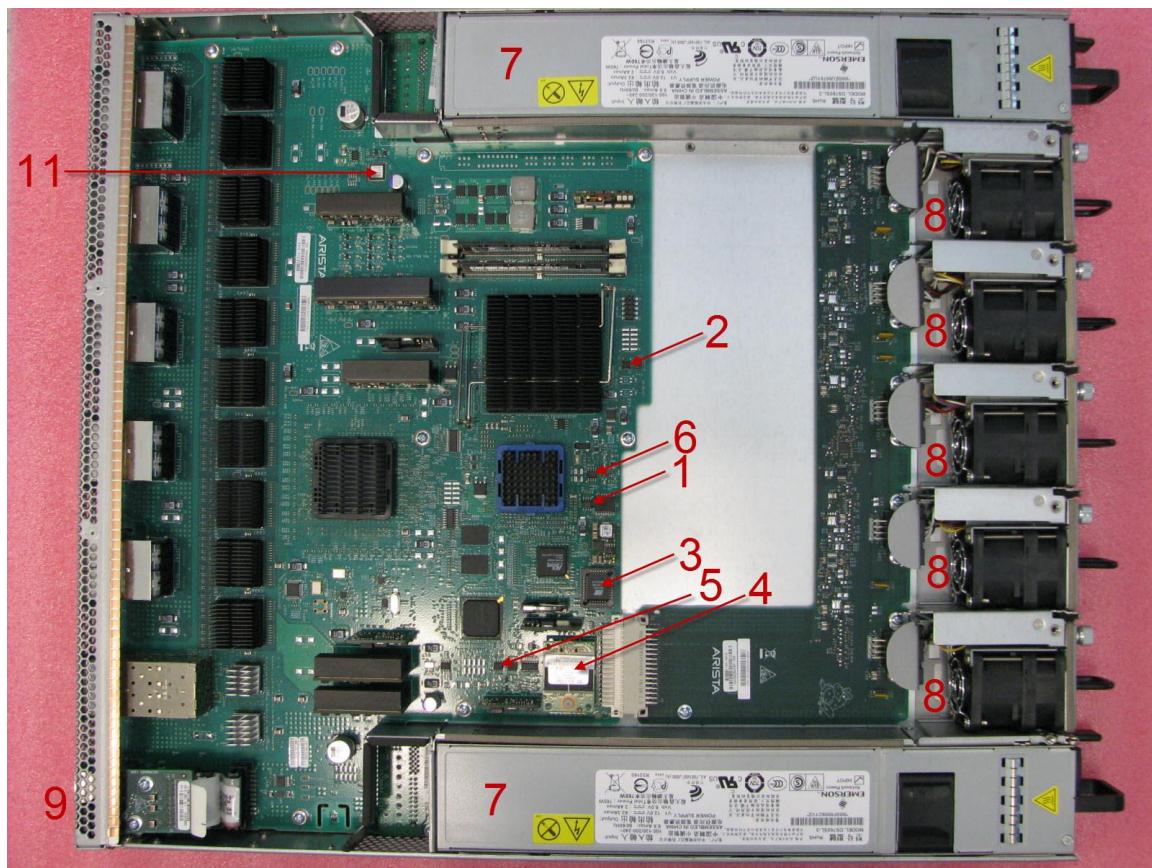


Figure 6-DCS-7120T Non-volatile Memory Locations – Top

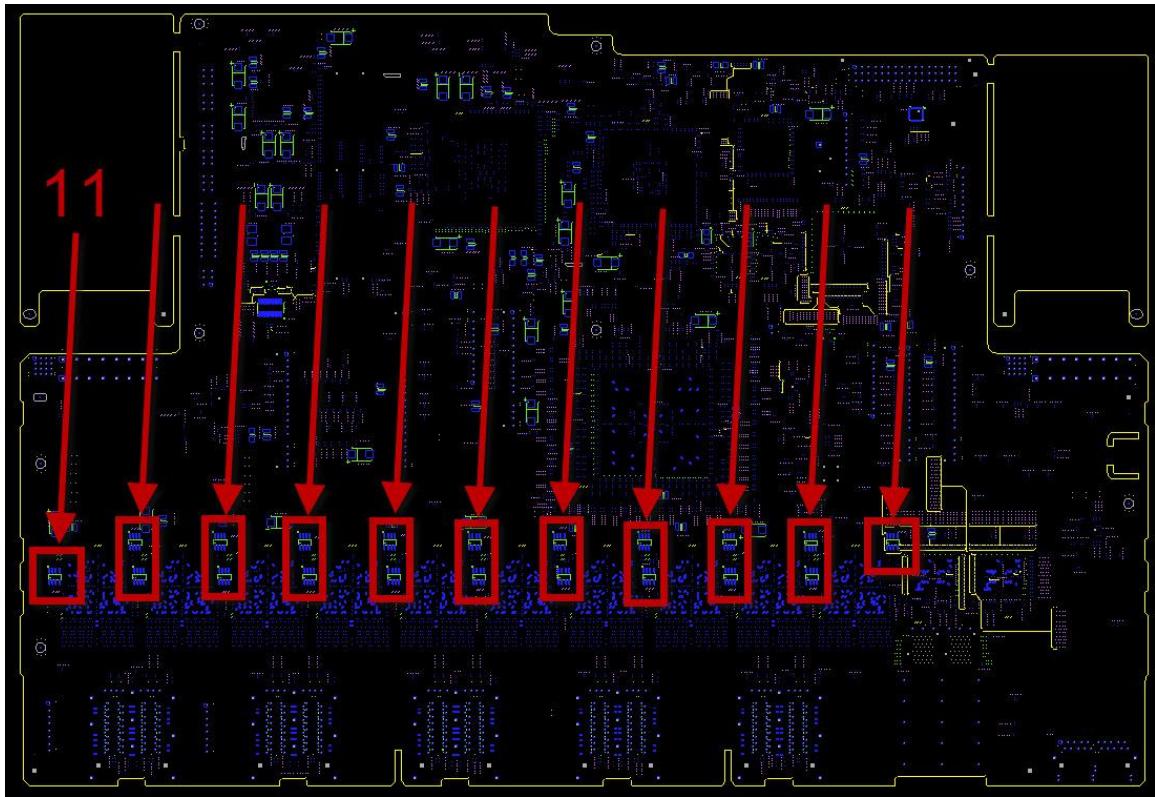


Figure 7 -DCS-7120T Non-volatile Memory Locations - Bottom

## 2.4. DCS-7140T Memory Map

Table 7-DCS-7140T Volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	SDRAM	CPU Memory	Yes	Yes	2GB

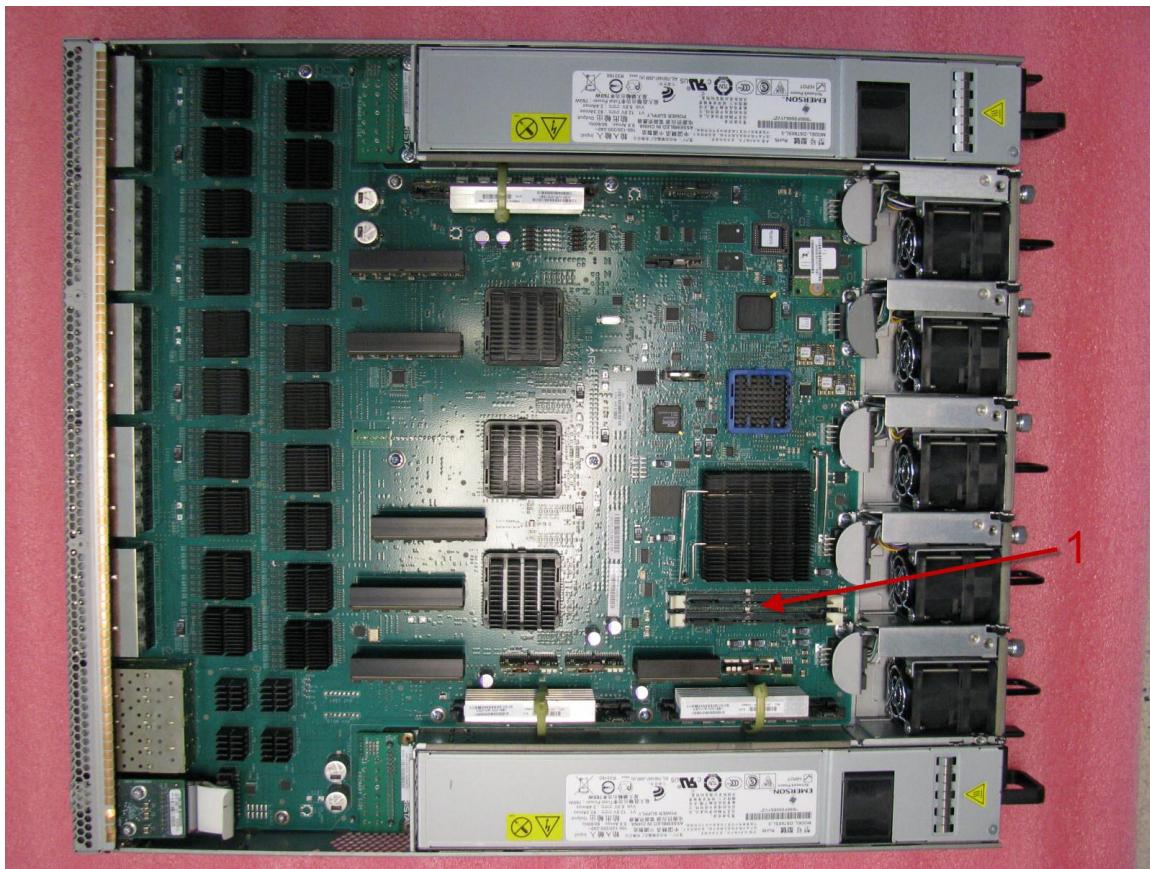


Figure 8-DCS-7140T Volatile Memory Locations

Table 8-DCS-7140T Non-volatile Memory Map

<b>Location</b>	<b>Type</b>	<b>Application</b>	<b>Removable</b>	<b>User Data</b>	<b>Size</b>
1	Flash	PC Board MAC address	No	No	2KB
2	Microcontroller	Security	No	No	134KB
3	Flash	BIOS flash memory	No	No	2MB
4	Flash	Software and user config	Yes	Yes	2GB
5	Flash	FPGA external memory	No	No	512KB
6	Flash	PC Board manufacturer data	No	No	64KB
7	Flash	Power supply manufacturer data	Yes	No	2KB
8	Flash	Fan manufacturer data	Yes	No	2KB
9	USB	Front panel (optional)	Yes	Yes	Various
10	Flash	Power Controller	No	No	very small
11	Flash	PHY firmware storage	No	No	40 x 0.5MB

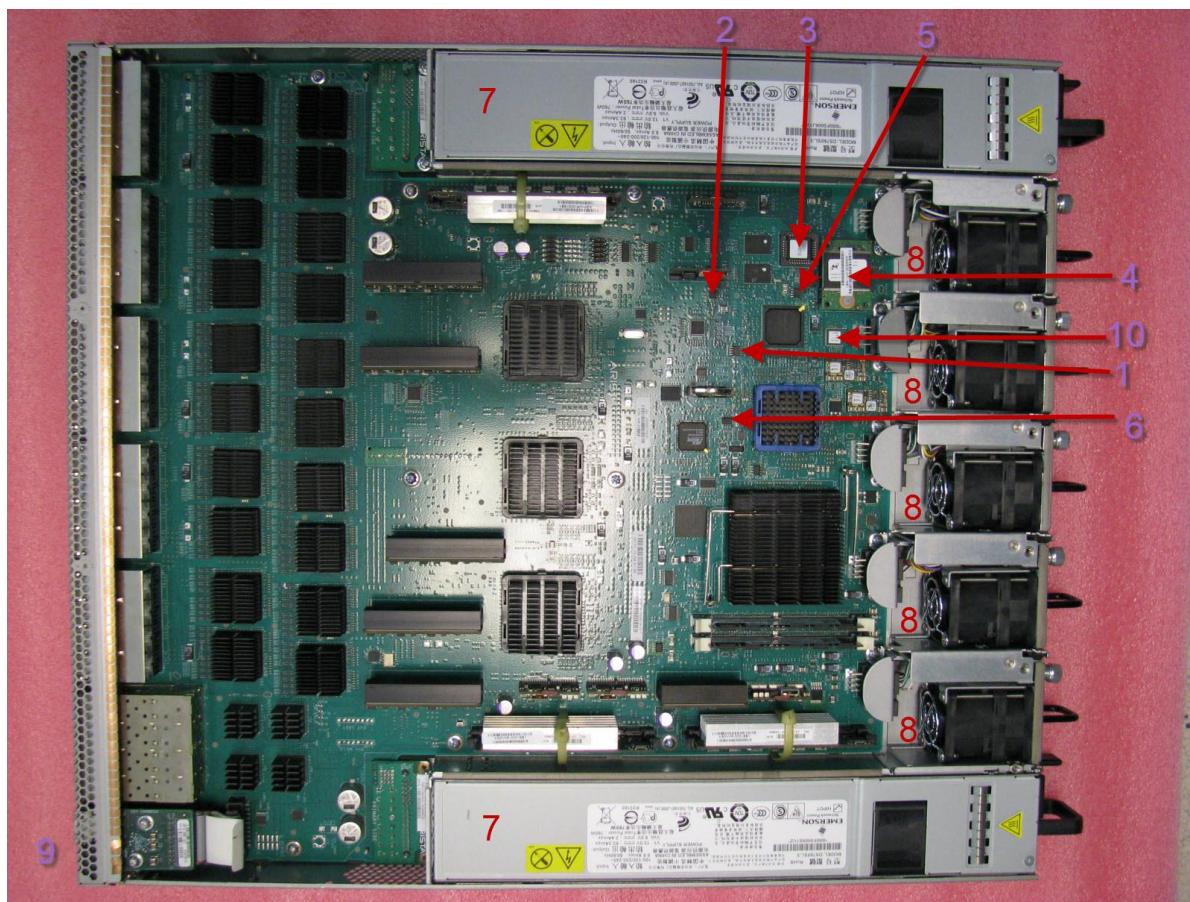


Figure 9-DCS-7140T Non-volatile Memory Locations – Top

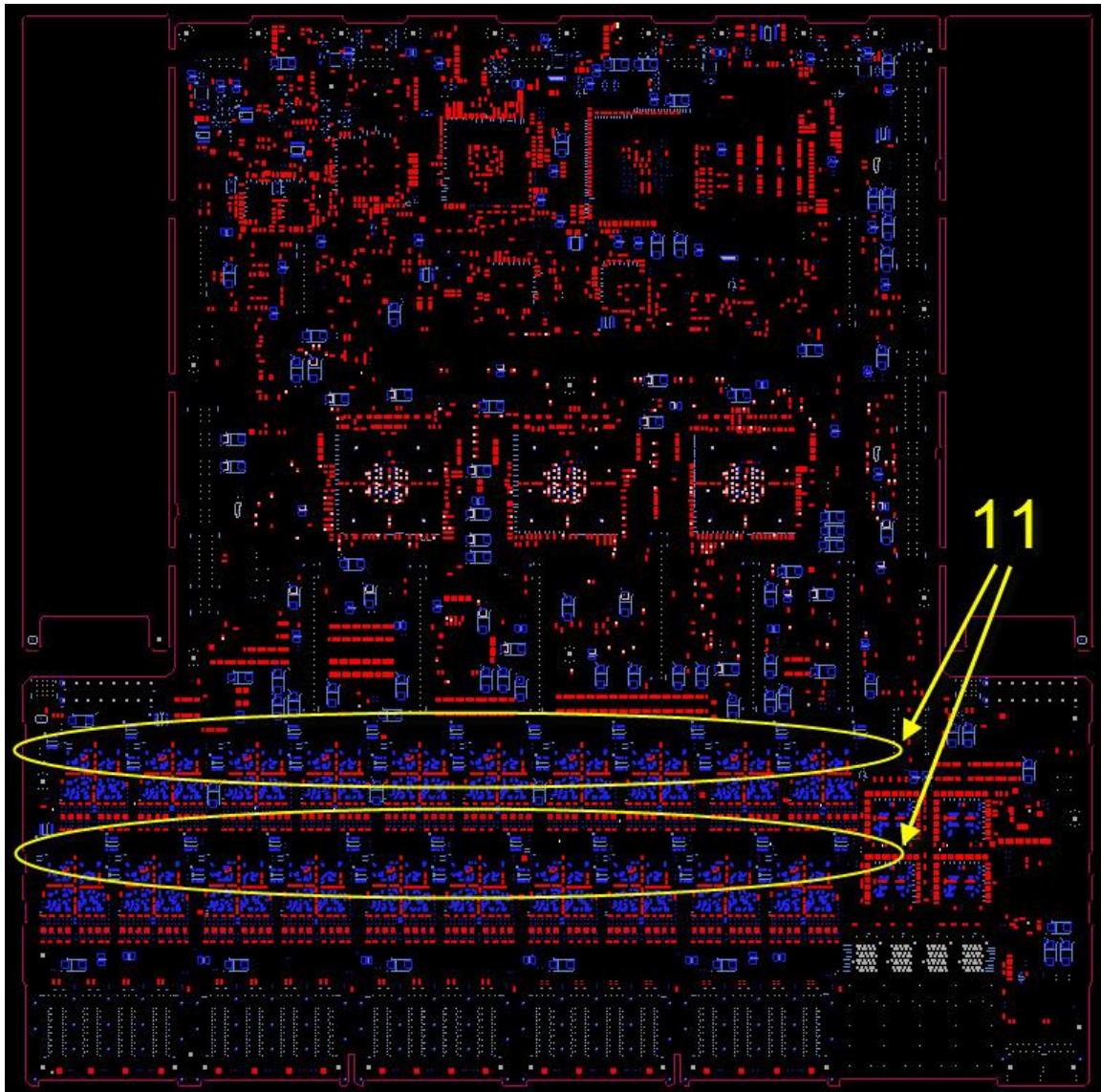


Figure 10-DCS-7140T Non-volatile Memory Locations – Bottom

### 3. Arista Bodega Family Product Memory

All Bodega family products are designed with two separate boards, the CPU board and switch board. There are two CPU cards called “Raven” and “Crow”. Each switch board is a separate entity that plugs into the CPU board.

#### 3.1. Raven CPU Board Memory Map

Table 9- CPU Board Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	DRAM – DDR3	CPU Memory	Yes	Yes	2GB

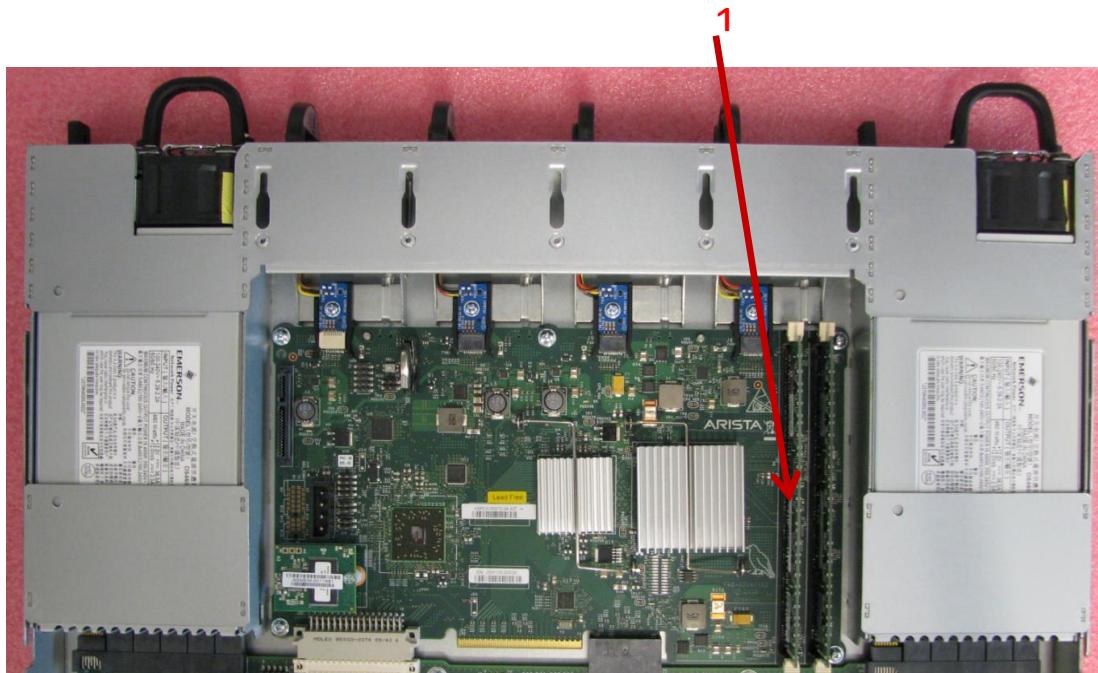


Figure 11-CPU Board Volatile Memory Locations

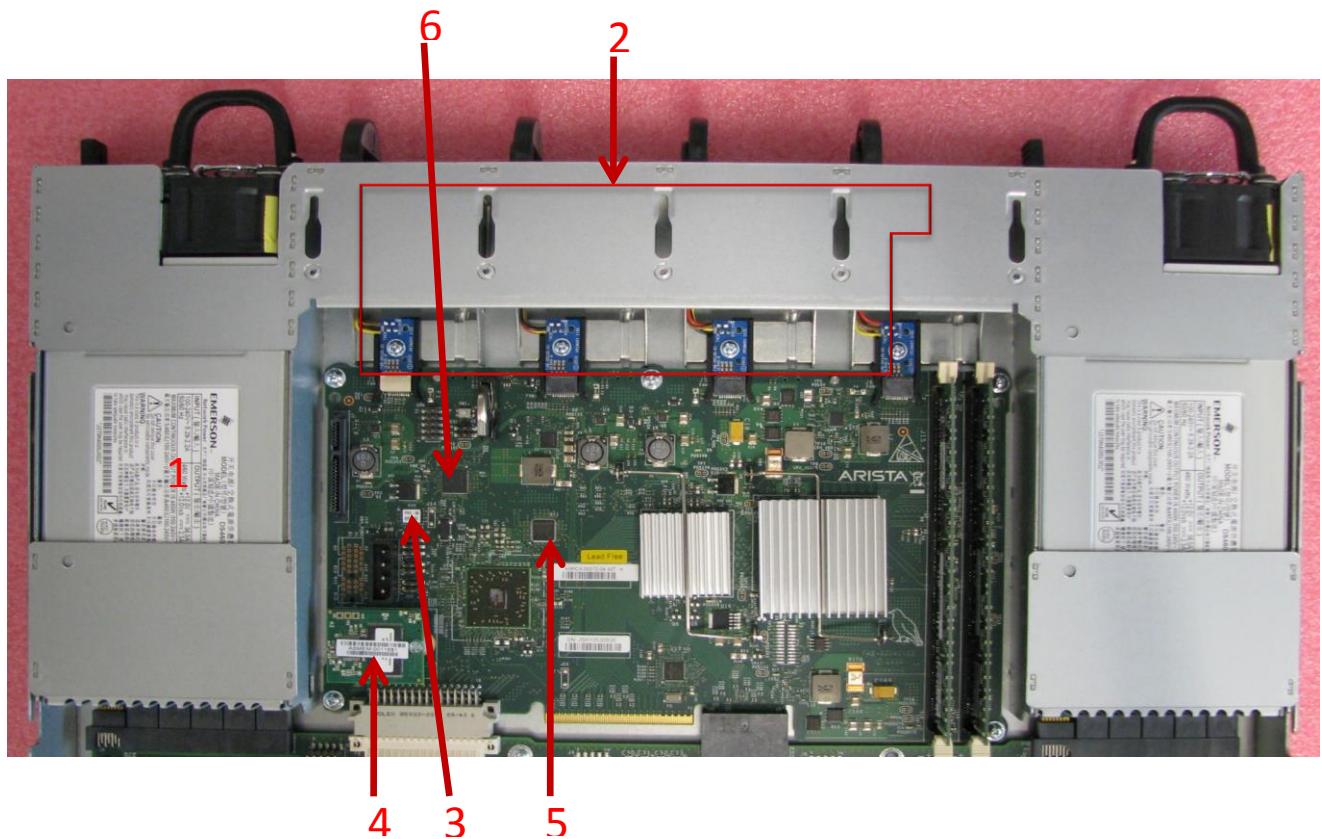


Figure 12- Raven CPU Board Non-Volatile Memory Locations

Table 10- Raven CPU Board Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	2KB
2	Flash	Manufacturer data	NO	No	2KB
3	Flash	Boot Memory	No	No	64MB
4	Flash	Software/User config	Yes	Yes	2GB
5	Microprocessor	LPC Bus Controller	No	No	NA
6	Flash	Power Controller	No	No	NA

### 3.1.1. Solid State Drive (SSD) Memory Map (Optional)

Table 11- Raven SSD Memory Map

Location	Type	Application	Removable	User data	Size
1	SSD	User data	Yes	Yes	50GB



Figure 13-CPU Board SSD Memory Location

*Note: The Crow CPU also supports this optional SSD, but it is not planned to be offered for sale.*

### 3.2. Crow CPU Card PCA-00459-xx

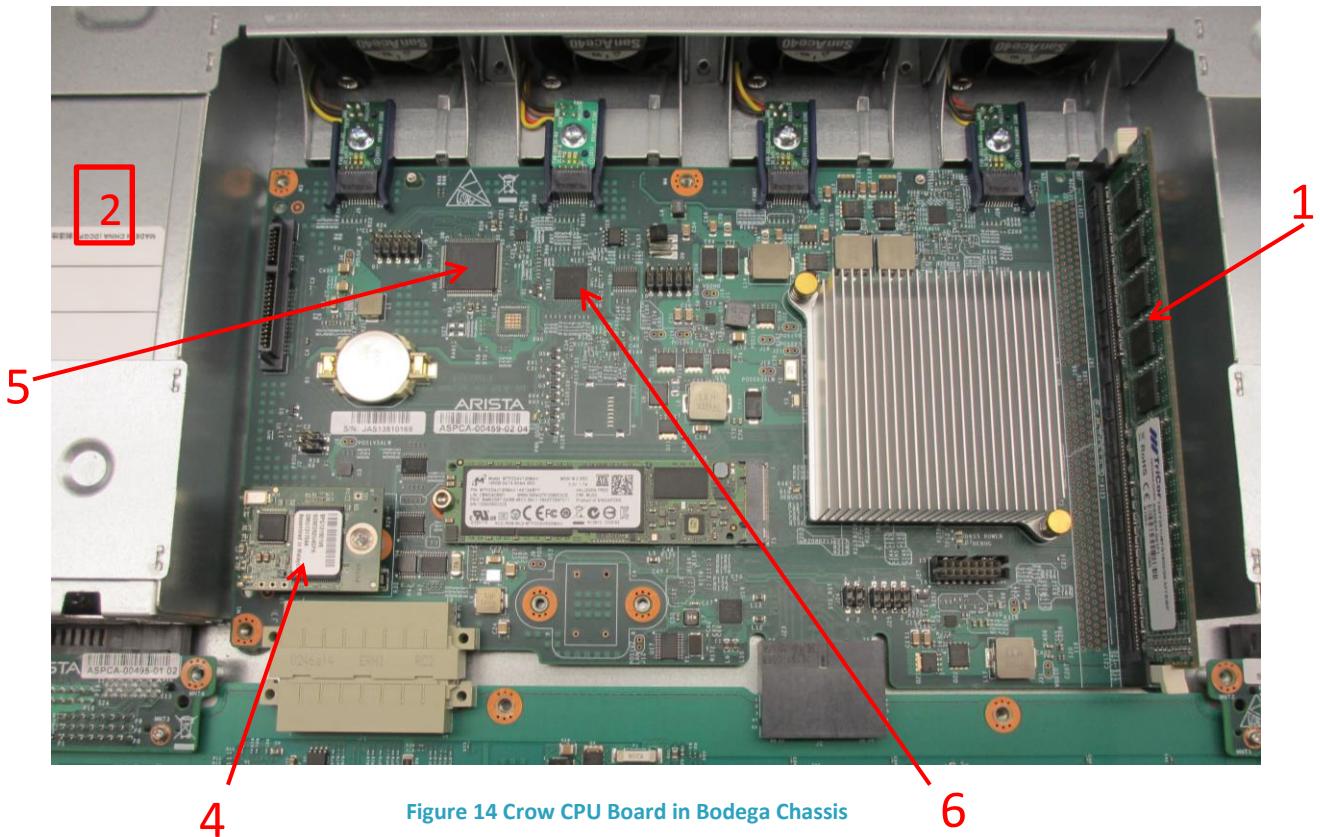


Figure 14 Crow CPU Board in Bodega Chassis

Table 12 Crow Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	DRAM – DDR3	CPU Memory	Yes	Yes	4GB

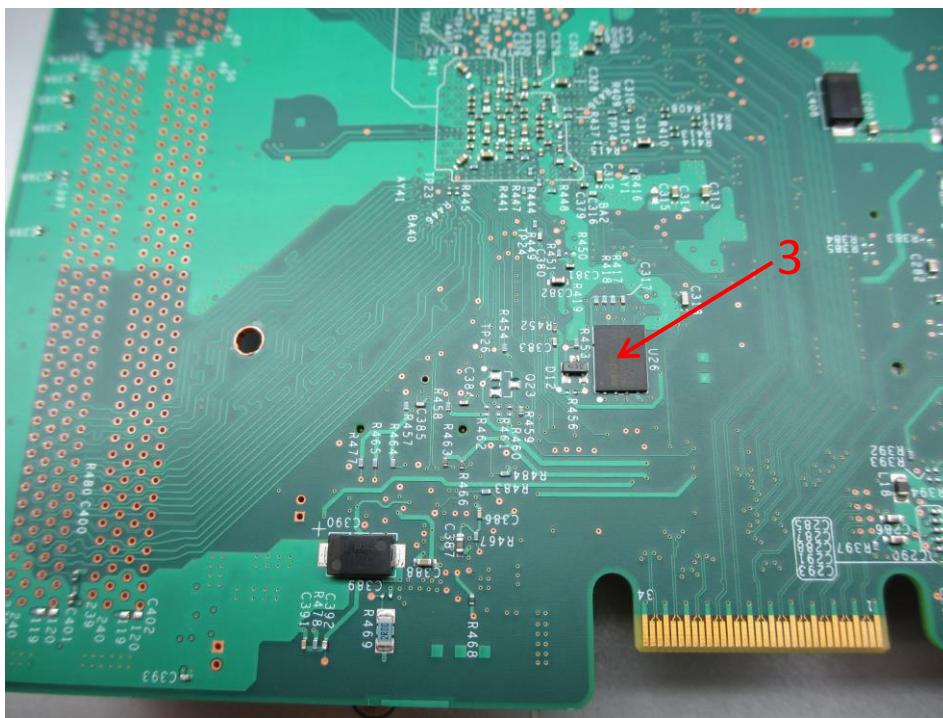
Note: The removable DIMM also has a non-volatile "SPD" SEPROM to identify itself, pre-written with Manufacturing data.

Table 13 Crow CPU Board Non-Volatile Memories

Location	Type	Application	Removable	User Data	Size
2 (inside both power supplies)	FLASH	Manufacturer data	No, but the supply is	No	2KB

			removable		
3 (secondary side)	Flash	Boot Memory	No	No	64Mbit
4	eUSB FLASH	Software/User config	Yes	Yes	4GB
5	CPLD internal configuration FLASH	UART and fan controller CPLD	No	No	Unknown
6	Flash	Power Controller UCD90120A	No	No	Unknown

**Table 14** Crow CPU, Secondary Side



### 3.2.1. Crow M.2 SSD [Optional]

The Crow CPU has a location for an optional M.2 SSD FLASH shown below, retained by one screw. Crow also supports the 2.5" SSD described previously for Raven but it is not planned to be offered for sale.

Table 15 Crow CPU M.2 SSD Option Location

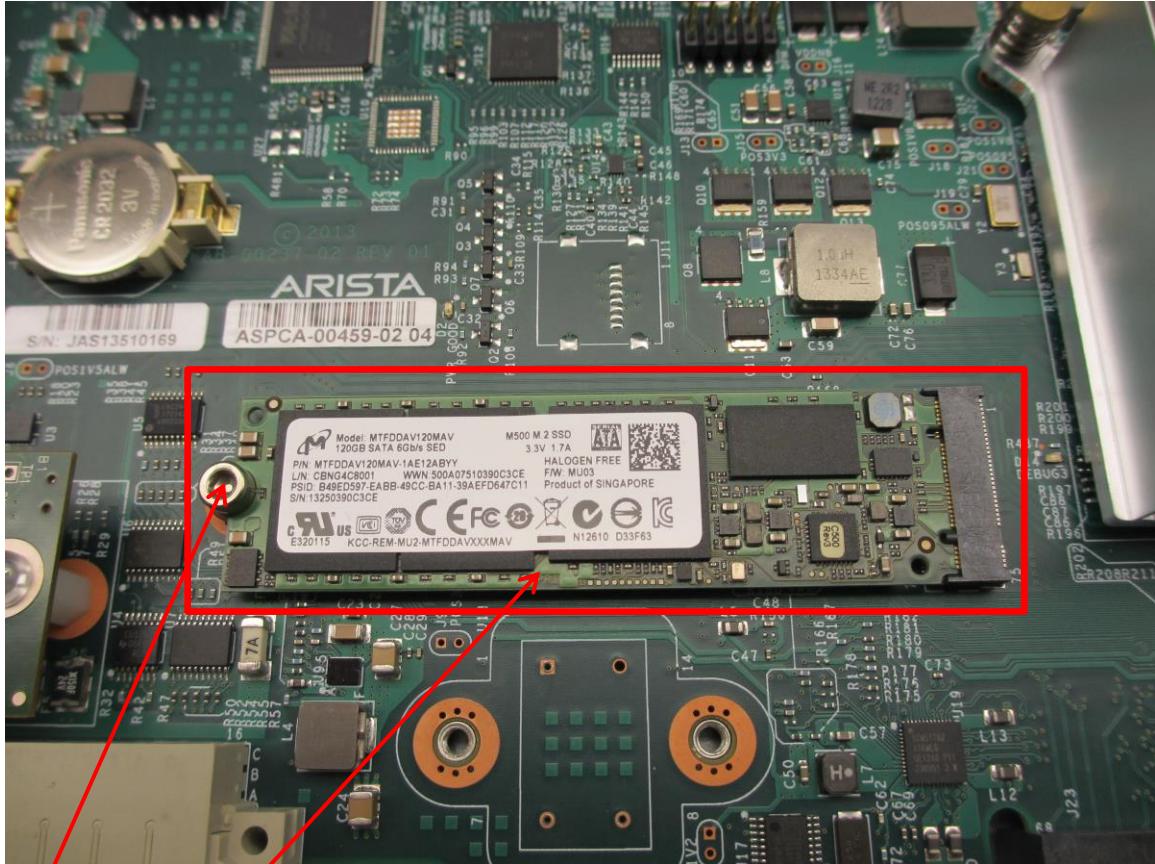


Table 16 Crow M.2 SSD Option

Location	Type	Application	Removable	User data	Size
1	SSD FLASH	User data	Yes	Yes	120GB

Note: Other storage capacities could be offered from time to time. The SSD shown is sized 2280 (80mm); Crow also supports 42mm length option boards in the same location but none are planned for sale.

### 3.2.2. DCS-7124SX Memory Map

Table 17-DCS-7142SX Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA configuration	No	No	8MB
5	Flash	CPLD	No	No	8KB
6	USB	User data	Yes	Yes	Various
7	Flash	SFP manufacturer data	Yes	No	2KB
8	Flash	Power Controller	No	No	NA

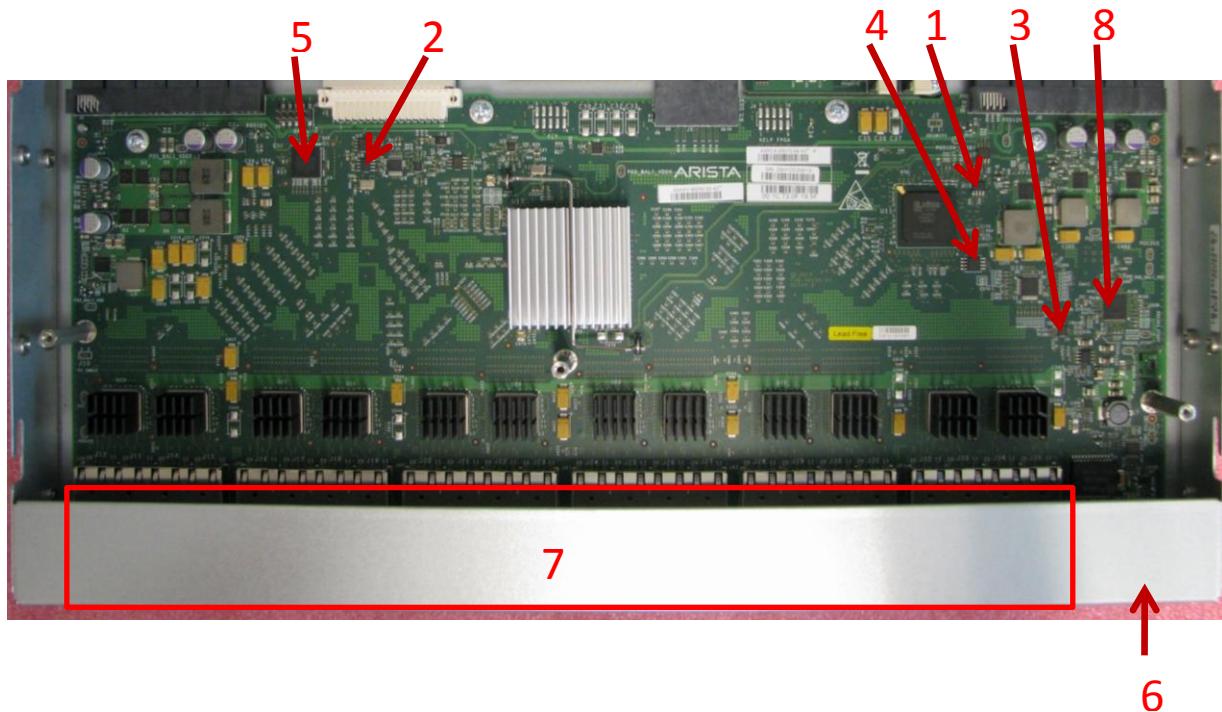


Figure 15-DCS-7124SX Non-volatile Memory Locations

### 3.3. DCS-7050S-XX Memory Map

Table 18 - DCS-7050S-52 Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA external flash 1	No	No	8MB
5	Flash	FPGA external flash 2	No	No	8MB
6	Flash	CPLD	No	No	8KB
7	USB	User data	Yes	Yes	Various
8	Flash	SFP manufacturer data	Yes	No	2KB
9	Flash	Manufacturer data	No	No	16KB
10	Flash	Power Controller	No	No	NA

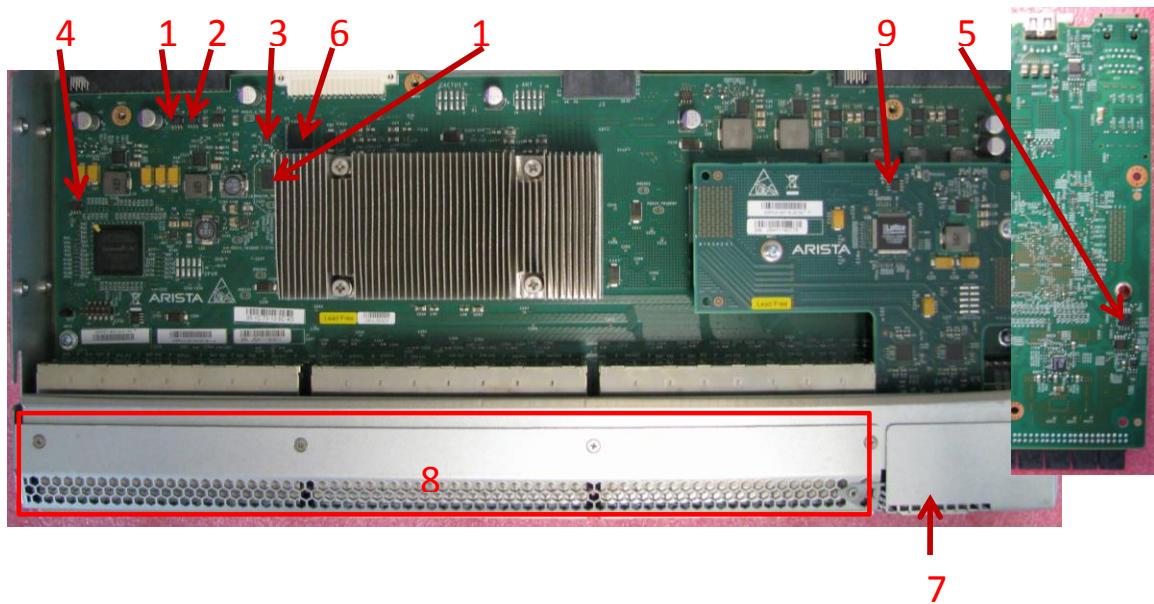


Figure 16-DCS-7050S-52 Non-volatile Memory Locations

Table 19-DCS-7050S-64 Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EERPOM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA external flash 1	No	No	8MB
5	Flash	FPGA external flash 2	No	No	8MB
6	Flash	CPLD	No	No	8KB
7	USB	User data	Yes	Yes	Various
8	Flash	SFP Module manufacturer data	Yes	No	2KB
9	Flash	Mezzanine Board Manufacturer data	No	No	16KB
10	Flash	Power Controller	No	No	NA

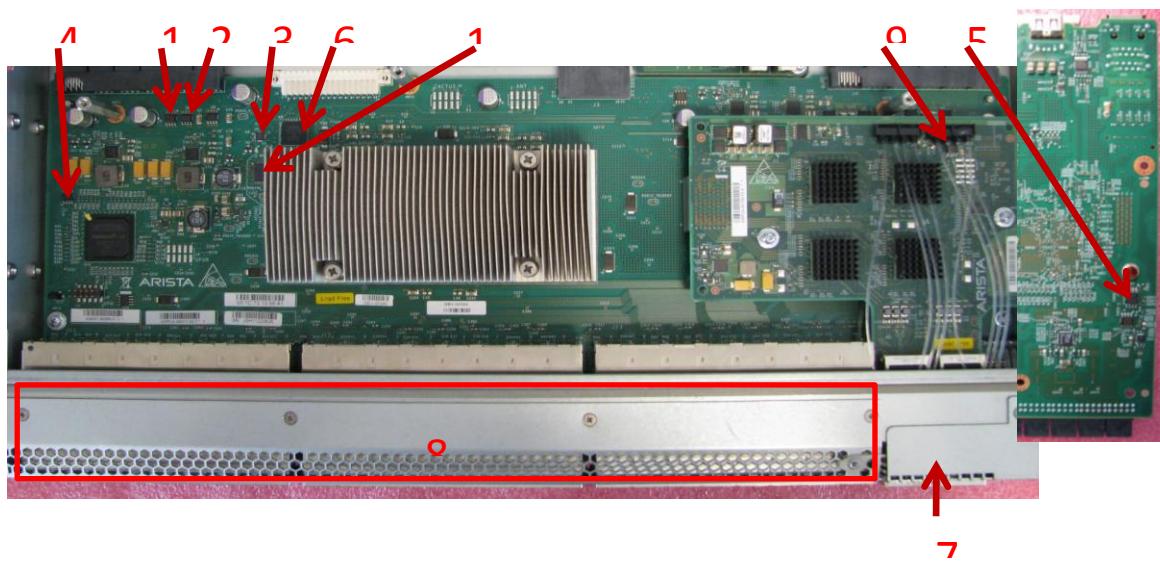


Figure 17-DCS-7050S-64 Non-volatile Memory Locations

### 3.4. DCS-7050T-XX Memory Map

Table 20-DCS-7050T-XX-X Non-volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	FLASH	FPGA configuration	No	No	8MB
4	Microcontroller	Security	No	No	134KB
5	FLASH	Power configuration	No	No	NA
6	FLASH	CPLD	No	No	8KB
7	EEPROM	Manufacturer data	No	No	16KB
Front Panel	USB	User data	Yes	Yes	Various

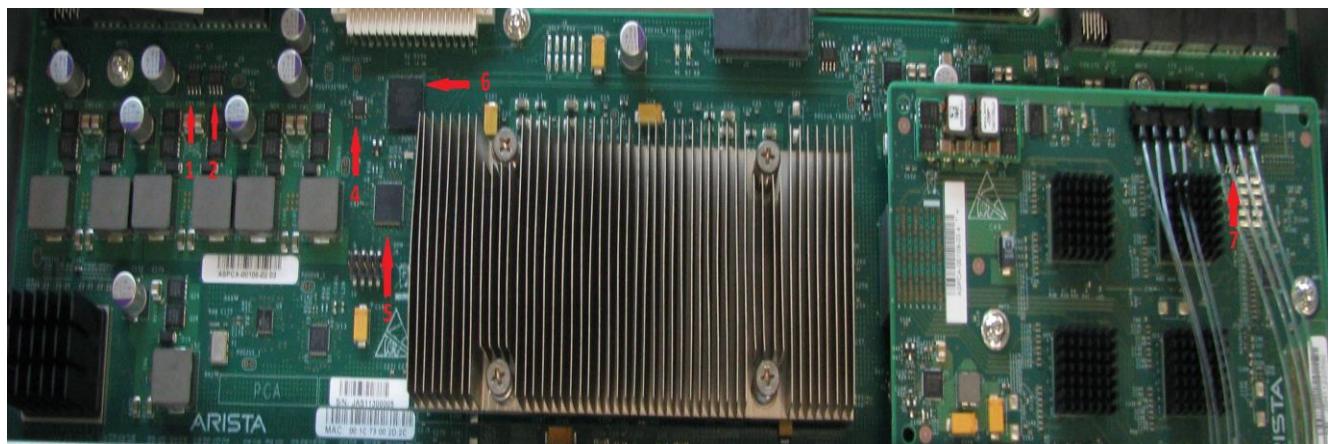


Figure 18-DCS-7050T-XX Non-volatile Memory Locations (Front)



Figure 19-DCS-7050T-XX Non-volatile Memory Locations (Back)

### 3.5. DCS-7050TX-64/48 Memory Map

Table 21-DCS-7050TX-64-X, DCS-7050-TX-48-X Non-volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	EEPROM (U1)	Manufacturer data	No	No	512Kb
2	EEPROM (U2)	Manufacturer data	No	No	16Kb
3	FLASH (U11)	FPGA configuration	No	No	64Mb
4	Microcontroller (U3)	Security	No	No	134KB
5	FLASH (U9)	Power configuration	No	No	NA
6	FLASH (U4)	CPLD	No	No	8KB
7	EEPROM (U17)	Manufacturer data	No	No	64Mb
Front Panel	USB	User data	Yes	Yes	Various
Front Panel	Optics	Manufacturer data	Yes	No	Various

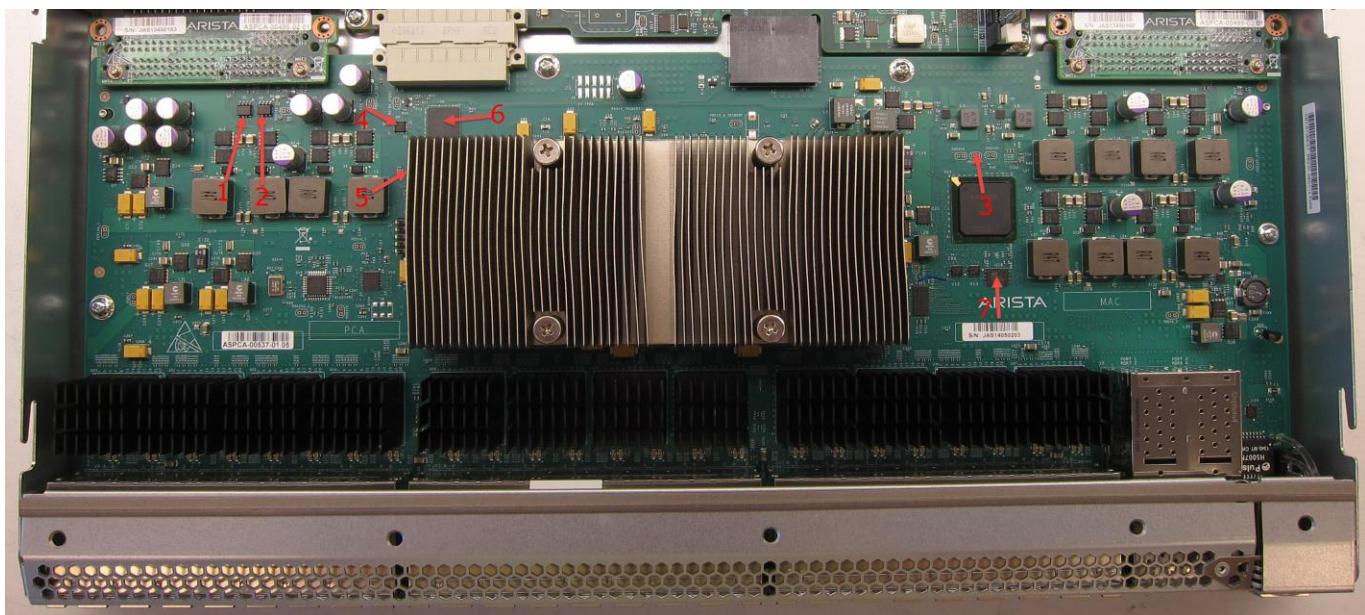


Figure 20-DCS-7050TX-XX Non-volatile Memory Locations (Front)

### 3.6. DCS-7124FX Memory Map

Table 22-DCS-7124FX Non-volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	FLASH	FPGA configuration	No	No	8MB
4	Microcontroller	Security	No	No	134KB
5	FLASH	CPLD	No	No	8KB
6	FLASH	Power controller	No	No	NA
7	FPGA / Battery	FPGA Security keys	Yes – remove battery	No	NA
Front Panel	USB	User data	Yes	Yes	Various

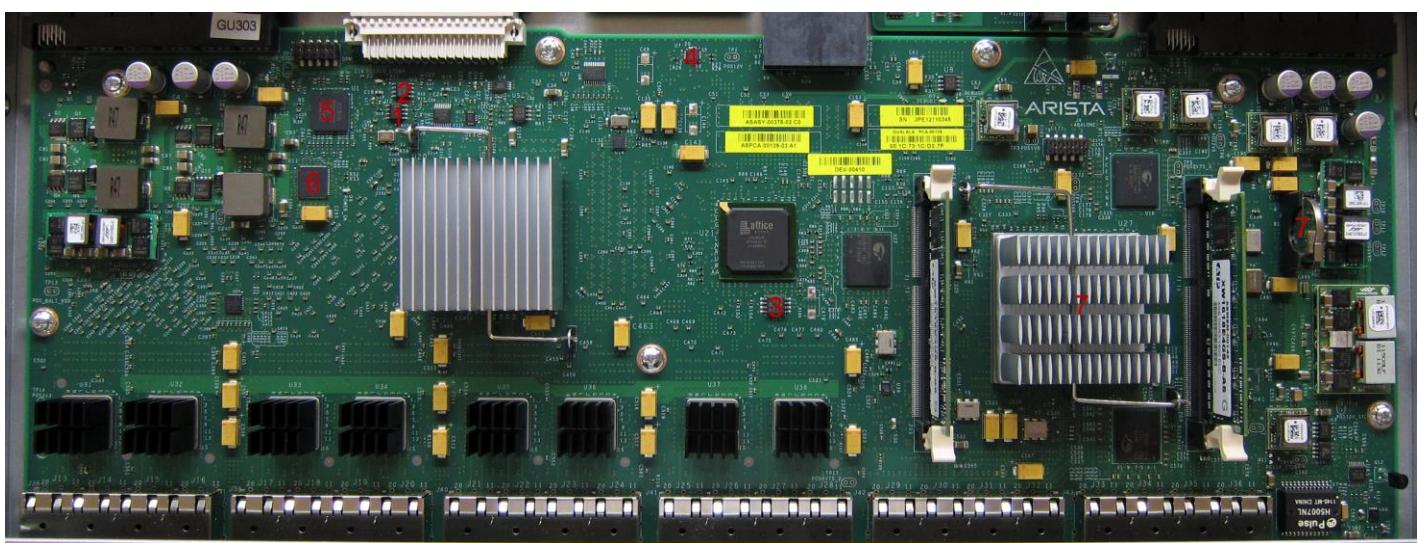


Figure 21-DCS-7124FX Non-volatile Memory Locations

Table 23-DCS-7124FX Volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	SRAM	User data	No	Yes	72Mb
2	SRAM	User data	No	Yes	72Mb
3	SRAM	User data	No	Yes	72Mb
4	DRAM	User data	Yes	Yes	4GB
5	DRAM	User data	Yes	Yes	4GB
6	FPGA	User data	No	Yes	50 Mb

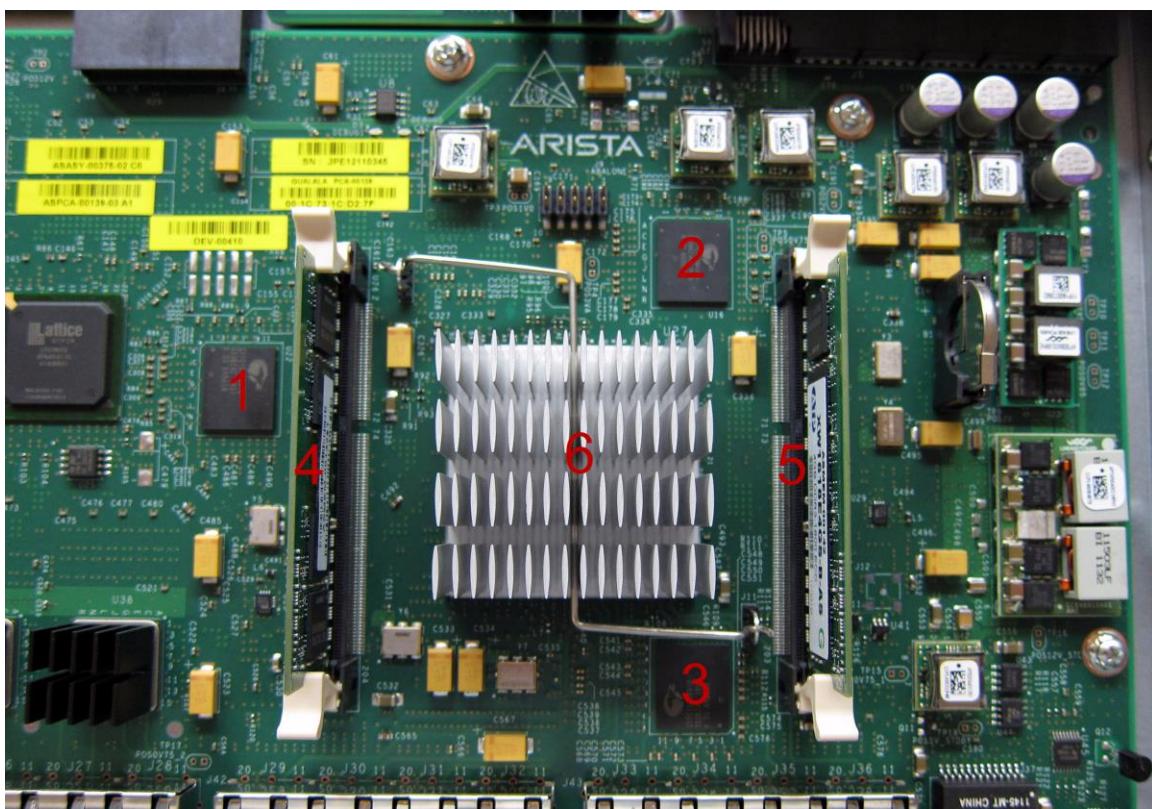


Figure 22-DCS-7124FX Volatile Memory Locations

### 3.7. DCS-7050Q-64 Memory Map

Table 24-DCS-7050Q-64 Non-volatile Memory Map

Location	Type	Application	Removable	User data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	FLASH	FPGA configuration	No	No	8MB
4	FLASH	FPGA configuration	No	No	8MB
5	Microcontroller	Security	No	No	134KB
6	FLASH	CPLD	No	No	8KB
7	FLASH	Power controller	No	No	NA
Front Panel	USB	User data	Yes	Yes	Various

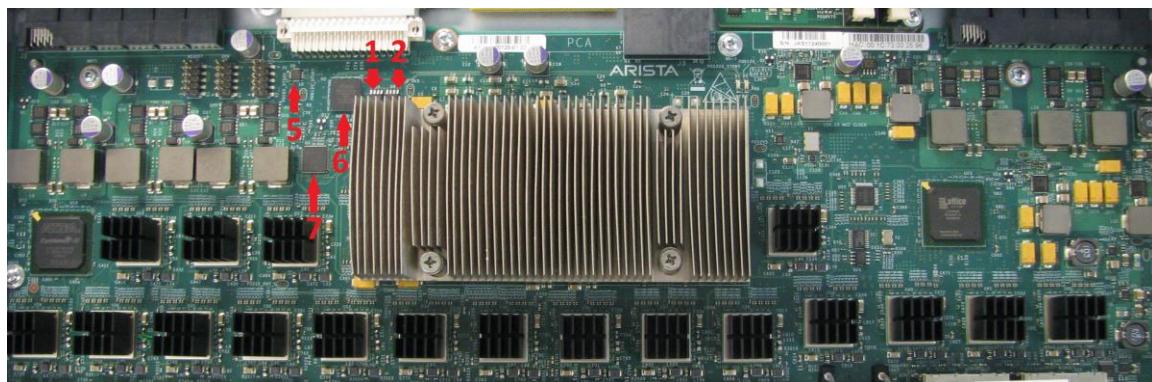


Figure 23-DCS-7050Q-64 Non-volatile Memory Locations (Front)

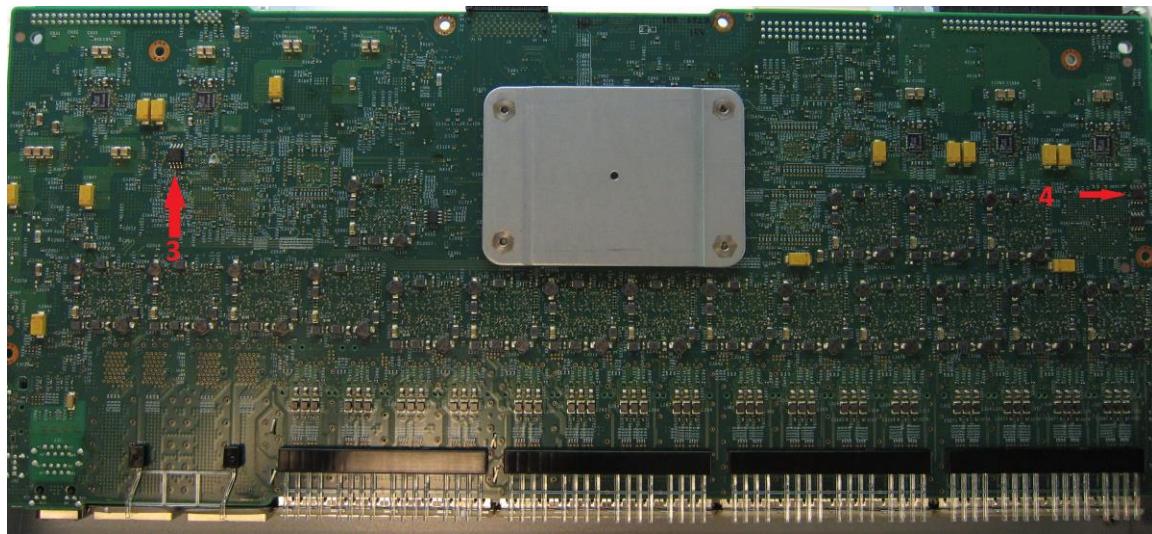


Figure 24- DCS-7050Q-64 Non-volatile Memory Locations (Back)

### 3.8. DCS-7050QX-32 and DCS-7050QX-32S Memory Map

Table 25-DCS-7050QX-32 Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA configuration	No	No	8MB
5	Flash	CPLD	No	No	8KB
6	Flash	CPLD	No	No	8KB
7	Flash	QSFP manufacturer data	Yes	No	2KB
8	Flash	Power Controller	No	No	NA
Front Panel	USB	User data	Yes	Yes	Various

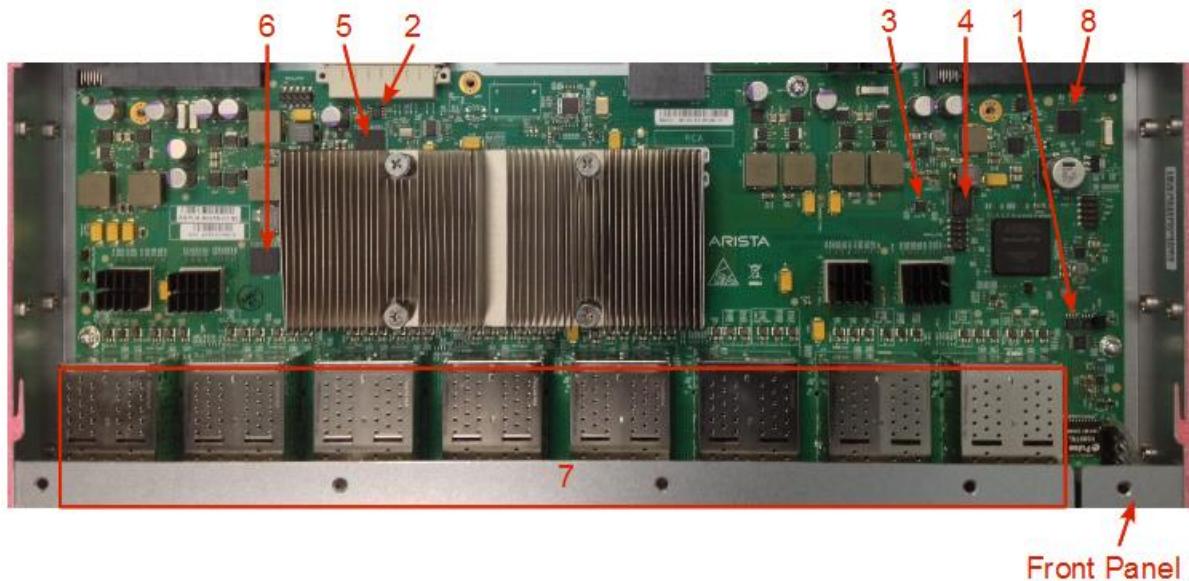


Figure 25-DCS-7050QX-32 Non-volatile Memory Locations

Table 26-DCS-7050QX-32S Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	512KB
2	EEPROM	Manufacturer data	No	No	16KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA configuration	No	No	8MB
5	Flash	CPLD	No	No	8KB
6	Flash	SFP/QSFP manufacturer data	Yes	No	2KB
7	Flash	Power Controller	No	No	NA
Front Panel	USB	User data	Yes	Yes	Various

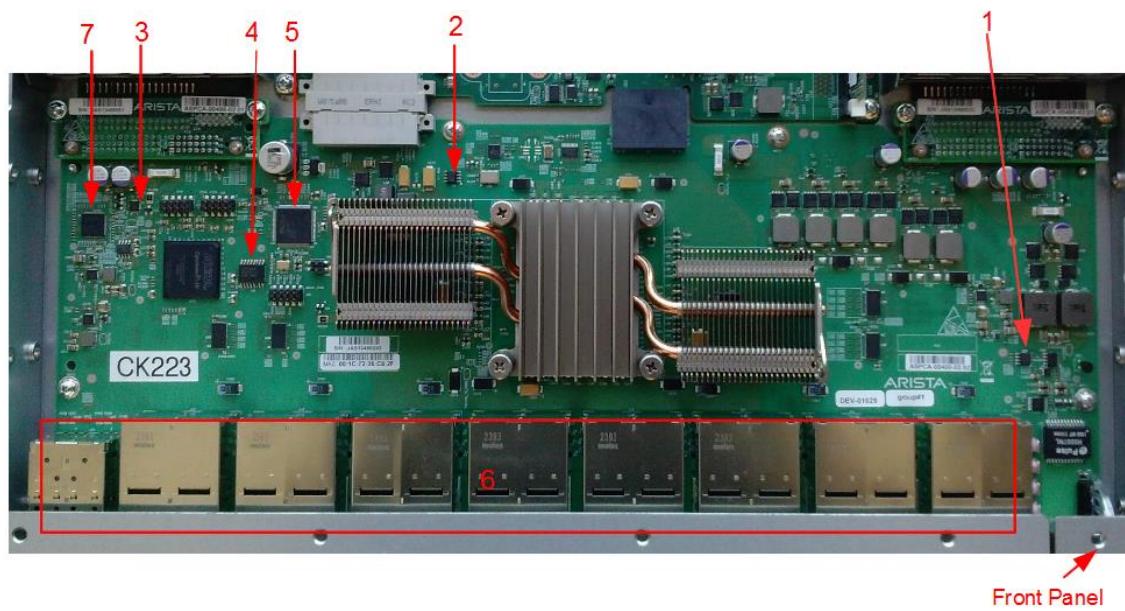


Figure 26-DCS-7050QX-32 Non-volatile Memory Locations

### 3.9. DCS-7150S-XX Memory Map

The Arista 7150S-24, 52 and 64 platforms utilize the same CPU board as the 7124SX platform. Please reference the 7124SX CPU board diagrams for memory location information relating to these platforms. *Note: If the optional SSD FLASH disk is ordered, that contains user data and must be removed.*

The non-volatile components are located on both the component side and back side for this family as shown.

Table 27: DCS-7150S-24, 52, 64 Non-Volatile Memory Location Map

Location	Type	Application	Removable	User Data
1	Flash	PC Board manufacturer data (for CPU)	No	No
2	CPLD Flash internal	Standby power logic configuration	No	No
3	Microcontroller	Security	No	No
4	Flash	FPGA external flash 1	No	No
5*	Flash	SFP Module manufacturer data	Yes	No
6*	Flash	QSFP+ Module manufacturer data	Yes	No
7	USB	Front panel, possible user inserted USB memory stick (optional)	Yes	Yes
8*	CPLD Flash internal	CPLD internal configuration data	No	No
9	Flash Internal	Power Controller	No	No
10	Flash	FPGA external flash 2	No	No
11	Flash internal	DC_DC controller configuration	No	No

5\* There are 24 SFP+ for DCS-7150S-24, 52 for DCS-7150S-52, 48 for DCS-7150S-64

6\* only DCS-7150S-64 has 4 QSFP+

8\* This CPLD is not populated in DCS-7150S-24

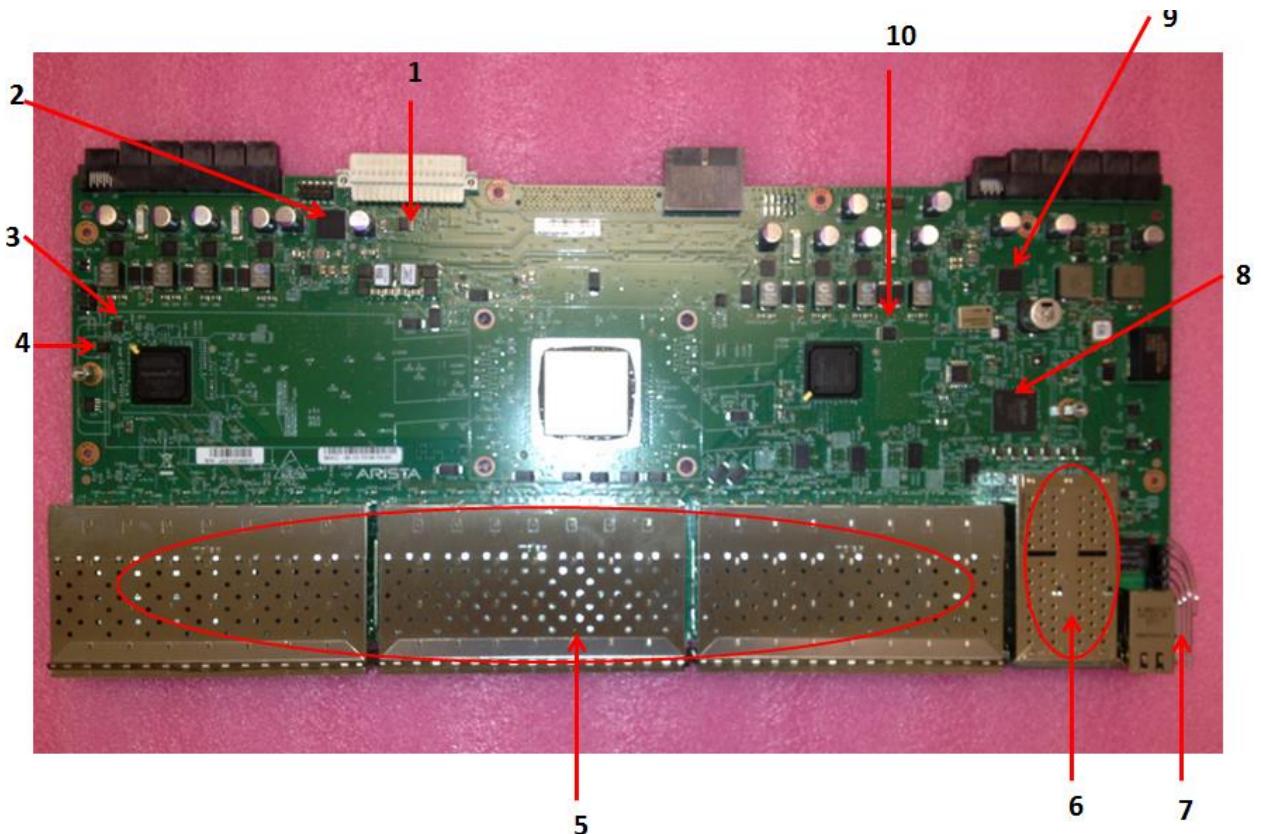


Figure 27-DGS-7150S-64 Component Side

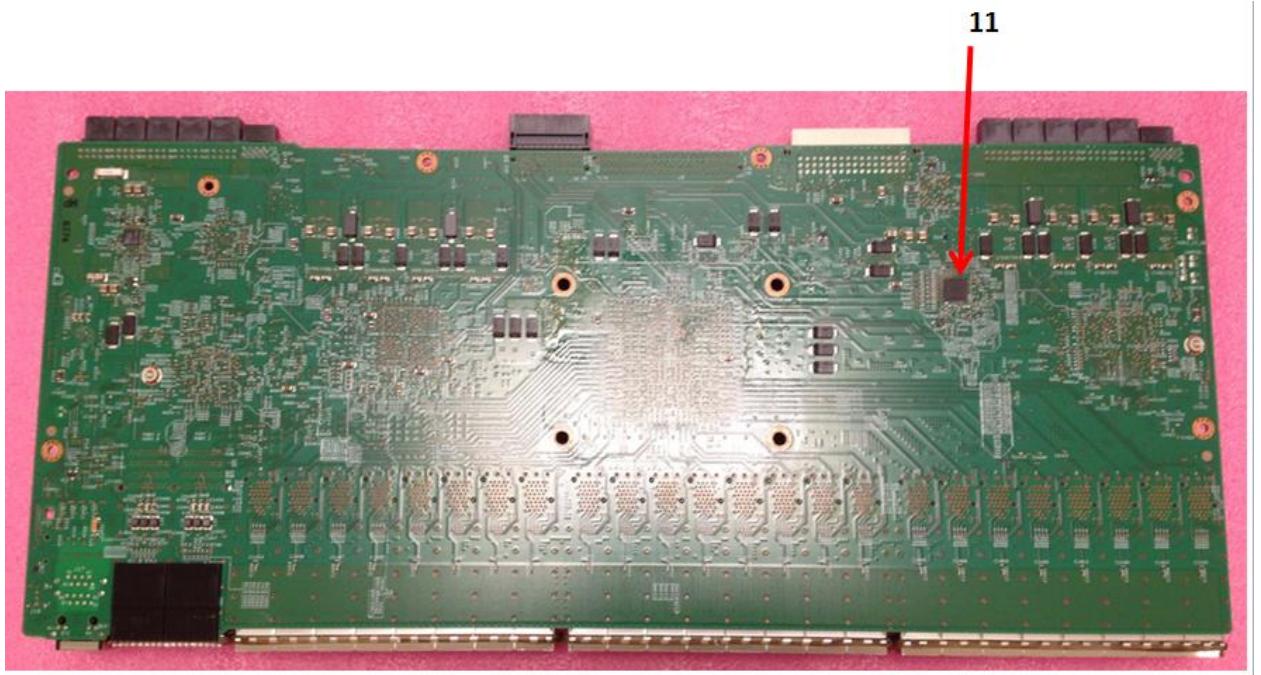


Figure 28- DCS-7150S-64 Back Side

### 3.10. DCS-7050SX-96/72 Memory Map

Table 28: DCS-7050SX-96/72 Non-Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	16Kb
2	EEPROM	Manufacturer data	No	No	512Kb
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA configuration	No	No	8MB
5	Flash	CPLD	No	No	8Kb
6	Flash	Power controller	No	No	NA
7	Flash	SFP module manufacturer data (see section 0)	Yes	No	2Kb
8 (bottom side of AOM)	EEPROM	Manufacturer data	No (but AOM is removable)	No	512Kb
9 (bottom side of AOM)	EEPROM	Manufacturer data <b>Not present on 7050SX-72</b>	No (but AOM is removable)	No	512Kb
Front Panel	USB	User data	Yes	Yes	Various

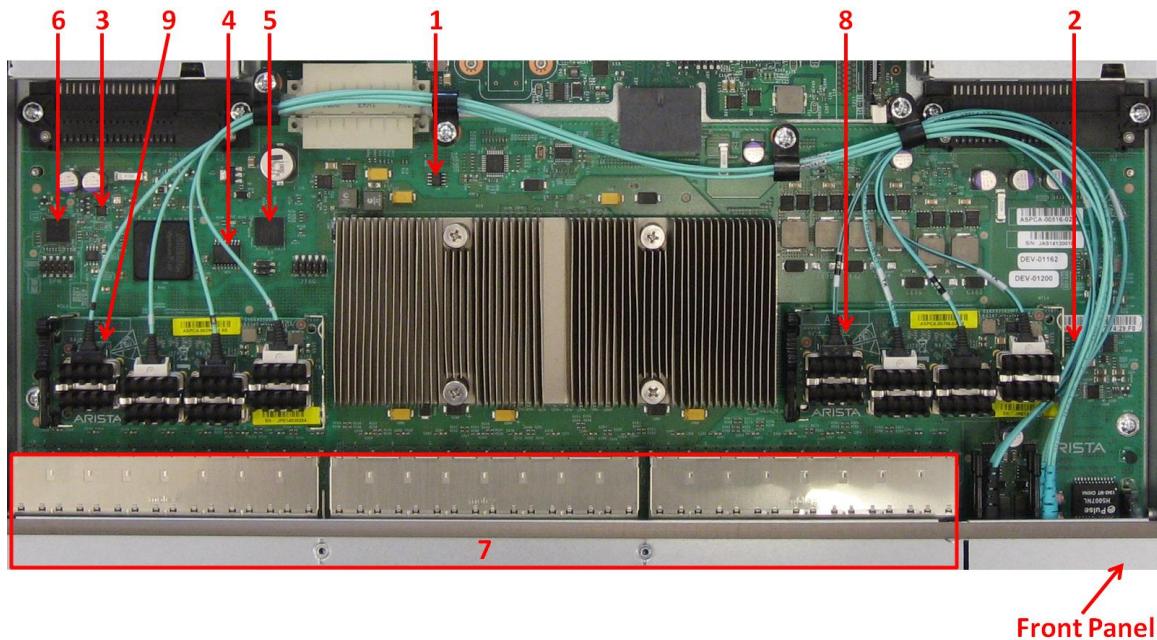


Figure 29: DCS-7050SX-96/72 Non-Volatile Memory Locations

### 3.11. DCS-7050SX-64 Memory Map

Table 29: DCS-7050SX-64 Non-Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	16Kb
2	EEPROM	Manufacturer data	No	No	512Kb
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA configuration	No	No	8MB
5	Flash	CPLD	No	No	8Kb
6	Flash	Power controller	No	No	NA
7	Flash	SFP module manufacturer data (see section 0)	Yes	No	2Kb
Front Panel	USB	User data	Yes	Yes	Various

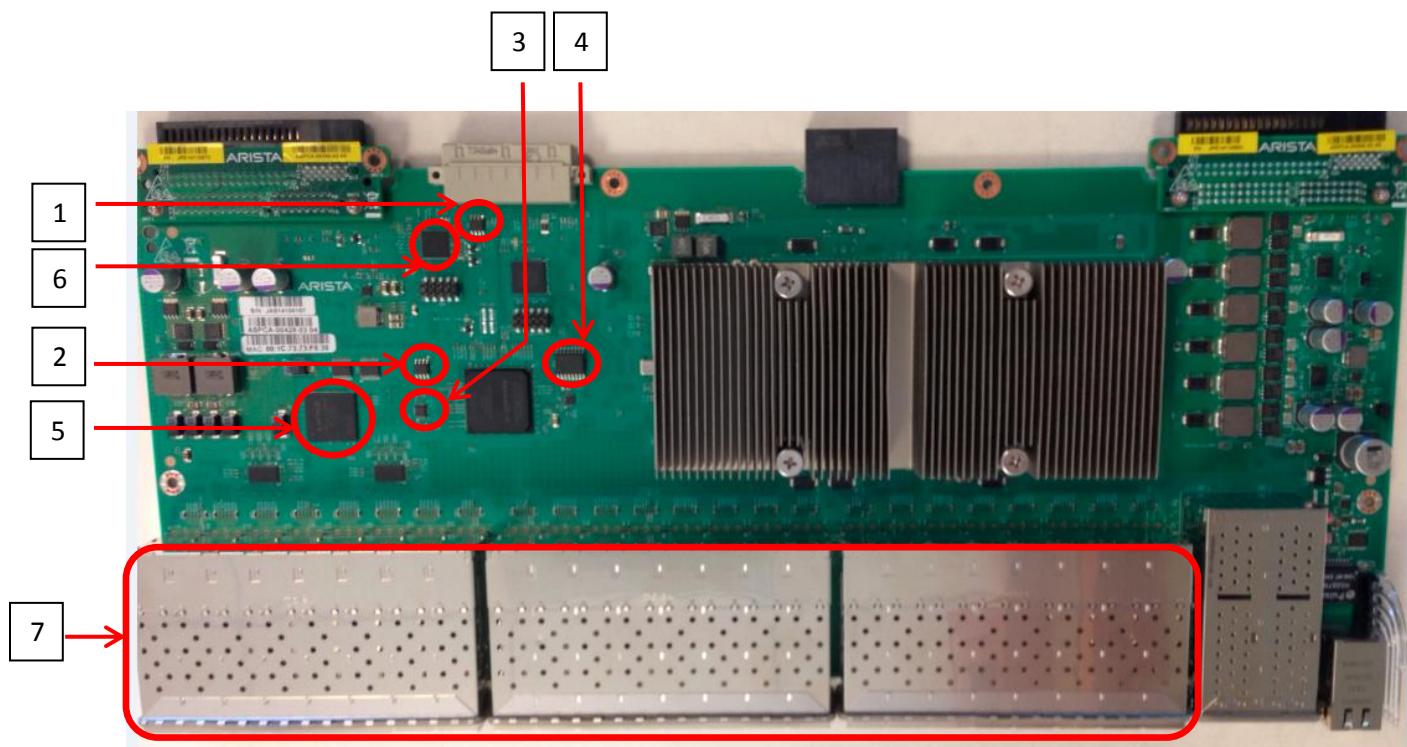


Figure 30: DCS-7050SX-64 Non-Volatile Memory Locations

### 3.12. Non-Volatile Memory Removal Procedure

In order to protect user data it may be necessary to remove the Flash RAM installed in the Arista switches. As with all electronic devices, Electrostatic Discharge (ESD) standards must be adhered to in order to prevent damage to sensitive electronic components. *Any damage to Arista owned equipment during the Flash removal procedures due to non-compliance with ESD standards will be the responsibility of the customer.* A comprehensive list of ESD standards can be found here: <http://www.esda.org/>

- 1) Unplug the switch and follow guidance regarding grounding (ESD) procedures
- 2) Remove screws on both left and right hand sides of the chassis. Reference photo below:



Figure 31-Arista 1RU Switch Screw Removal Diagram

- 3) Slide chassis cover rearwards quickly to release it
- 4) Lift the chassis cover off and locate the Flash
- 5) Use a Phillips screwdriver to loosen the plastic retaining screw on the Flash RAM
- 6) Remove the Flash by gently pulling straight up off of the board
- 7) Use a Phillips screwdriver to less screws (s1–s3) label on Figure 32
- 8) Remove the SSD by gently pulling straight up off of the board
- 9) If there are SFP or QSFP pluggable modules in the front connectors, remove them
- 10) Dispose of the Flash in accordance with local policy
- 11) Replace the chassis cover and retaining screws
- 12) Notify your Arista account team that you have performed sanitization procedures on the equipment

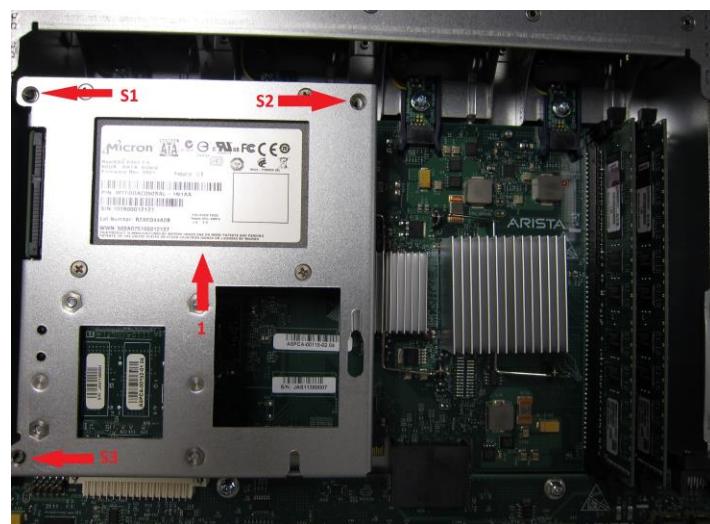


Figure 32-CPU Board SSD Screw Location

## 4. Arista Jenner Family Product Memory

All Jenner family products are designed with two separate boards, the CPU board and switch board. The CPU card is called “Crow” which is the same as used in other products. Each switch board is a separate entity that plugs into the CPU board.

### 4.1. Crow CPU Card PCA-00459-xx

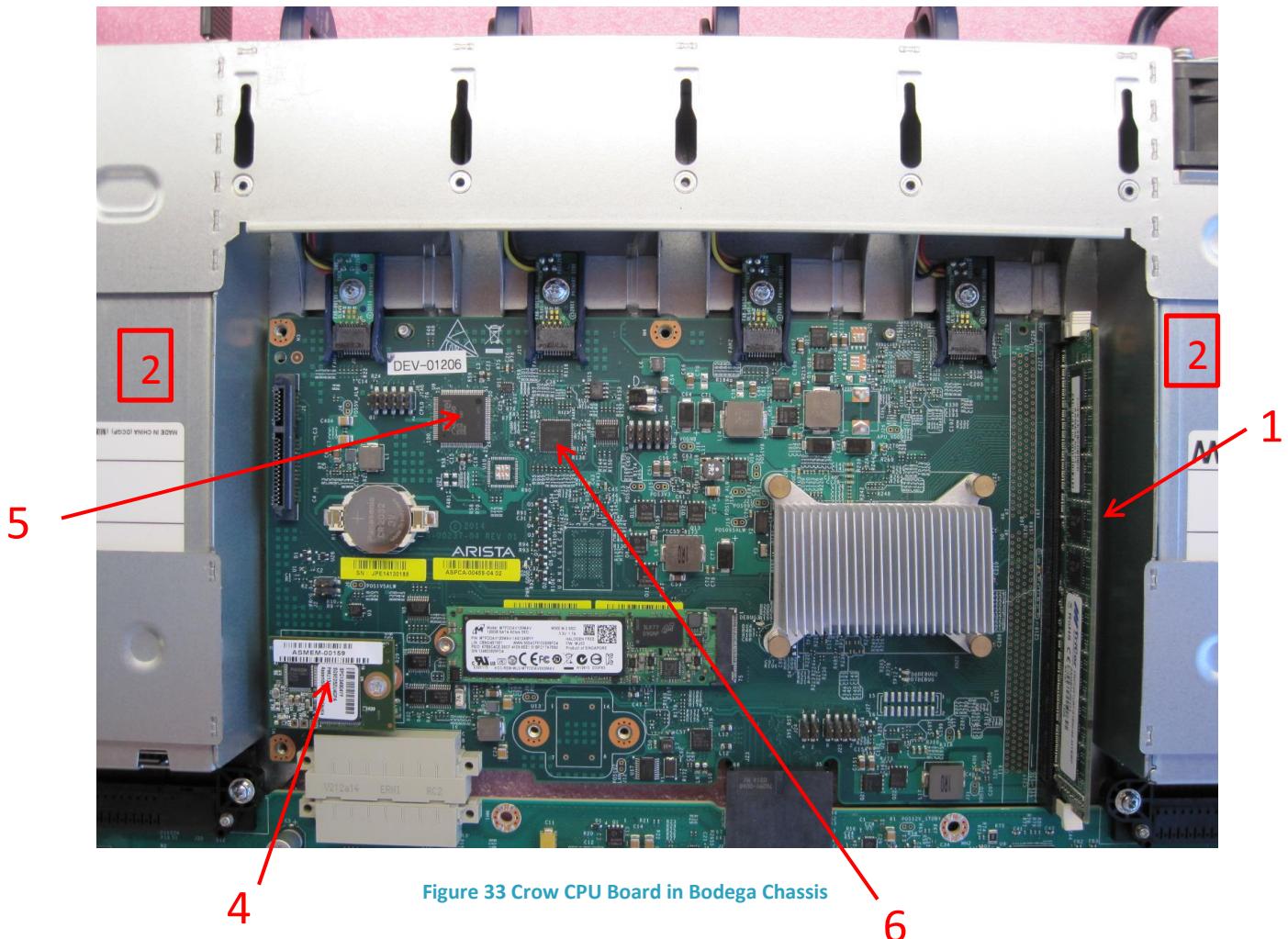


Table 30 Crow Volatile Memory

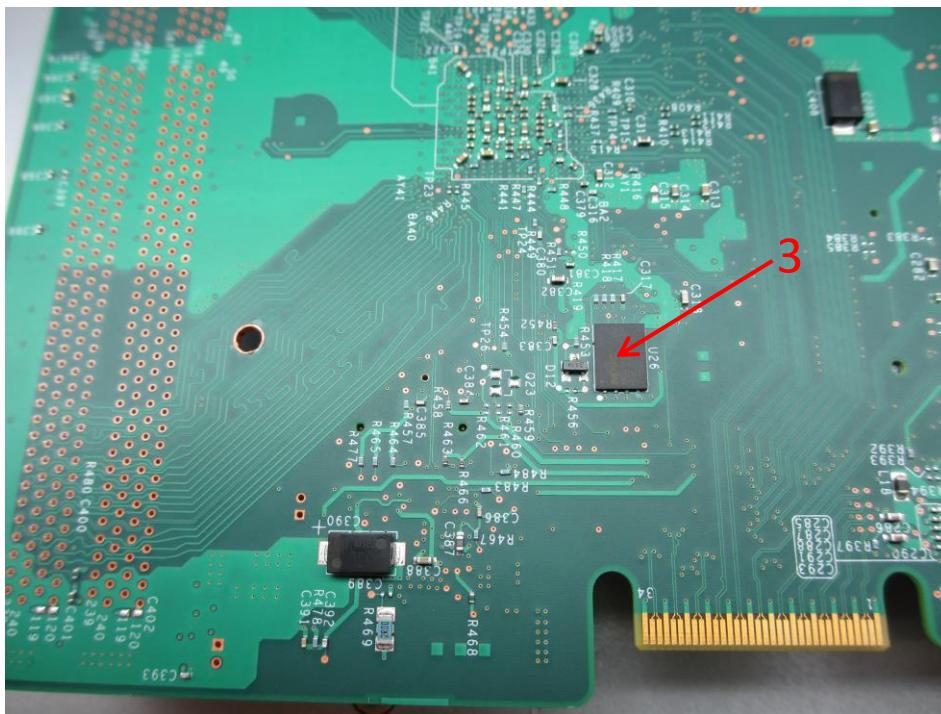
Location	Type	Application	Removable	User Data	Size
1	DRAM – DDR3	CPU Memory	Yes	Yes	4GB

Note: The removable DIMM also has a non-volatile “SPD” SEPROM to identify itself, pre-written with Manufacturing data.

Table 31 Crow CPU Board Non-Volatile Memories

Location	Type	Application	Removable	User Data	Size
2 (inside both power supplies)	FLASH	Manufacturer data	No, but the supply is removable	No	2KB
3 (secondary side)	Flash	Boot Memory	No	No	64Mbit
4	eUSB FLASH	Software/User config	Yes	Yes	4GB
5	CPLD internal configuration FLASH	UART and fan controller CPLD	No	No	Unknown
6	Flash	Power Controller UCD90120A	No	No	Unknown

Table 32 Crow CPU, Secondary Side



#### 4.1.1. Crow M.2 SSD [Optional]

The Crow CPU has a location for an optional M.2 SSD FLASH shown below, retained by one screw. Crow also supports the 2.5" SSD described previously for Raven but it is not planned to be offered for sale.

Table 33 Crow CPU M.2 SSD Option Location

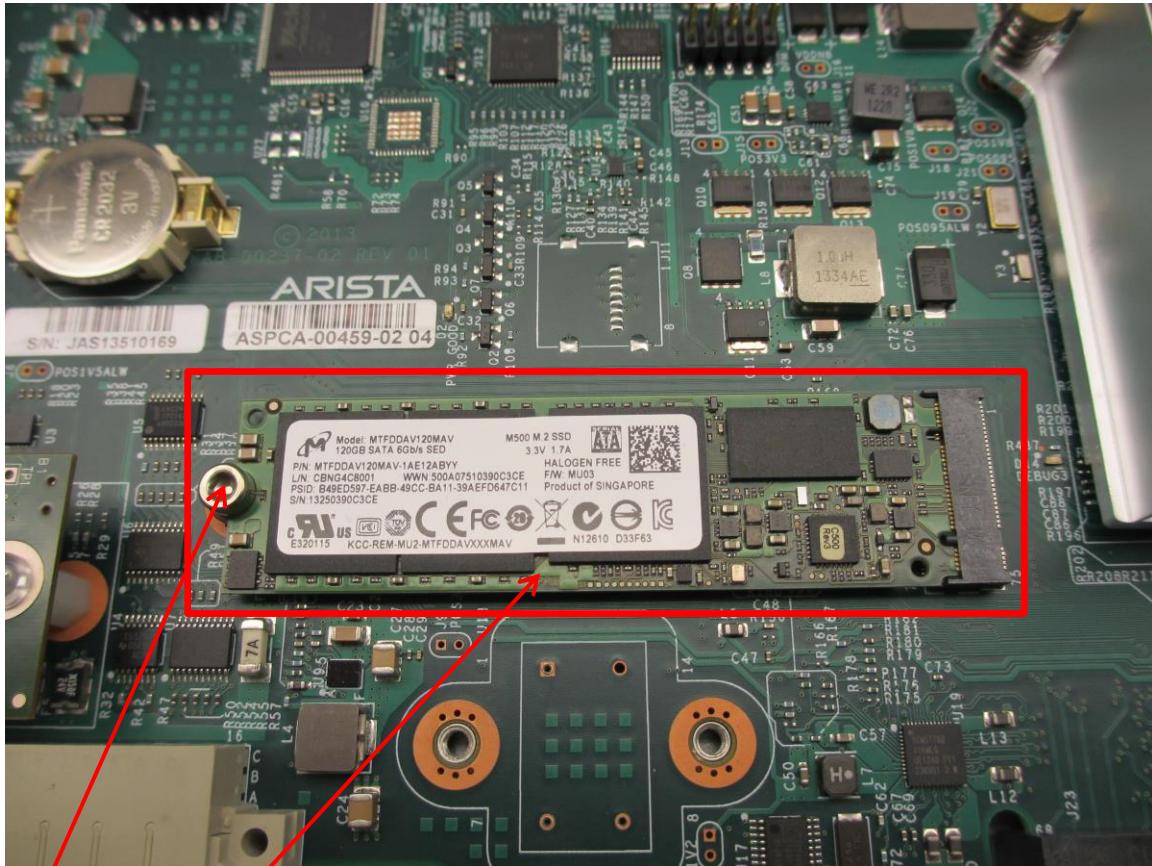


Table 34 Crow M.2 SSD Option

Location	Type	Application	Removable	User data	Size
1	SSD FLASH	User data	Yes	Yes	120GB

Note: Other storage capacities could be offered from time to time. The SSD shown is sized 2280 (80mm); Crow also supports 42mm length option boards in the same location but none are planned for sale.

## 4.2. DCS-7280SE-64 Memory Map

Table 35 - DCS-7280SE-64 Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Manufacturer data	No	No	512Kb
2	EEPROM	Manufacturer data	No	No	16Kb
3	Microcontroller (one of two locations will be stuffed)	Security	No	No	134KB
4	Flash	FPGA external flash	No	No	16MB
5	CPLD	CPLD	No	No	NA
6	Flash, 48 pieces total	SFP+ EEPROM	Yes	No	256B Each
7	Flash, 4 pieces total	QSFP+ EEPROM	Yes	No	640B Each
8	Flash	Power Controller	No	No	NA

Table 36 - DCS-7280SE-64 Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
9	DDR3 (top & bottom of board), 48 pieces total	Look up tables	No	No	256MB Each

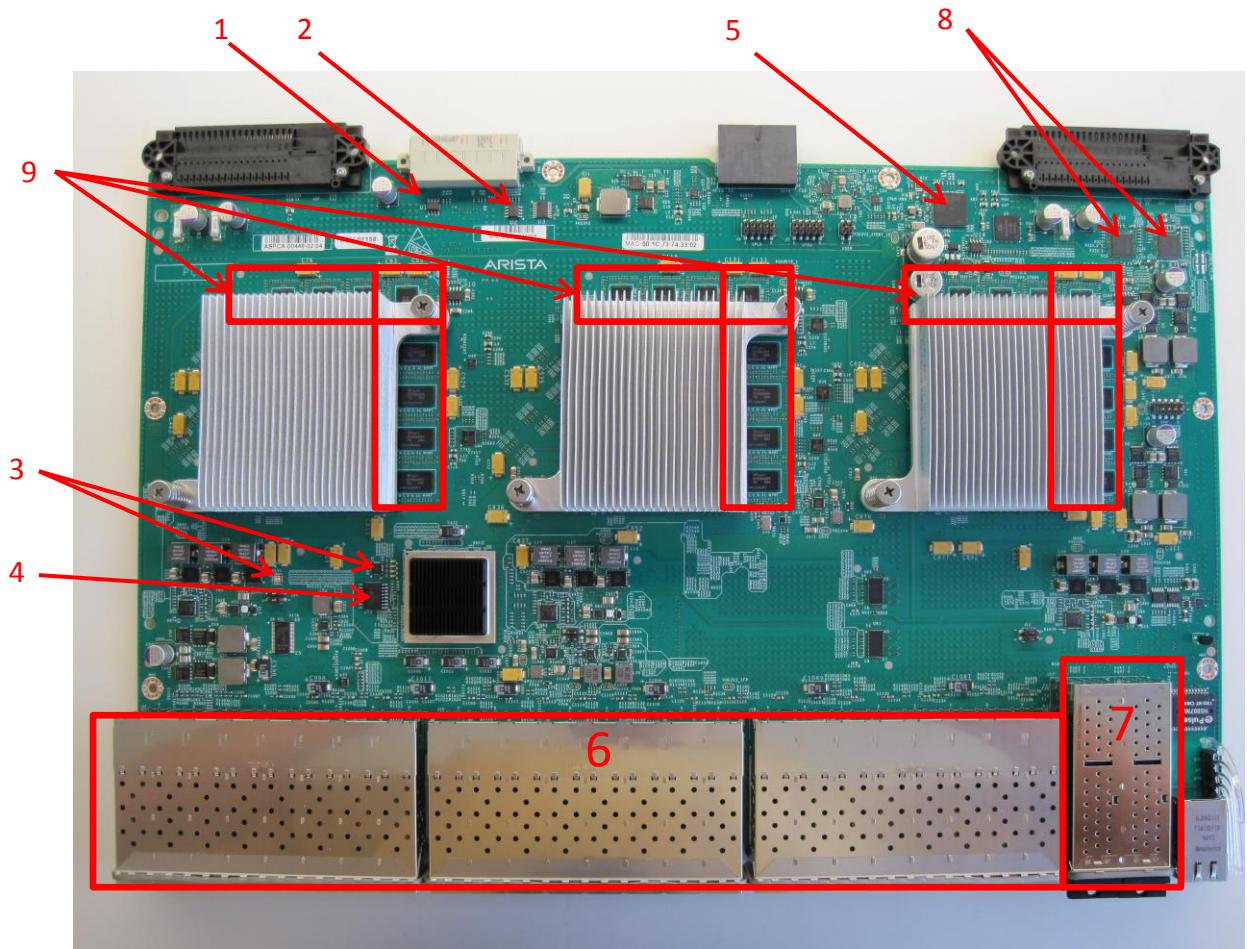


Figure 34-DCS-7280SE-64 Non-volatile Memory Locations

### 4.3. DCS-7050TX-96/72 Memory Map

Table 37 - DCS-7050TX-96-X, DCS-7050TX-72-X Non-Volatile Memory Map

Location	Type	Ref	Application	Removable	User data	Size
1	Flash	U8	Power configuration	No	No	NA
2	Microcontroller (one of two locations stuffed)	U7 or U6	Security	No	No	130kB
3	SPI flash	U14	PHY firmware	No	No	64Mbit
4	SPI flash	U12	FPGA configuration	No	No	64Mbit
5	Flash	U9	CPLD	No	No	8kbit
6	EEPROM	U5	Manufacturer data	No	No	16kbit
7	EEPROM	U29	Manufacturer data	No	No	512kbit
8, 9	Optical module EEPROM		Manufacturer data	Yes	No	512kbit
8, 9	Optical module		Configuration	Yes	No	NA
Front Panel	USB		User data	Yes	Yes	Various

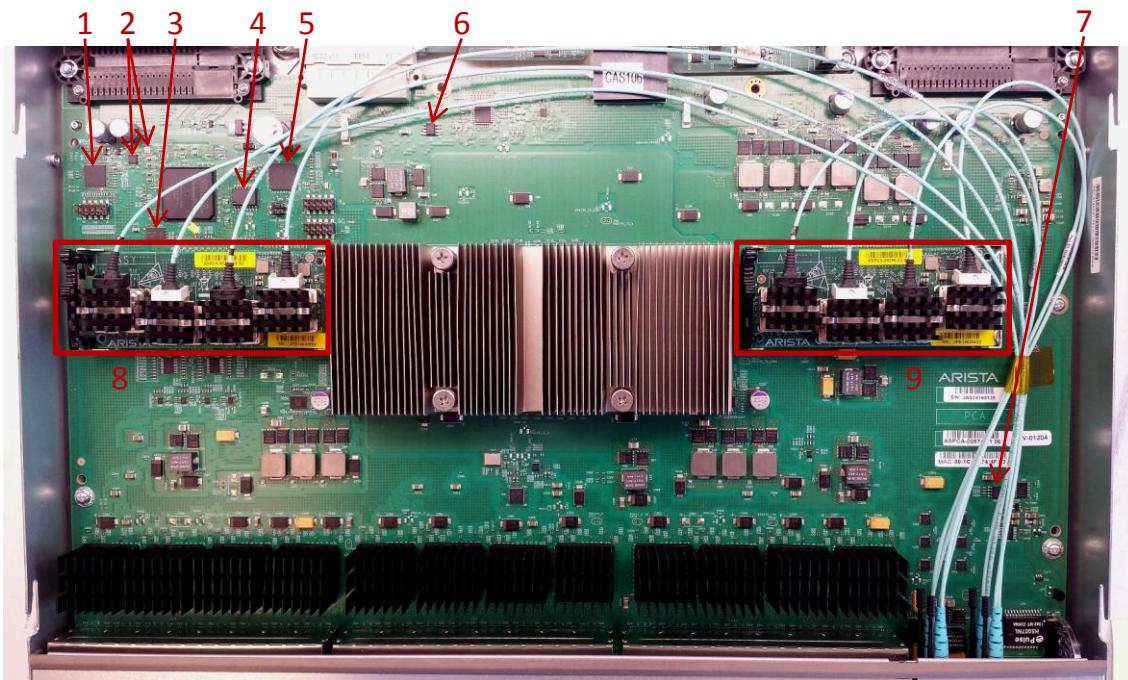


Figure 35 - DCS-7050TX-96-X, DCS-7050TX-72-X Non-Volatile Memory Locations

## 5. Arista Mendocino Switch Memory

DCS-7010T-48 (Mendocino) is a fixed configuration 1RU switch. The major components are the switch card, fan module and (internal) power supplies. Only the switch card contains memory.

Table 38-DCS-7010T-48 Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	SDRAM	CPU Memory	No	Yes	4GB
2	ASIC (U25)	internal memory	No	Yes	unknown
3	PHYs (7)	internal memory	No	Yes	unknown
4	FPGA	Internal memory	No	Yes	5Mb
5	CPU	Instruction & data cache	No	Yes	1MB

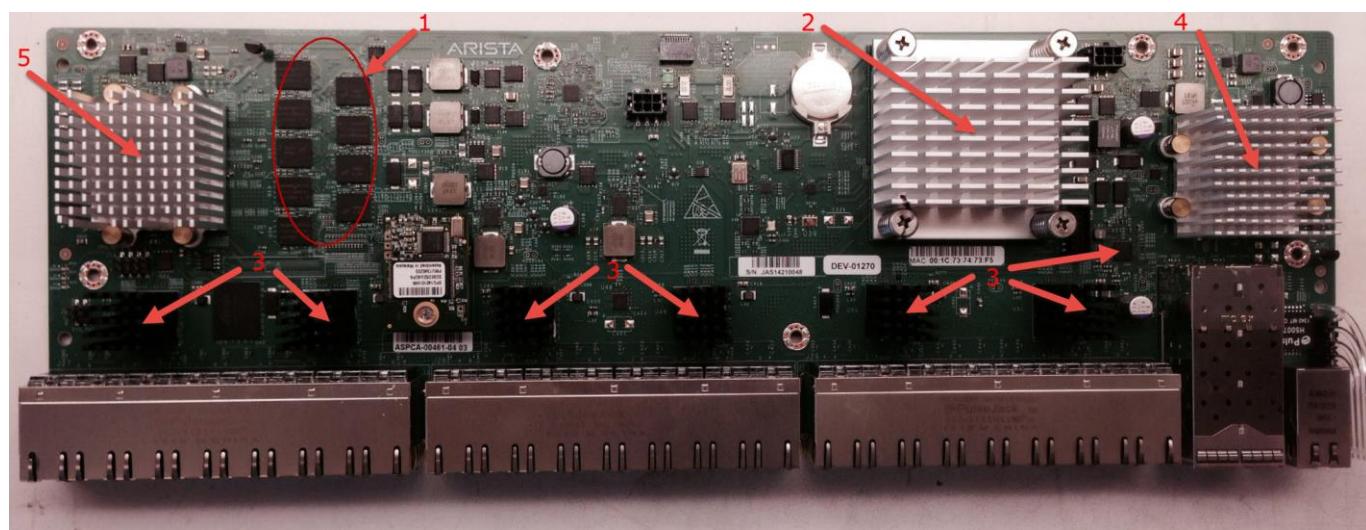


Figure 36- DCS-7010T-48 Volatile Memory Locations

Table 39-DCS-7010T-48 Non-Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
6	Flash (U33)	Manufacturer data	No	No	16Kb
7	Security (U31)	security	No	No	134KB
8	Security (U55)	security	No	No	134KB
9	Flash (U55)	Software / user config	No	Yes	4GB
10	Flash (J19)	Software / user config	Yes	Yes	4GB
11	Flash (U32)	Software	No	No	64Mb
12	Flash (U11)	Manufacturer data	No	No	16Kb
13	Flash (U30)	FPGA configuration	No	No	64Mb
Front Panel	USB	User data	Yes	Yes	various
Front Panel	Optics	Manufacturer data	Yes	No	various

Note: Either item 7 or 8 is used, not both; and either item 9 or 10 is used, not both.



Figure 37- DCS-7010T-48 Non-Volatile Memory Locations – Top

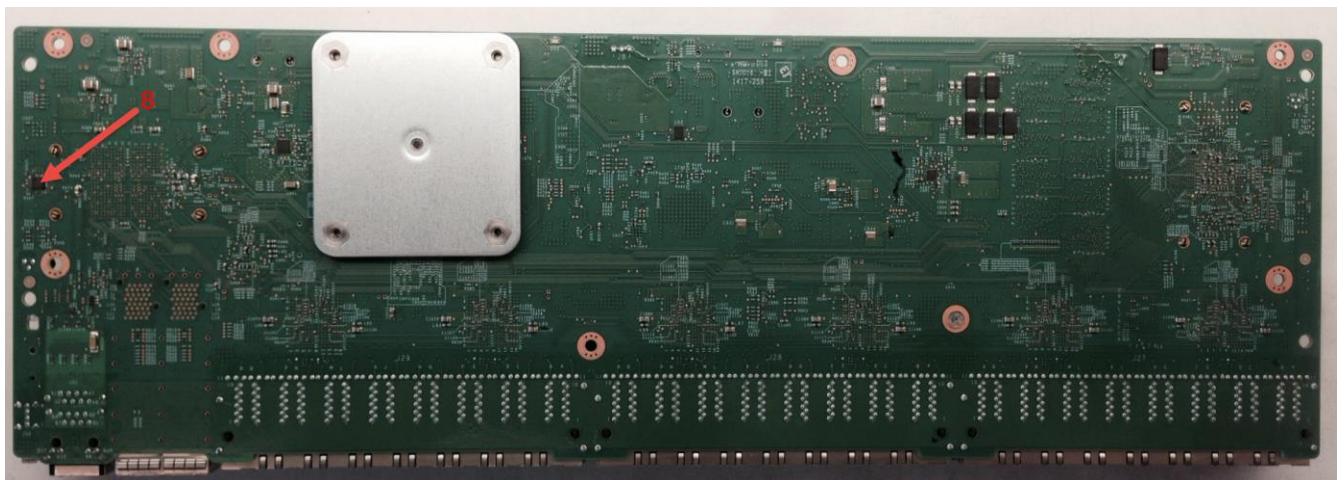


Figure 38- DCS-7010T-48 Non-Volatile Memory Locations - Bottom

## 6. Arista Modular Switches Memory

The Arista modular switches are built on a modular chassis comprised of several components containing memory modules. These major components are the Supervisor Module, Fan Module, 7504 and 7508 Fabric Module and linecards. Each of these components and their respective non-volatile/volatile memory are identified below.

### 6.6. DCS-7500 Supervisor Module

Table 40-DCS-7500 Supervisor Module Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	SDRAM	CPU Memory	Yes	Yes	4GB

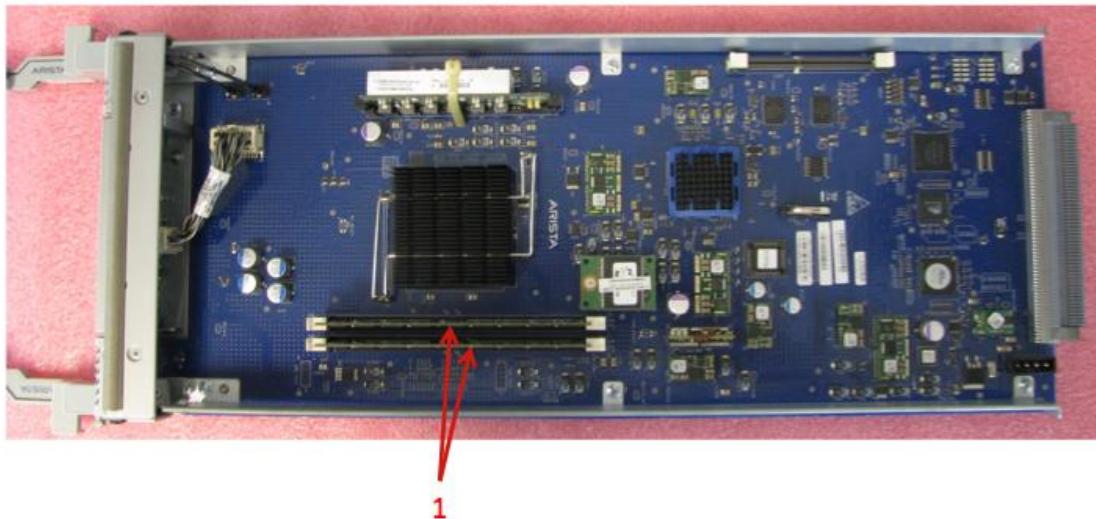


Figure 39- DCS-7500 Supervisor Module Volatile Memory Location

Table 41- Supervisor Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	FPGA Configuration	No	No	1MB
2	Flash	Manufacturer data	No	No	64KB
3	Flash	PC Board MAC Address	No	No	2KB
4	Real Time Clock	Stores time of day and date	Yes	No	
5	Flash	External flash memory	Yes	No	2MB
6	Flash	User and system config	Yes	Yes	2G
7	Flash	Power controller- Power configuration	No	No	
8	Microcontroller	Security	No	No	134KB
9	Flash	PCIE Switch external memory	No	No	

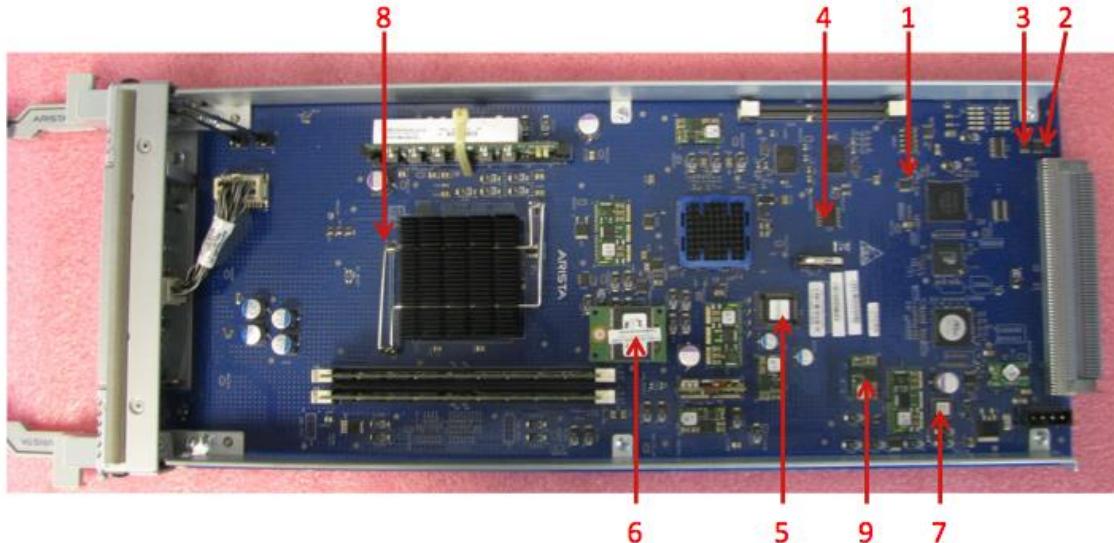


Figure 40- Supervisor Module Non-volatile Memory Location

## 6.7. DCS-7500E Supervisor Module

Table 42-DCS-7500E Supervisor Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1	DRAM-DDR3	CPU Memory	Yes	Yes	4Gb
2	DRAM-DDR3	CPU Memory	Yes	Yes	4Gb

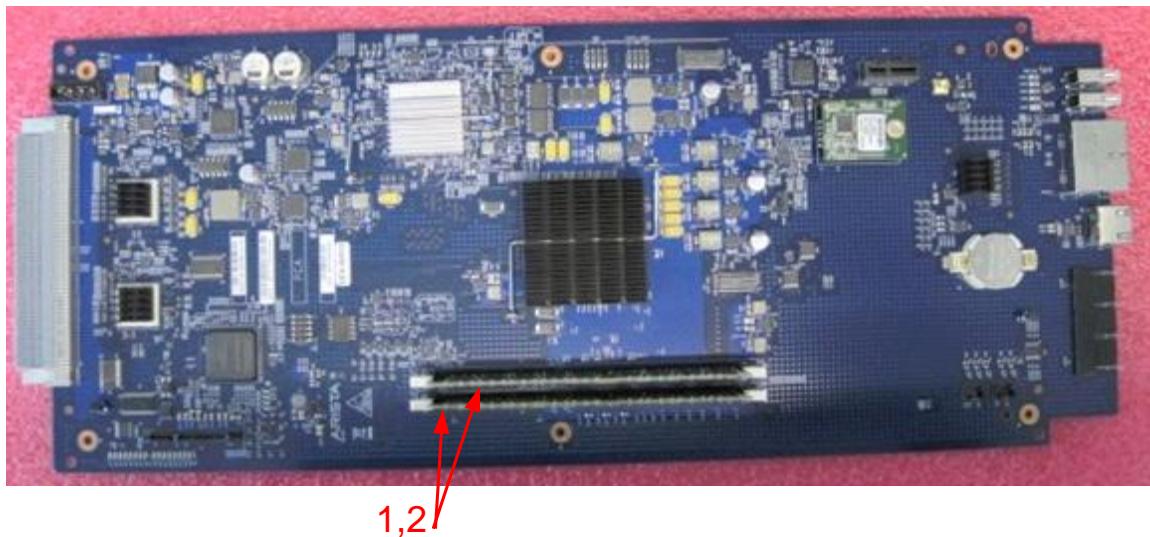


Figure 41-DCS-7500E Supervisor Volatile Memory Locations

Table 43- DCS-7500E Supervisor Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	FPGA Config flash	No	No	16MB
3	Microcontroller	Security	No	No	134KB
4	Flash	PCIE Switch Memory	No	No	64KB
5	Flash	PCIE Switch Memory	No	No	64KB
6	Flash	GBE Switch Flash	No	No	64MB
7	Flash	GBE Switch Flash	No	No	64MB
8	Flash	USB Flash	Yes	No	2GB
9	Flash	GBE Switch Flash	No	No	256KB
10	Flash	SSD	Yes	Yes	>100GB
Front Panel	Flash	USB memory stick	Yes	Yes	various

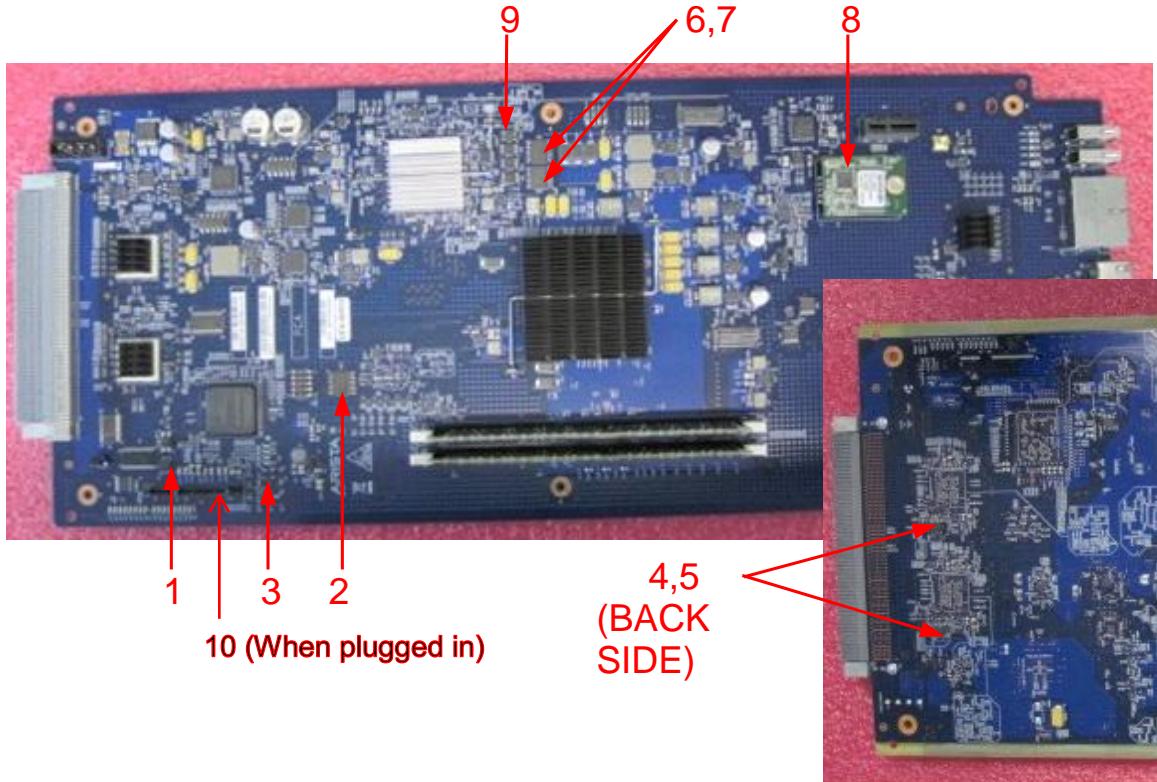


Figure 42- DCS-7500E Supervisor Linecard Non-volatile Memory Locations

## 6.8. DCS-7300 Supervisor Module

Table 44-DCS-7300E Supervisor Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1	DRAM-DDR3	CPU Memory	Yes	Yes	4Gb
2	DRAM-DDR3	CPU Memory	Yes	Yes	4Gb

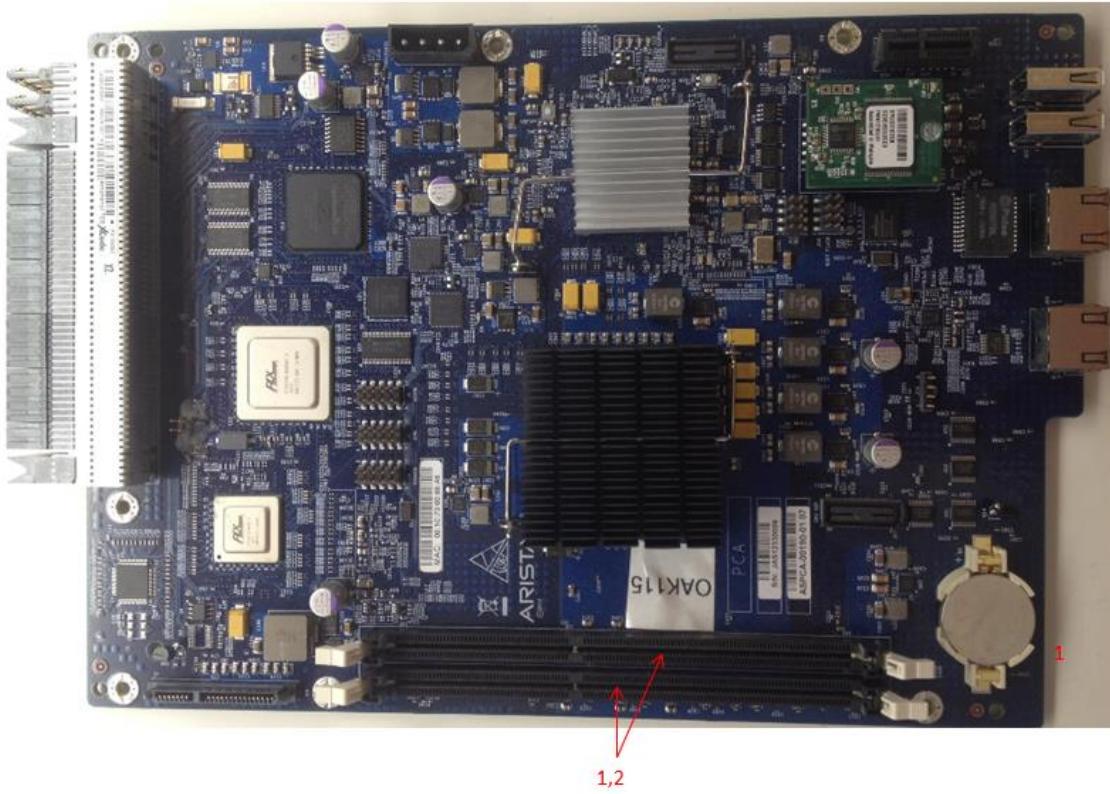


Figure 43-DCS-7300E Supervisor Volatile Memory Locations

Table 45- DCS-7300E Supervisor Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	FPGA Config flash	No	No	16MB
3	Flash	PCIE Switch Memory	No	No	64kb
4	Flash	PCIE Switch Memory	No	No	64kb
5	Flash	SPI Flash	No	No	64Mb
6	Flash	SPI Flash	No	No	64Mb
7	Flash	USB Flash	Yes	Yes	2GB
8	Flash	SPI Flash	No	No	256kb
9	Flash	SSD Drive	Yes	Yes	N/A
Front Panel	Flash	USB memory stick	Yes	Yes	various

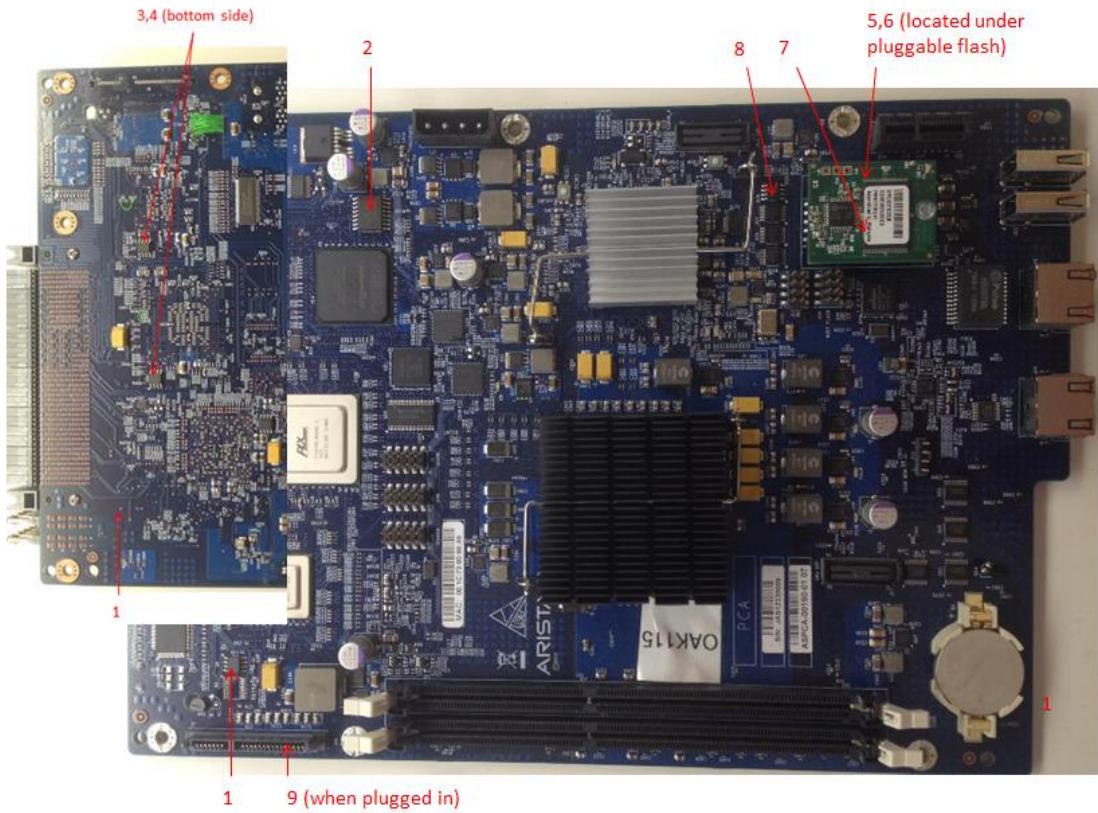


Figure 44- DCS-7300E Supervisor Linecard Non-volatile Memory Locations

## 6.9. DCS-7500 Fan Module

Table 46- DCS-7500 Fan Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	PC board manufacturer data	No	No	2KB

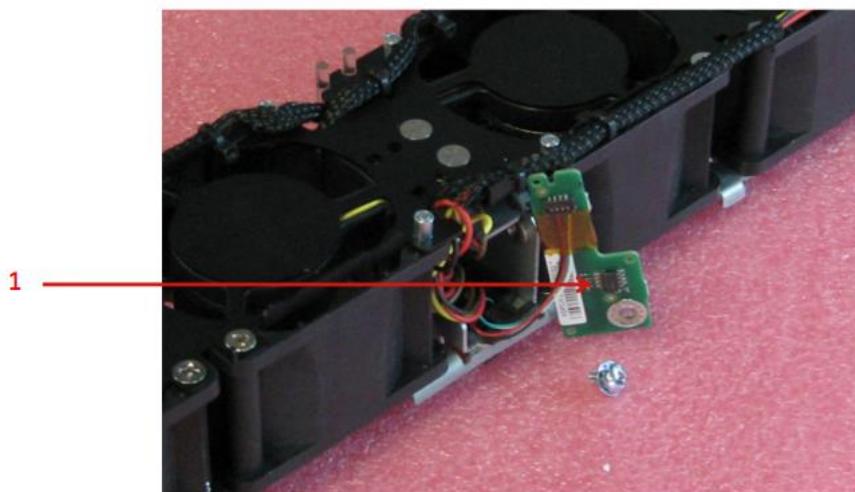


Figure 45- DCS-7500 Fan Module Non-volatile Memory Location

## 6.10. DCS-7504 Fabric Module

No volatile memory on this PCA.

Table 47-DCS-7504 Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	64KB
2	Microcontroller	Security	No	No	134KB
3	Flash	FPGA configuration	No	No	1MB
4	Flash	Power Controller	No	No	

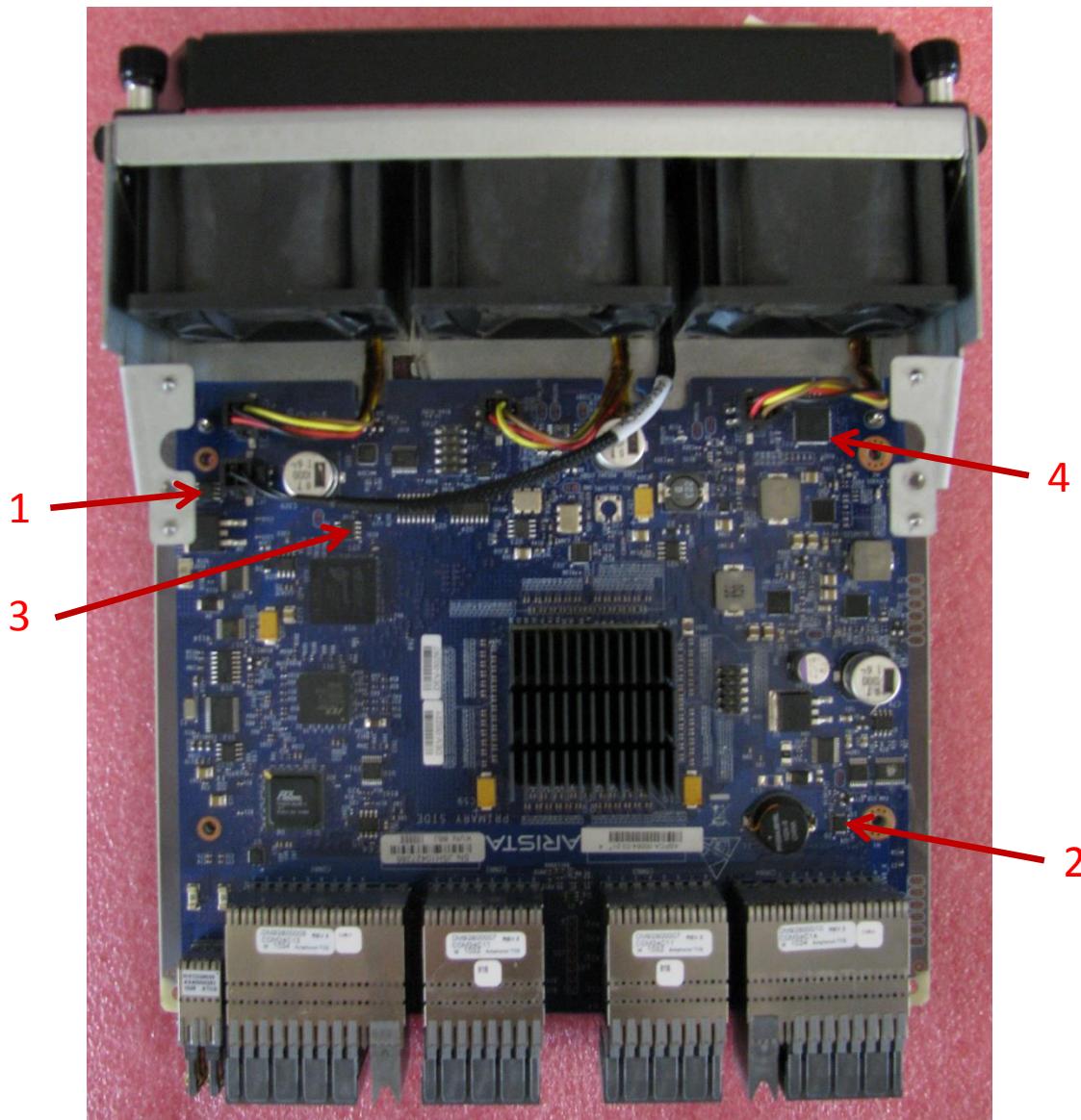


Figure 46-DCS-7504 Fabric Module Non-volatile Memory Location

## 6.11. DCS-7504E Fabric Module

No volatile memory on this PCA.

Table 48- DCS-7504E Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	PCIE Switch Memory	No	No	4MB
3	Flash	Switch External Flash	No	No	512KB
4	Microcontroller	Security	No	No	134KB

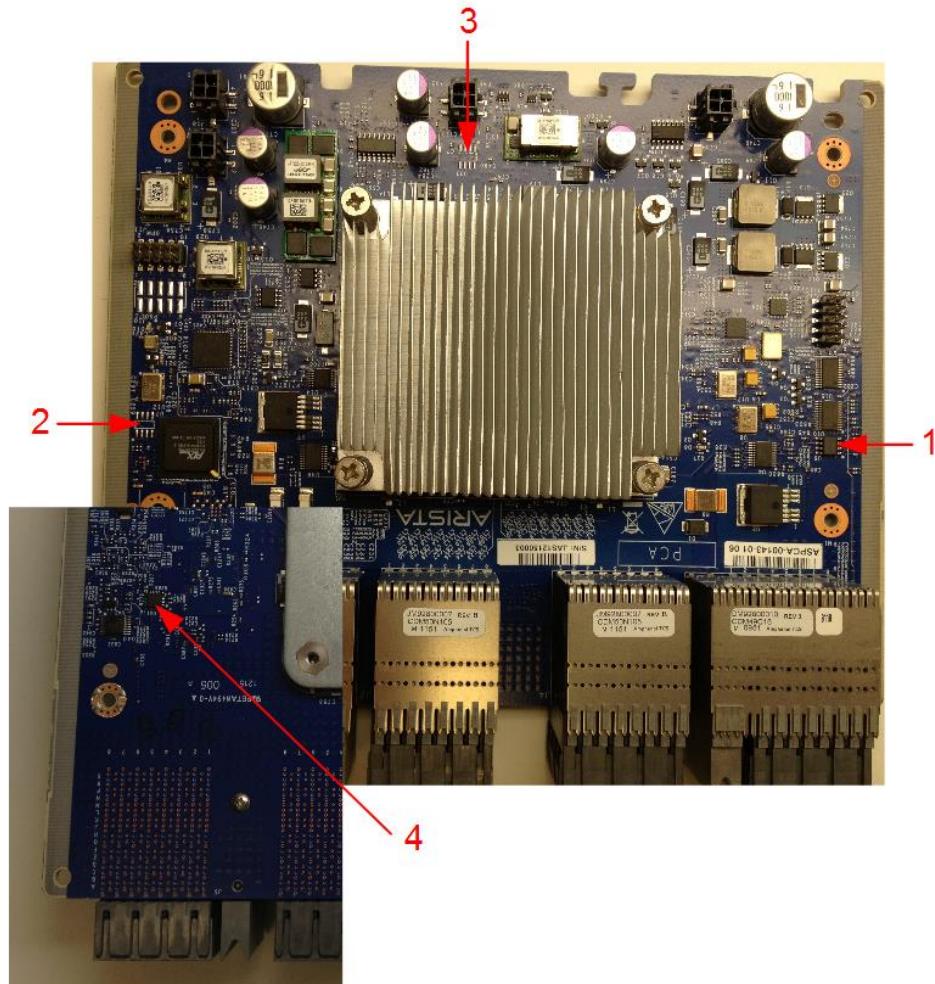


Figure 47- DCS-7504E Fabric Module Non-volatile Memory Map

## 6.12. DCS-7304 Fabric Module

No volatile memory on this PCA.

Table 49-DCS-7304 Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	PCIE Switch Memory	No	No	4MB
3	Flash	Switch External Flash	No	No	512KB

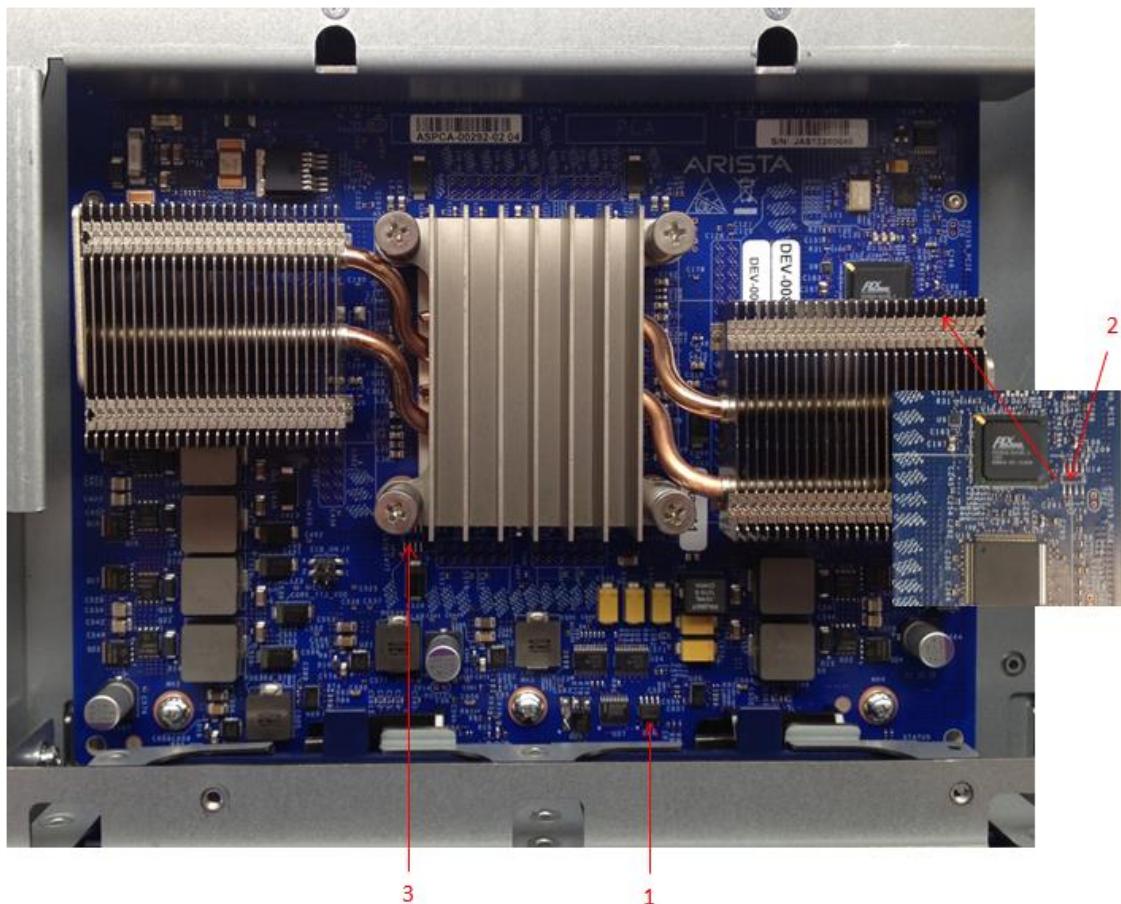


Figure 48-DCS-7304 Fabric Module Non-volatile Memory Location

## 6.13. DCS-7508 Fabric Module

No volatile memory on this PCA.

Table 50-DCS-7508 Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	64KB
2	Microcontroller	Security	No	No	134KB
3	Flash	PCIE Switch external flash	No	No	
4	Flash	FPGA configuration	No	No	1MB
5	Flash	Power Controller	No	No	

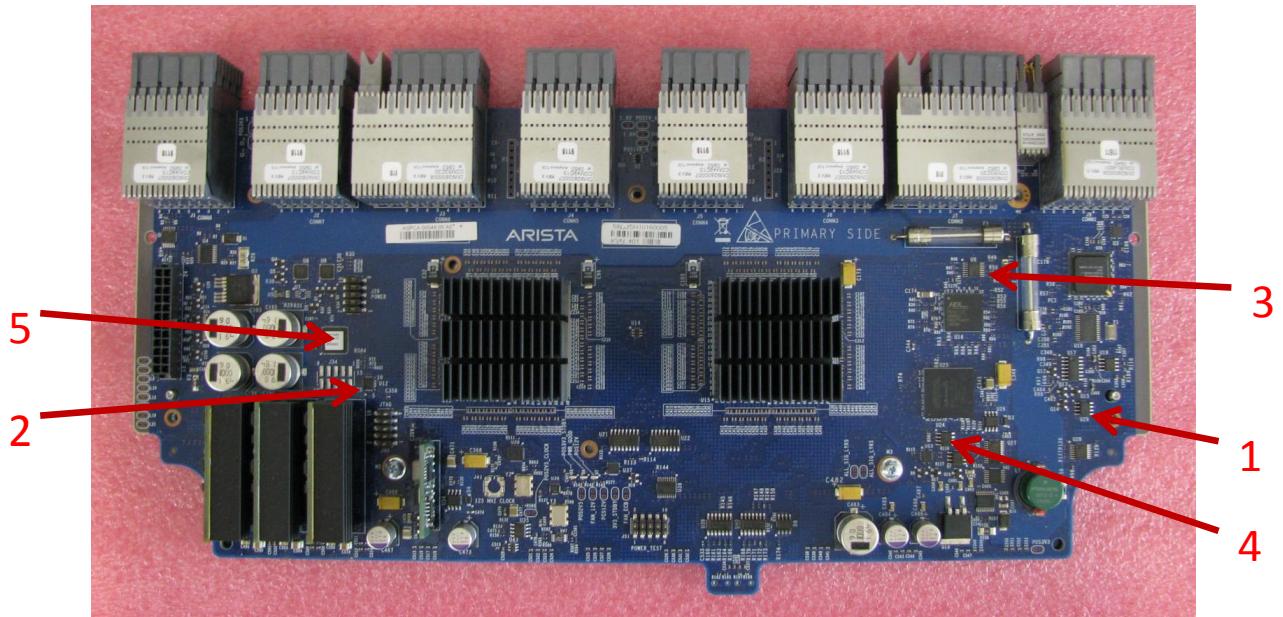


Figure 49-DCS-7508 Fabric Module Non-volatile Memory Location

## 6.14. DCS-7508E Fabric Module

No volatile memory on this PCA.

Table 51- DCS-7508E Fabric Module Non-Volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	PCIE Switch Memory	No	No	4MB
3	Flash	Switch External Flash	No	No	512KB
4	Flash	Switch External Flash	No	No	512KB
5	Microcontroller	Security	No	No	134KB

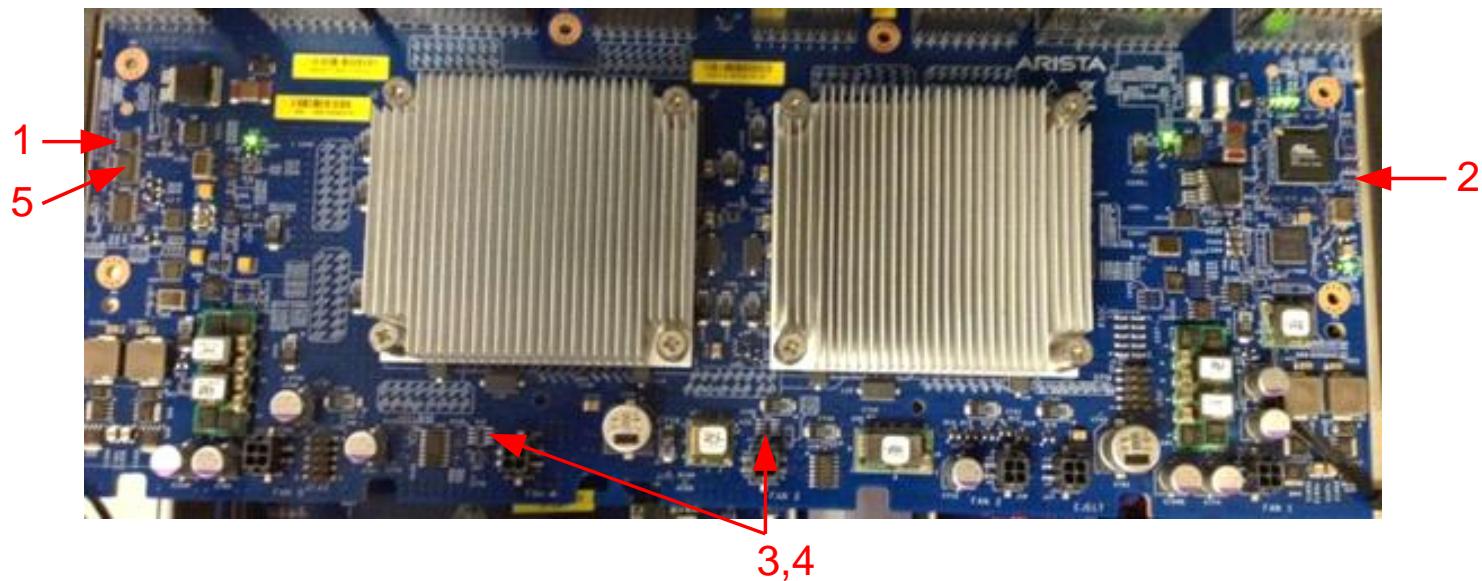


Figure 50- DCS-7508E Fabric Module Non-Volatile Memory Map

## 6.15. DCS-7308 Fabric Module

No volatile memory on this PCA.

Table 52-DCS-7308 Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	PCIE Switch Memory	No	No	4MB
3, 4	Flash	Switch External Flash	No	No	512KB

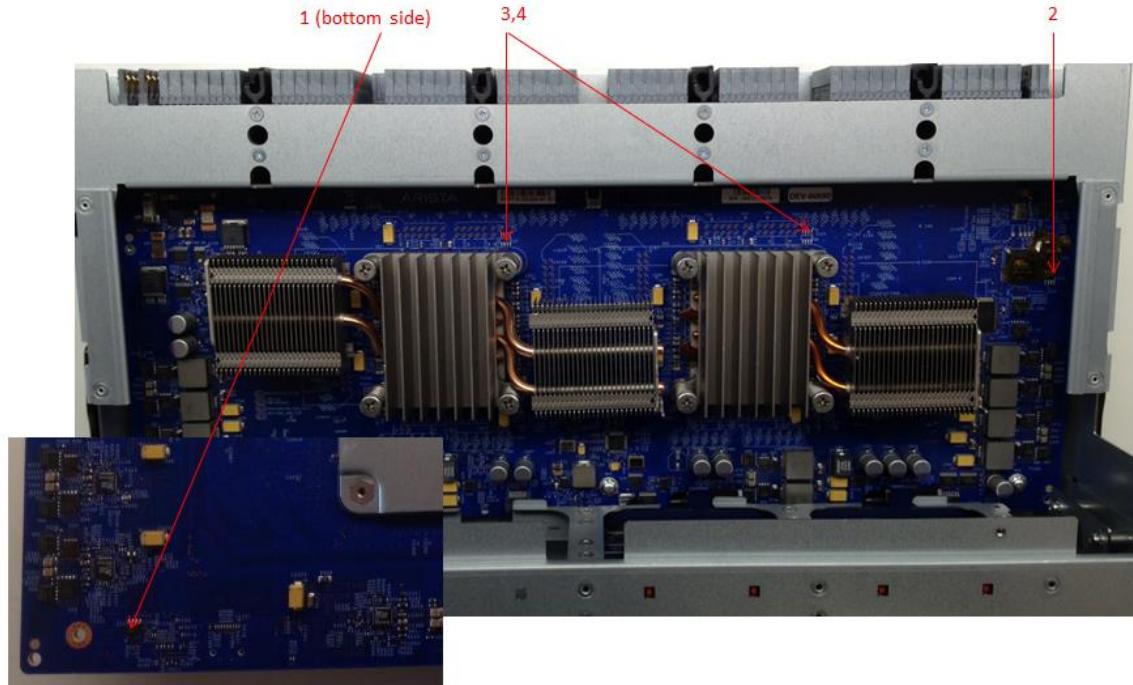


Figure 51-DGS-7308 Fabric Module Non-volatile Memory Location

## 6.16. DCS-7316 Fabric Module

No volatile memory on this PCA.

Table 53-DCS-7316 Fabric Module Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	PCIE Switch Memory	No	No	4MB
3,4,5,6	Flash	Switch External Flash	No	No	512KB

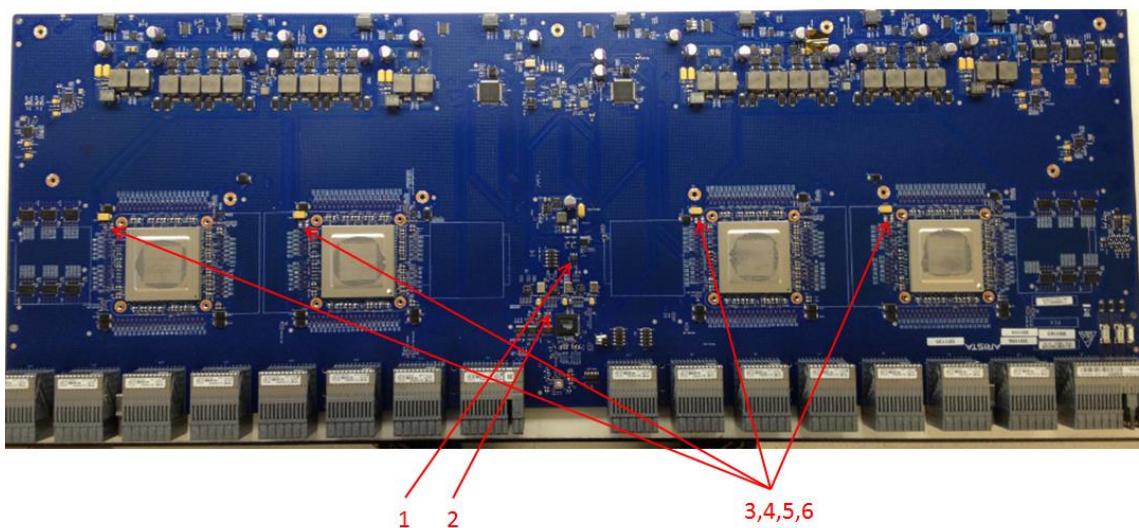


Figure 52-DCS-7316 Fabric Module Non-volatile Memory Location

## 6.17. DCS-7548S Linecard

Table 54-DCS-7548S Linecard Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1	DRAM-GDDR3	Temporary Packet Storage	No	Yes	512Mb
2	SRAM-QDR2	Look-up Tables	No	Yes	36Mb

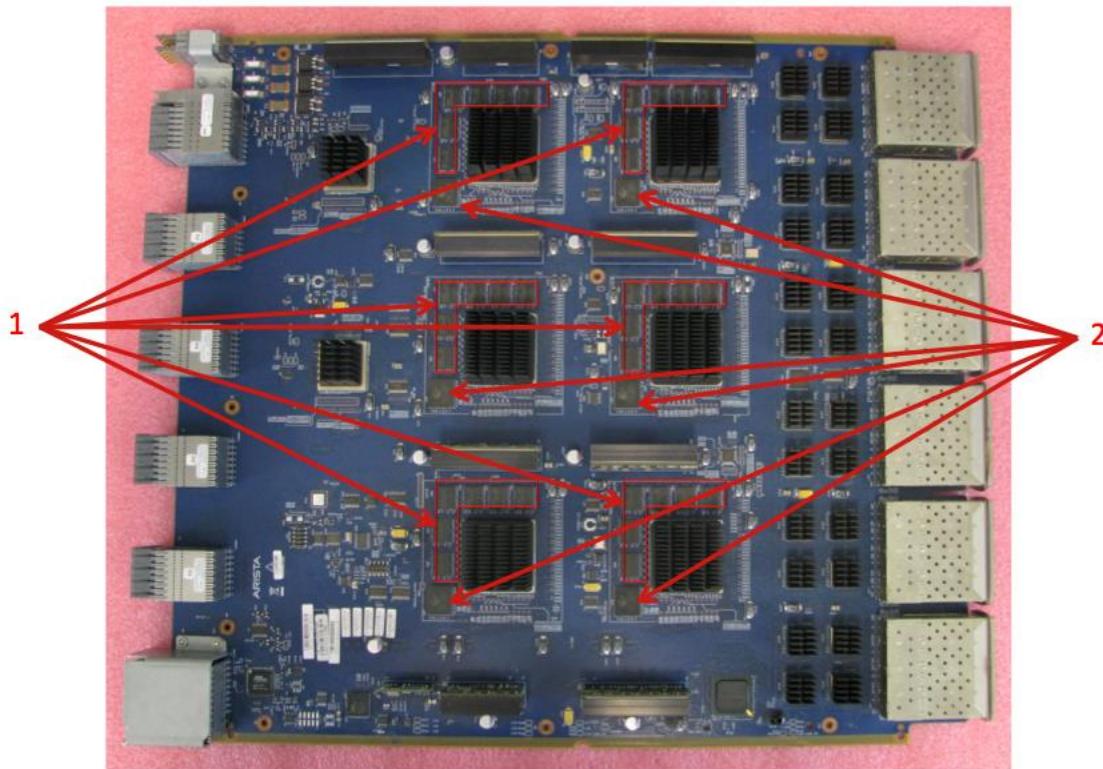


Figure 53-DCS-7548S Linecard Volatile Memory Location

Table 55-DCS-7548S Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Power configuration	No	No	
2	Flash	Manufacturer data	No	No	64KB
3	Microcontroller	Security	No	No	134KB
4	Flash	FPGA external memory	No	No	1MB
5	Flash	CPLD	No	No	8KB

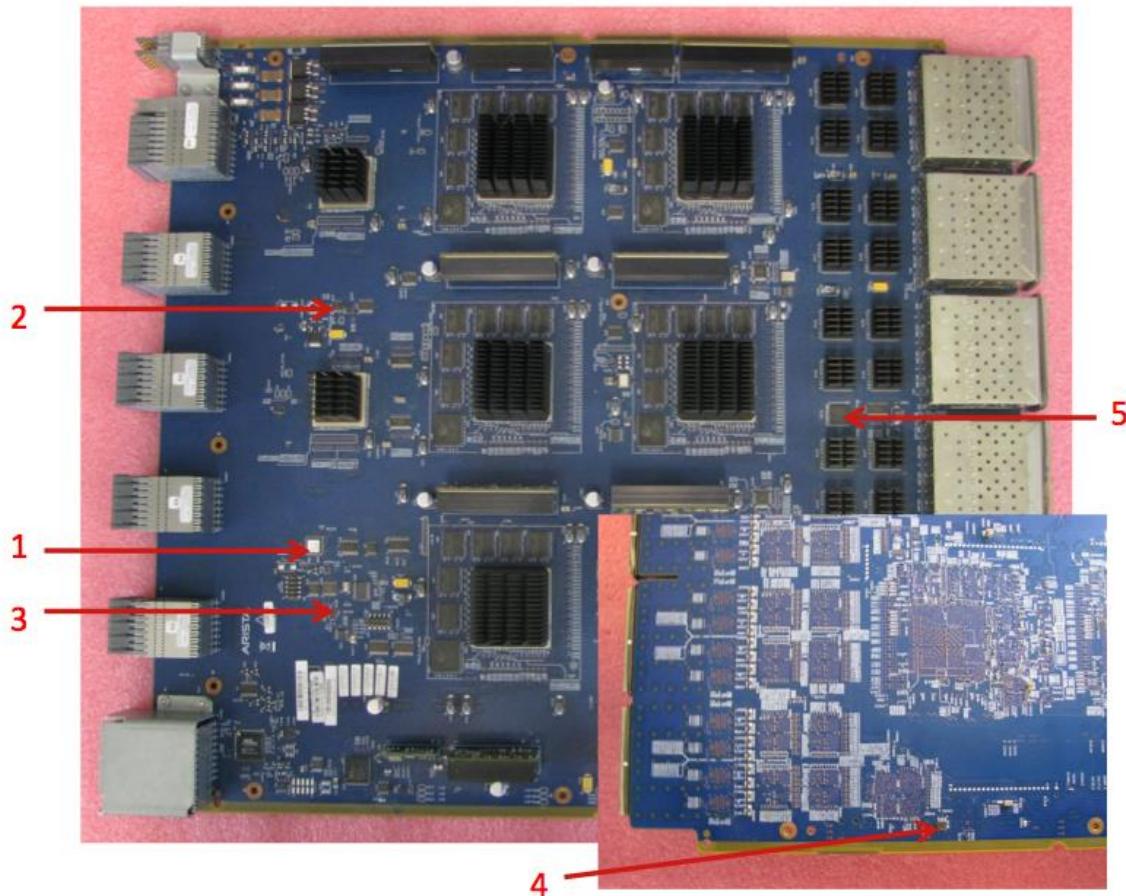


Figure 54-DCS-7548S Linecard Non-volatile Memory Map

## 6.18. DCS-7500E-36Q Linecard

Table 56- DCS-7500E-36Q Linecard Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1-96	DRAM-DDR3	Temporary Packet Storage	No	Yes	1Gb

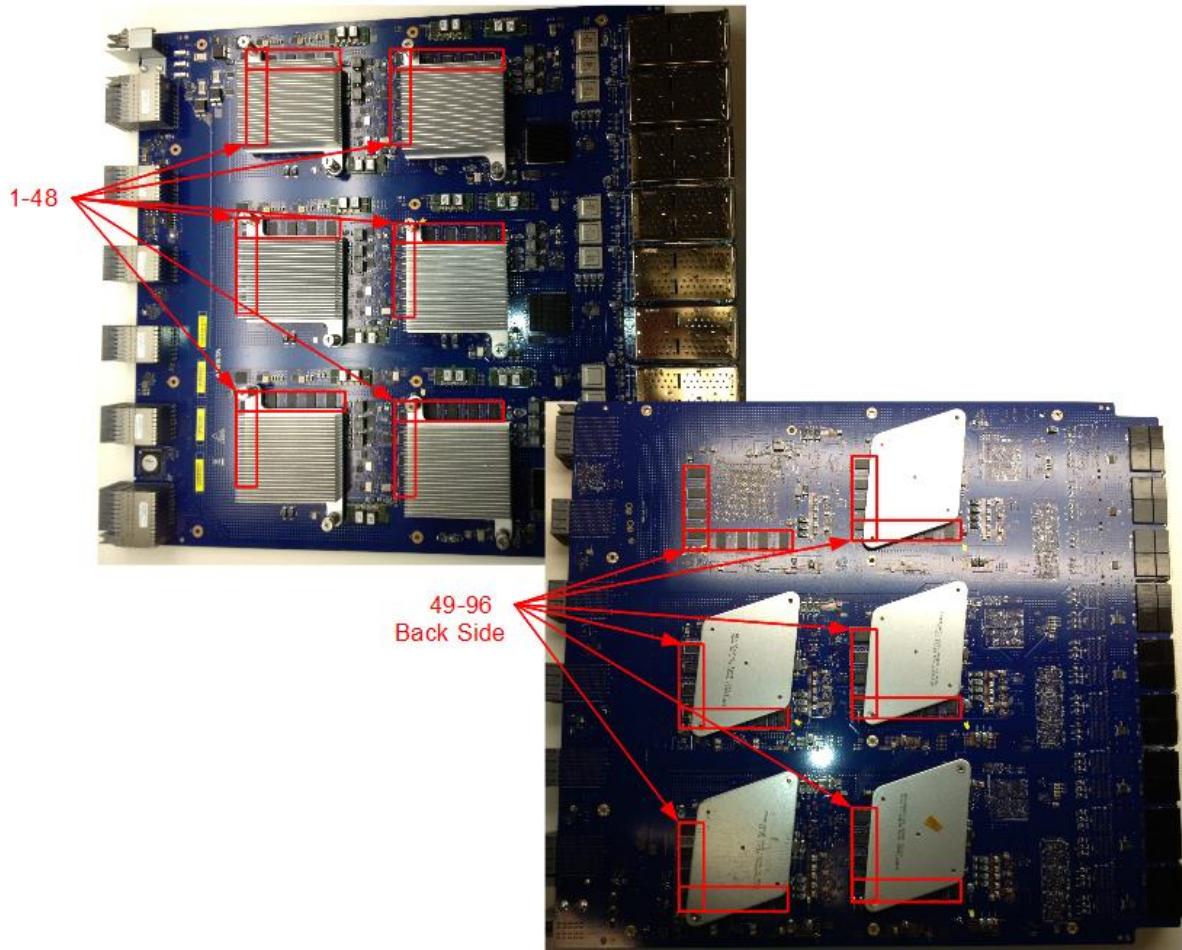


Figure 55- DCS-7500E-36Q Linecard Volatile Memory Location

Table 57- DCS-7500E-36Q Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	FPGA Config Flash	No	No	16MB
2	Microcontroller	Security	No	No	134KB
3-8	Flash	PCIE	No	No	512KB
9	Flash	Manufacturer data	No	No	512KB

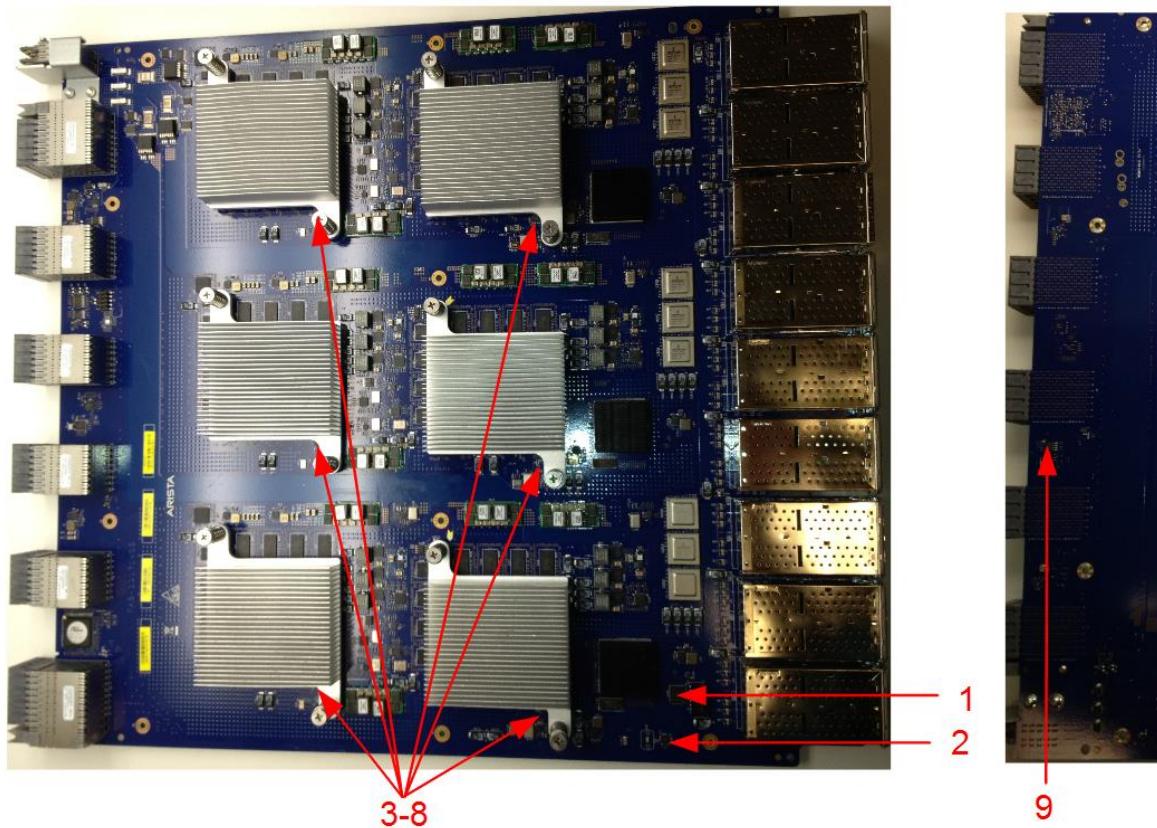


Figure 56- DCS-7500E-36Q Linecard Non-volatile Memory Map

## 6.19. DCS-7500E-xxS Linecard

Table 58- DCS-7500E-xxS Linecard Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1-48	DRAM-DDR3	Temporary Packet Storage	No	Yes	1Gb

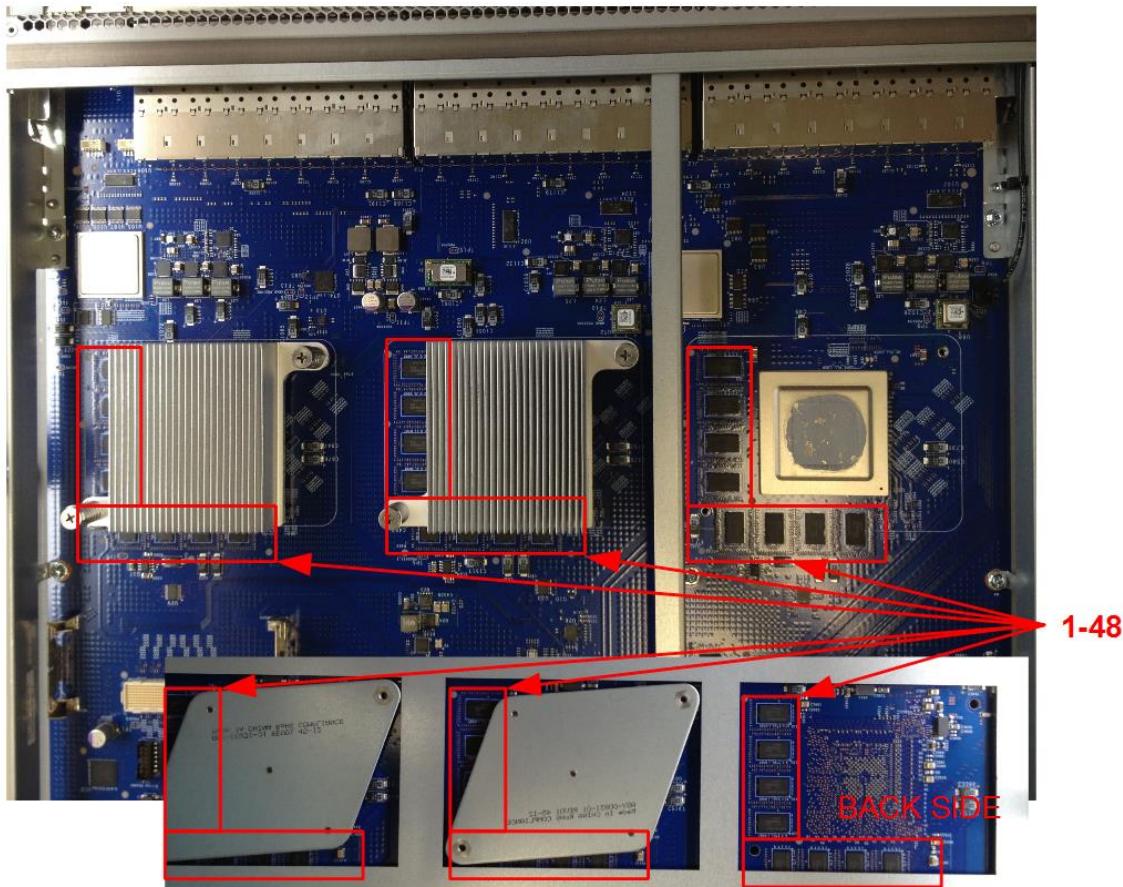


Figure 57- DCS-7500E-xxS Linecard Volatile Memory Location

Table 59- DCS-7500E-xxS Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	FPGA Config flash	No	No	16MB
3	Microcontroller	Security	No	No	134KB

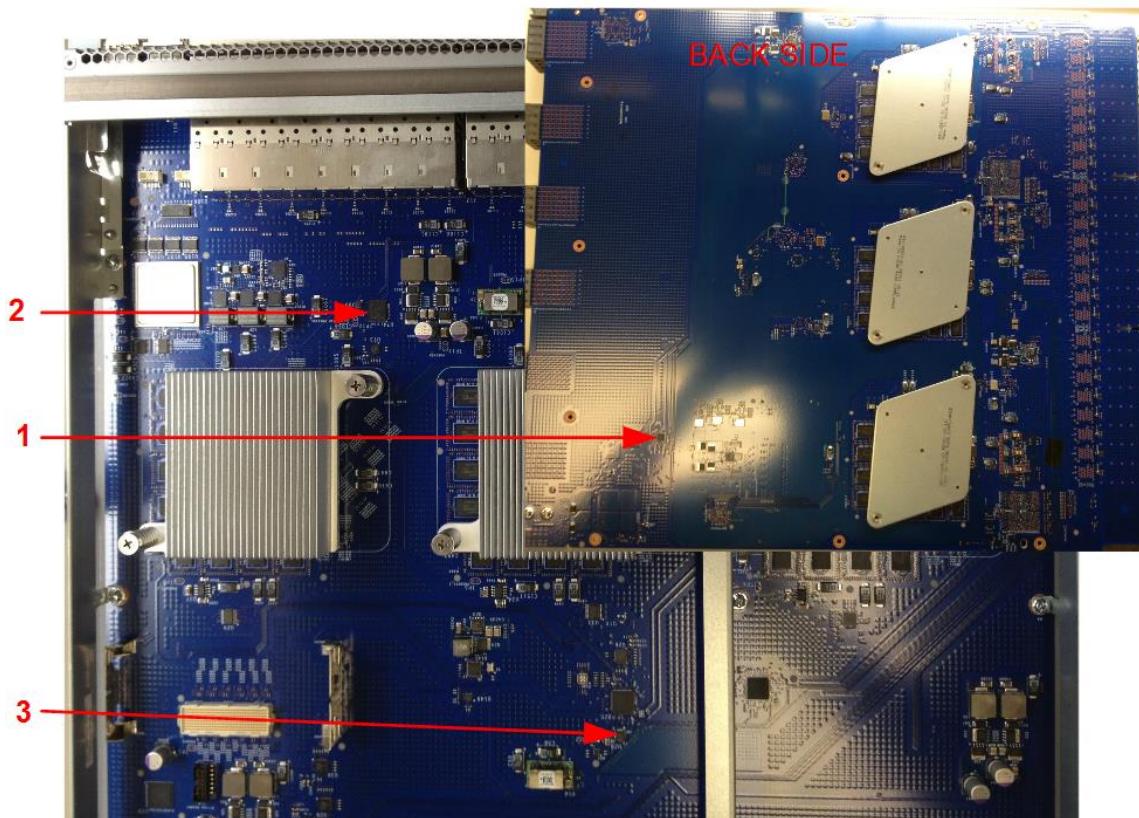


Figure 58- DCS-7500E-xxS Linecard Non-volatile Memory Map

## 6.20. DCS-7500E-6C2-LC Linecard

Table 60- DCS-7500E-6C2-LC Linecard Volatile Memory Map

Location	Type	Application	Removable	User Data	Size (each)
1-48	DRAM-DDR3	Temporary Packet Storage	No	Yes	1Gb

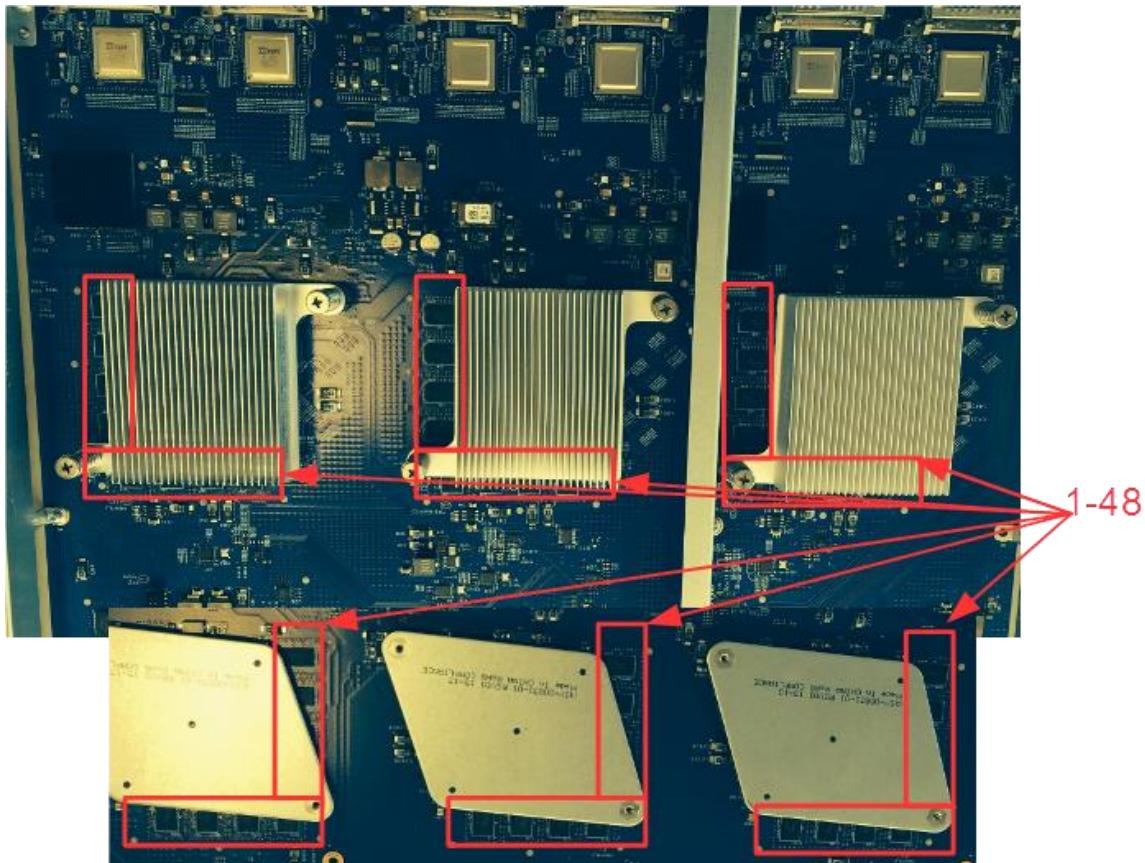


Figure 59- DCS-7500E-6C2-LC Linecard Volatile Memory Location

Table 61- DCS-7500E-6C2-LC Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512KB
2	Flash	FPGA Config flash	No	No	16MB
3	Microcontroller	Security	No	No	134KB

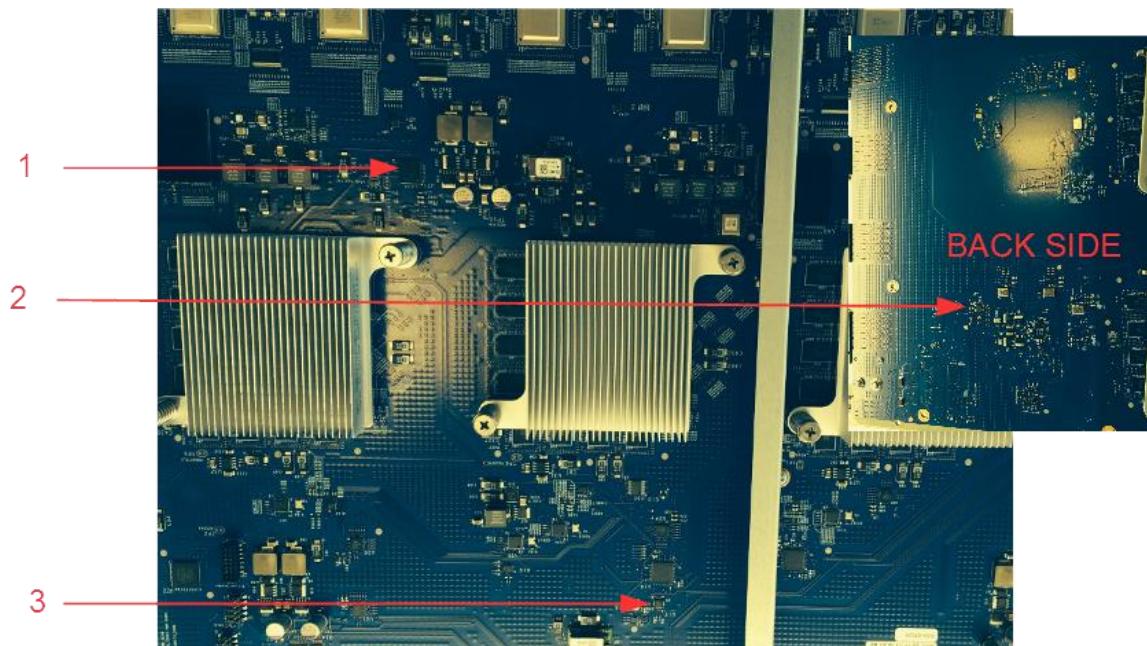


Figure 60- DCS-7500E-6C2-LC Linecard Non-volatile Memory Map

## 6.21. DCS-7300X-32Q Linecard

No volatile memory.

Table 62- DCS-7300X-32Q Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512Kbit
2	Flash	FPGA Config flash	No	No	16Mbit
3	Flash	PCIE	No	No	512Kbit
4	Flash	Switch EEPROM	No	No	512Kbit
5	Flash	Switch EEPROM	No	No	512Kbit

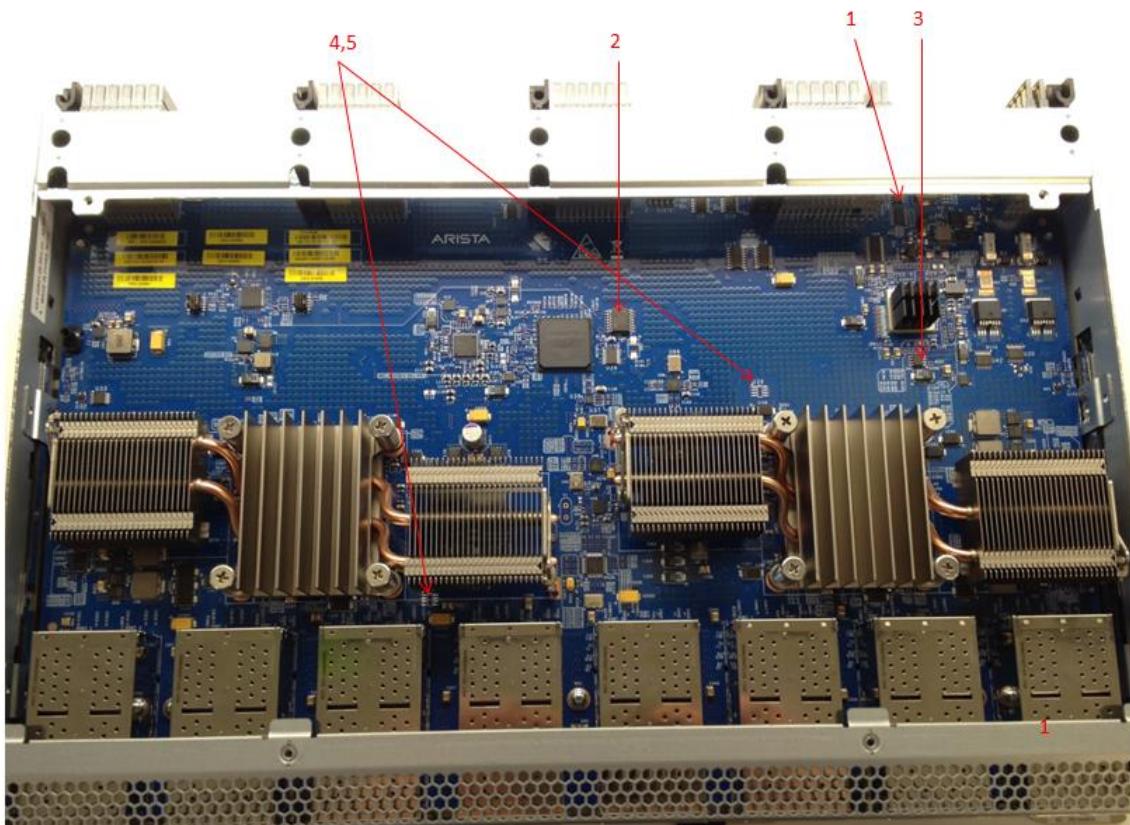


Figure 61- DCS-7300X-32Q Linecard Non-volatile Memory Map

## 6.22. DCS-7300X-64S Linecard

No volatile memory.

Table 63- DCS-7300X-64S Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512Kbit
2	Flash	FPGA Config flash	No	No	16Mbit
3	Flash	PCIE	No	No	512Kbit
4	Flash	Switch EEPROM	No	No	512Kbit

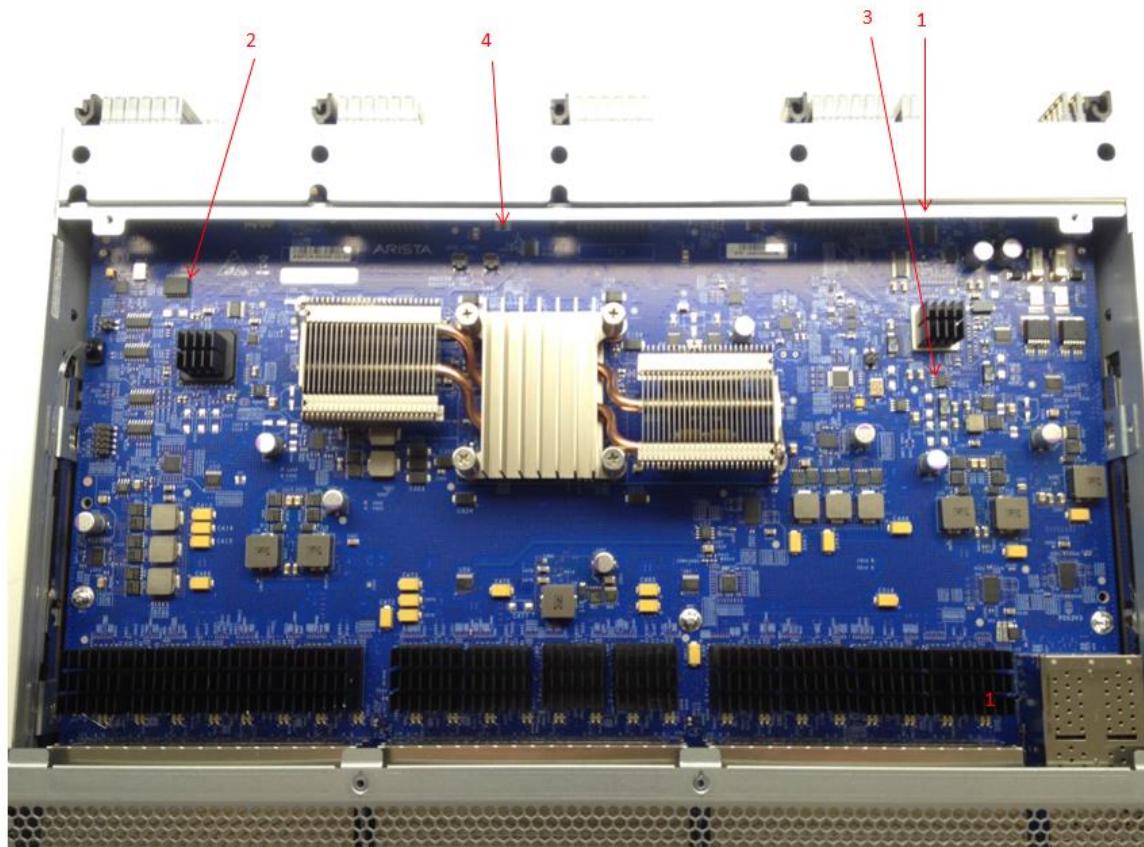


Figure 62- DCS-7300X-64S Linecard Non-volatile Memory Map

## 6.23. DCS-7300X-64T Linecard

No volatile memory.

Table 64- DCS-7300X-64T Linecard Non-volatile Memory Map

Location	Type	Application	Removable	User Data	Size
1	Flash	Manufacturer data	No	No	512Kbit
2	Flash	FPGA Config flash	No	No	16Mbit
3	Flash	PCIE	No	No	512Kbit
4	Flash	Switch EEPROM	No	No	512Kbit

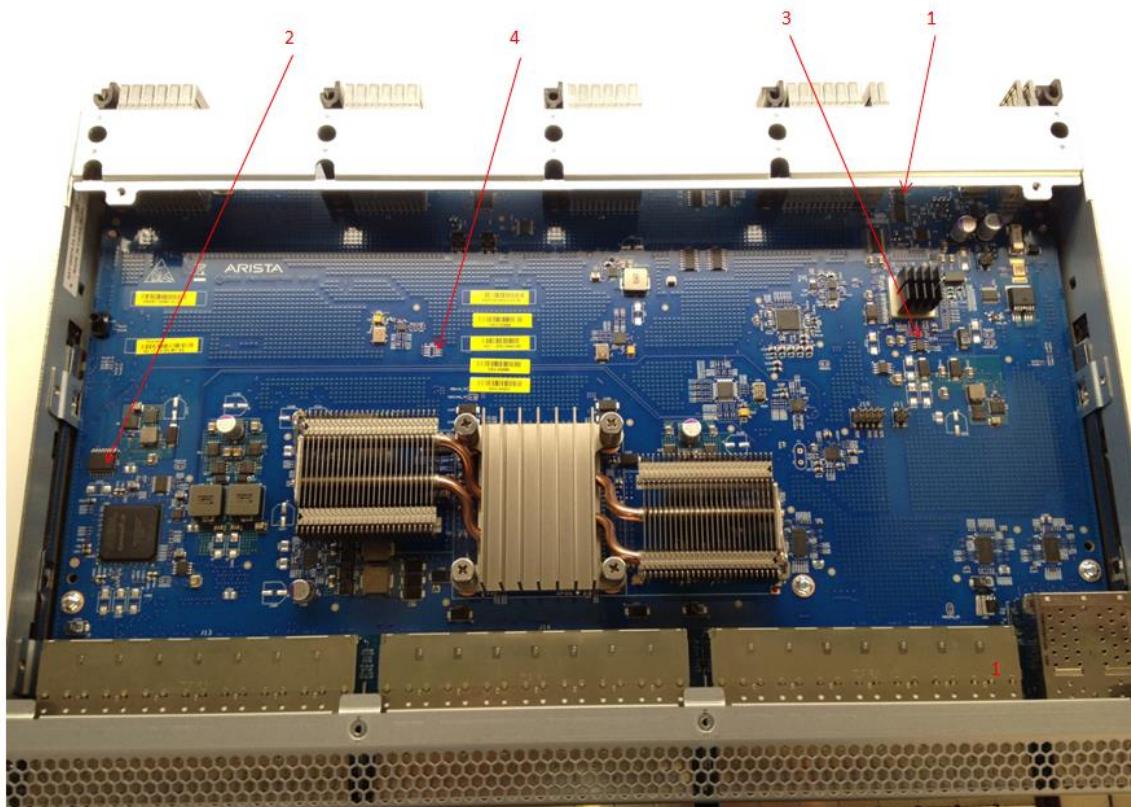


Figure 63- DCS-7300X-64T Linecard Non-volatile Memory Map

## 6.24. 7500/7500E Non-Volatile Flash Removal Procedure

In order to protect user data it may be necessary to remove the Flash RAM installed in the Arista modular switches. The only item that requires removal for the sanitization of the Arista DCS-750x series switches is the Flash contained on the Supervisor Module/s, and so will be the only component referenced in these procedures. As with all electronic devices, Electrostatic Discharge (ESD) standards must be adhered to in order to prevent damage to sensitive electronic components. *Any damage to Arista owned equipment during the Flash removal procedures due to non-compliance with ESD standards will be the responsibility of the possessor.* A comprehensive list of ESD standards can be found here: <http://www.esda.org/>

- 1) Unplug the switch and follow guidance regarding grounding (ESD) procedures
- 2) Remove the Supervisor Module from the chassis. Reference photo below:

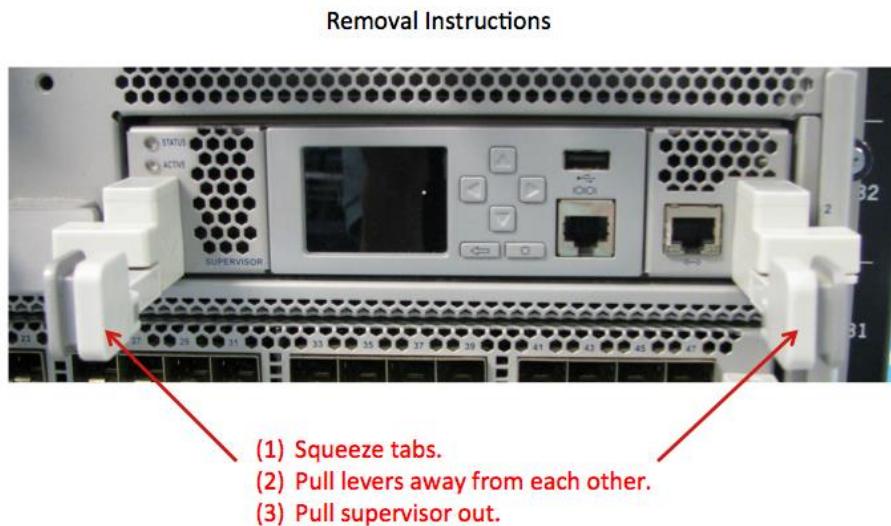


Figure 64-Modular switches Non-Volatile Memory Removal

- 3) Reference the corresponding (7500 / 7500E) Supervisor Module *Non-Volatile Memory Map* for Flash identification (Item 6)
- 4) Use a Phillips head screwdriver to loosen the plastic retaining screw on the Flash RAM
- 5) Remove the Flash by gently pulling straight up off of the board
- 6) If a USB flash drive is located in the USB connector on the user-facing side of the supervisor panel, unplug it
- 7) Dispose of the Flash in accordance with local policy
- 8) Replace the Supervisor Module into the chassis

- 9) Notify your Arista account team that you have performed sanitization procedures on the equipment

## 7. Arista 2-RU Switch Family

CPU card is common to all 2-RU products.

### 7.1 CPU Card Memory Map

CPU card is located on lower deck. Refer to switch section for upper deck removal instructions.

Table 65 - CPU Board Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	DRAM – DDR3	CPU Memory	Yes	Yes	4 GB x2
2	RAM (Battery Backup)	RTC	No	No	NA
3	RAM	Digital Potentiometer	No	No	NA x2

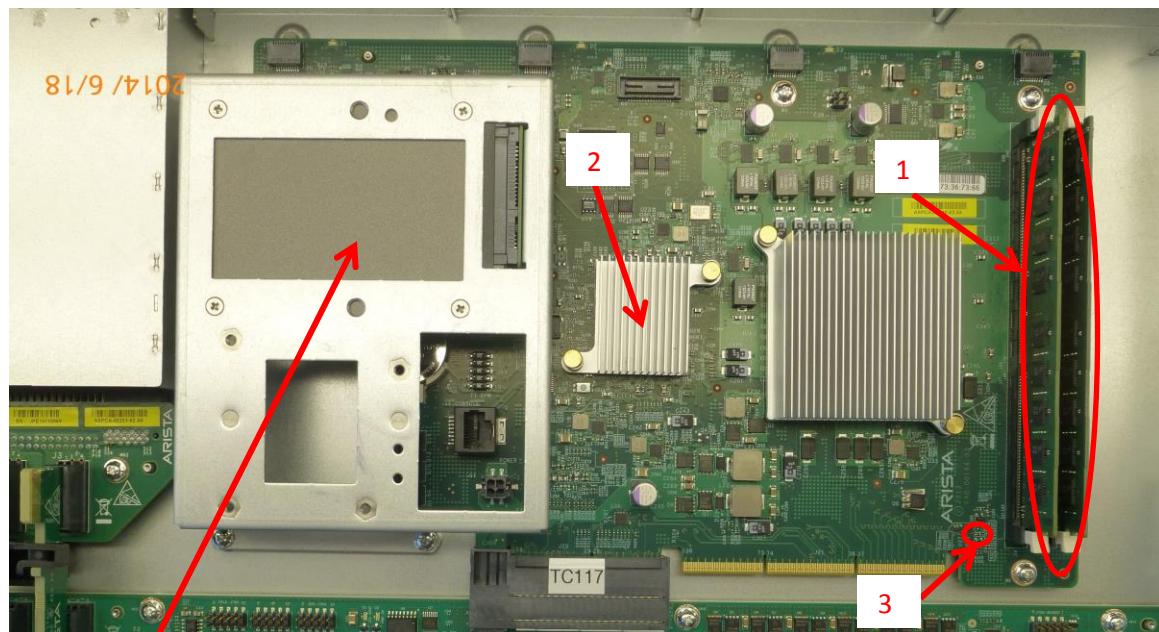


Figure 65: CPU Card Volatile Memory and SSD Locations

8 (SSD, on the table below)

Table 66 - CPU Board Non-Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	Flash	Power Supply	No	No	NA
2	Flash	Power Controller	No	No	NA
3	Flash	Power Controller	No	No	NA
4	Flash	CPLD	No	No	NA
5	Flash	GBE Configuration	No	No	256 Kb
6	Flash	Boot Code	No	No	64 Mb
7	Flash	USB Flash Drive	Yes	Yes	4 GB
8, shown above	Flash	Solid-State Driver, optional data storage	Yes	Yes	100 GB

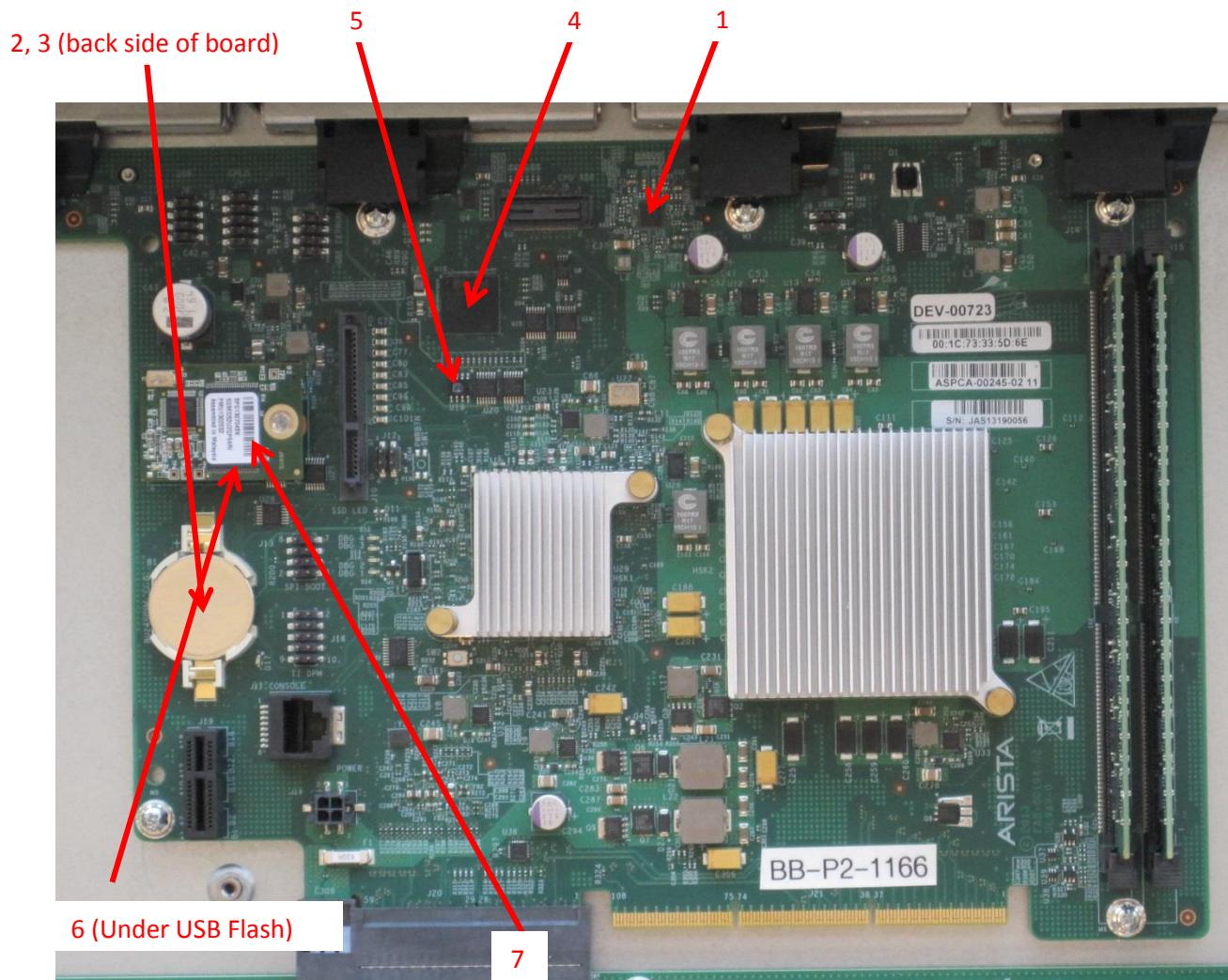


Figure 66: CPU Board Non-Volatile Memory Locations

## 7.2 DCS-7050SX-128

### 7.2.1 Upper Switch Card Memory Map

The switch card on upper deck contains only non-volatile memory.

Table 67 – Upper Switch Card Non-volatile Memory

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Board MFG info, serial number	No	No	2k bytes
2	Flash	Power Controller	No	No	NA

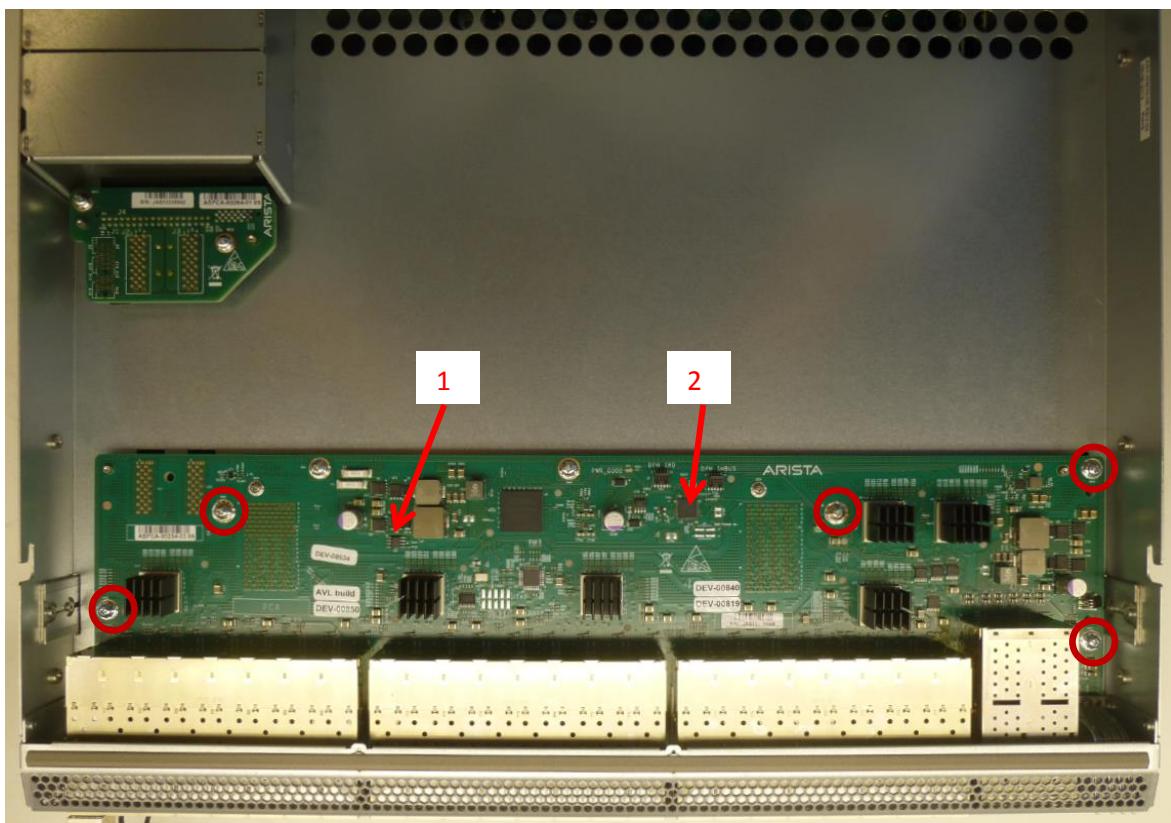


Figure 67: DCS-750SX-128 Upper Switch Card non-volatile Locations

To expose lower deck, follow the steps below:

1. Remove power supplies (PSU)
2. Remove fan modules
3. Remove 5 screws circled in red above
4. Lift up the upper deck

## 7.2.2 Lower Switch Card Memory Map

The switch card on lower deck contains only non-volatile memory.

Table 68 – Lower Switch Card Non-volatile Memory

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Board MFG info, serial number	No	No	2k bytes
2	Built-in Flash	Power Controller	No	No	NA
3	Flash	FPGA configuration	No	No	8M bytes
4	Built-in Flash	CPLD configuration	No	No	NA

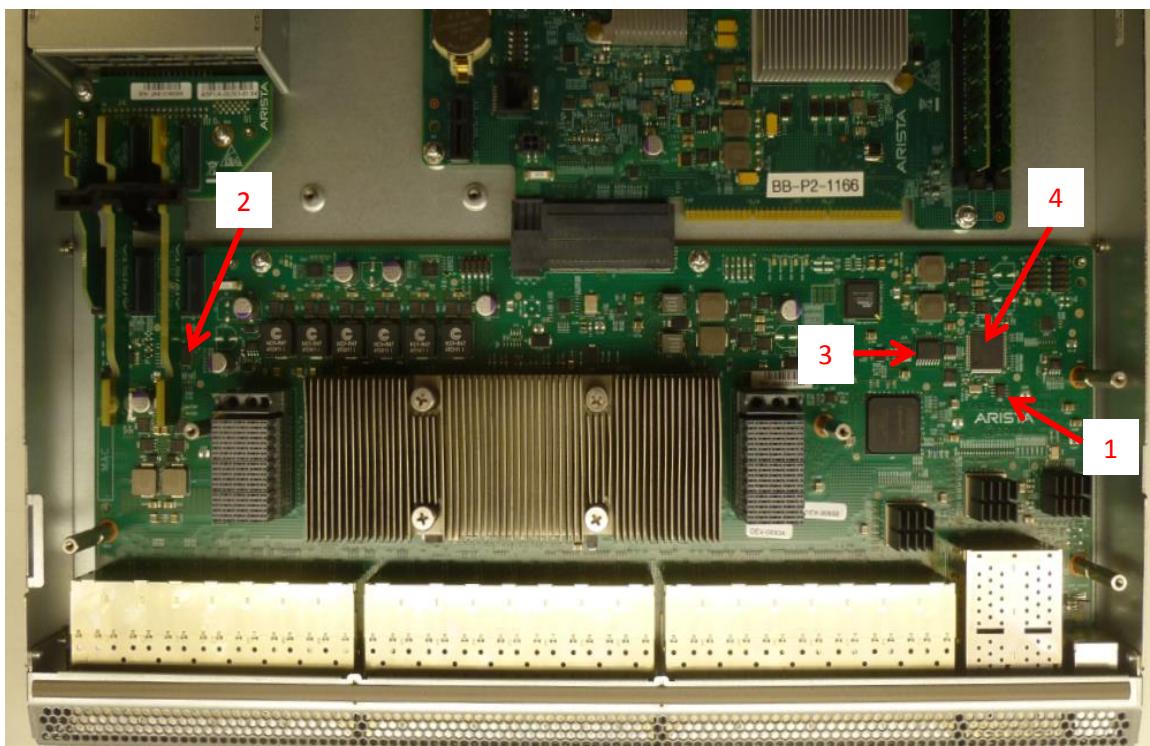


Figure 68: DCS-750SX-128 Lower Switch Card non-volatile Locations

## 7.3 DCS-7250QX-64

### 7.3.1 Upper Switch Card Memory Map

Table 69: Upper Switch Card Non-volatile Memory

Location	Type	Application	Removable	User Data	Size
1	SEEPROM	Clocking configuration	No	No	N/A
2	SEEPROM	Manufacturer data	No	No	256 KB
3	Flash	FPGA Configuration	No	No	8 MB
4	SEEPROM	System configuration	No	No	8 MB
5	CPLD	System configurations	No	No	128 KB
6	FLASH	Power Controller storage	No	No	64 KB
7	SEEPROM	Manufacturer data	No	No	2 KB

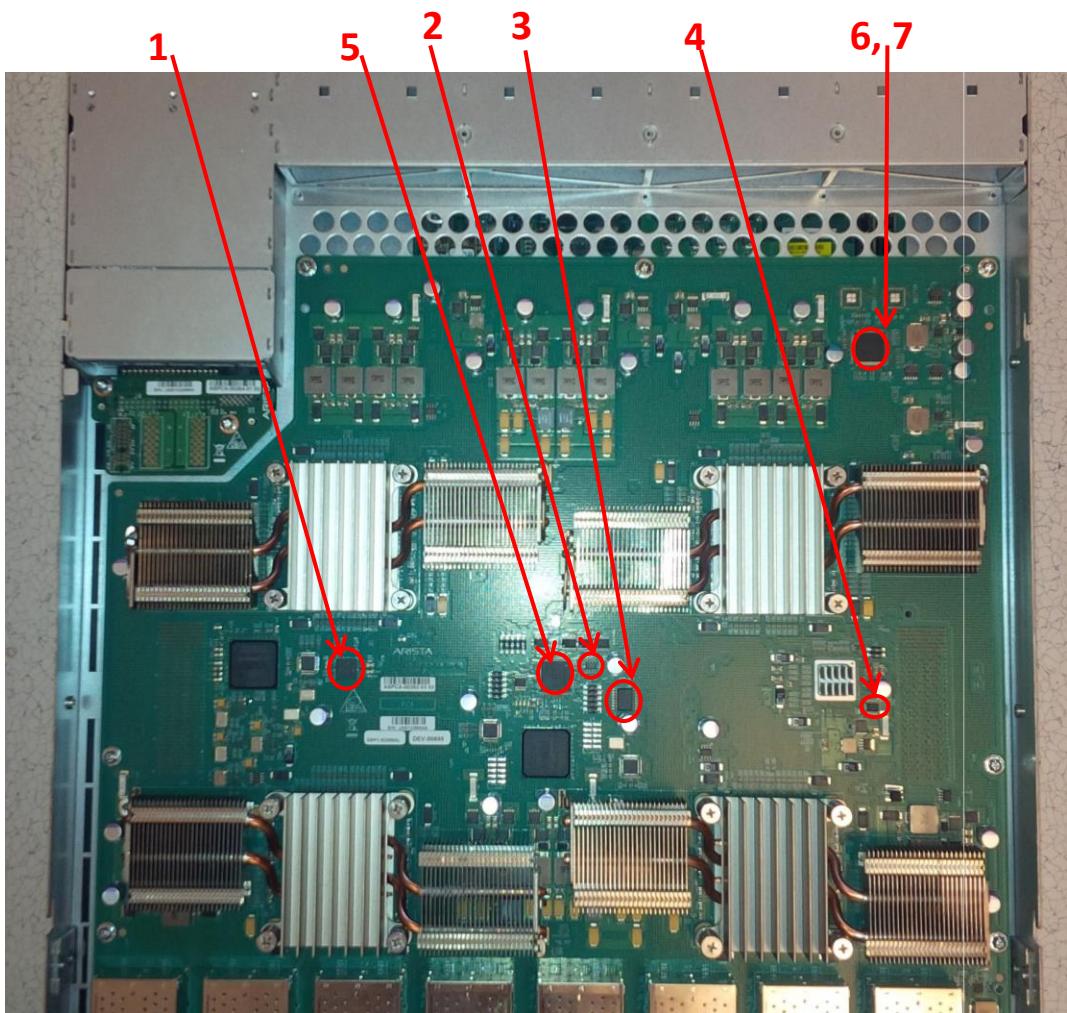


Figure 69: DCS-7250QX-64 Upper Switch Card Non-volatile Memory Locations

Table 70: Upper Switch Card Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	Switch ASIC	Temporary packet storage	No	Yes	12 MB
2	Switch ASIC	Temporary packet storage	No	Yes	12 MB
3	Switch ASIC	Temporary packet storage	No	Yes	12 MB
4	Switch ASIC	Temporary packet Storage	No	Yes	12 MB
5	FPGA	System Configurations	No	No	138 MB
6	FPGA	System Configurations	No	No	320 MB
7	SRAM	Power Controller Storage	No	No	8 KB

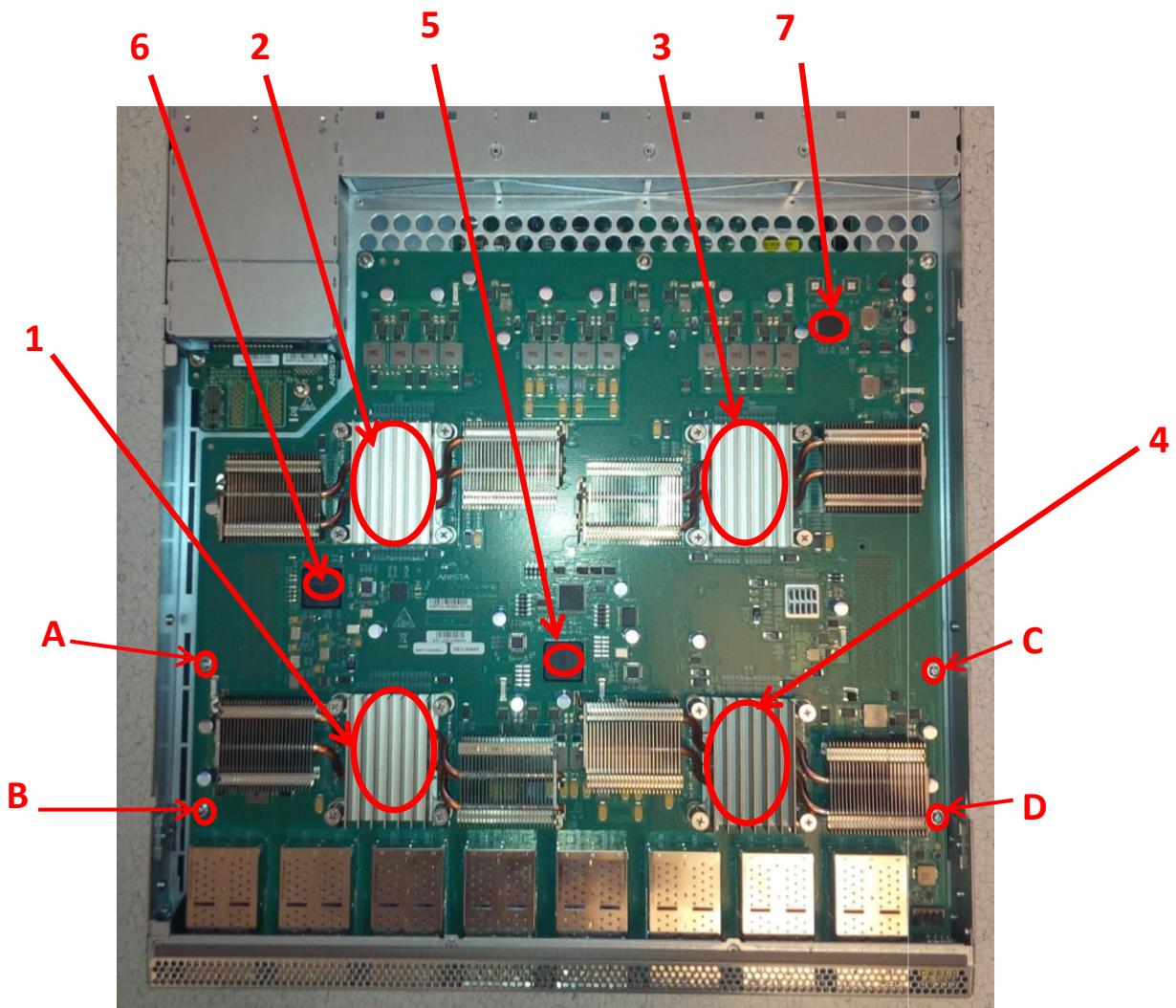


Figure 70: DCS-7250QX-64 Upper Switch Card Volatile Memory Locations

To expose the lower deck, follow the steps below:

1. Remove power supplies (PSU).
2. Remove fan modules.
3. Remove 4 screws labeled A, B, C, D in Figure 70.
4. Lift up the upper deck.

### 7.3.2 Lower Switch Card Memory Map

Table 71: Lower Switch Card Non-Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	CPLD	System configurations	No	Yes	128 KB
2	Flash	FPGA Configurations Storage	No	No	8 MB
3	Flash	Power Controller Storage	No	No	256 MB
4	EEPROM	PCIe Controller Configurations	No	No	8 KB
5	IDPROM	Manufacturing data	No	No	8 MB

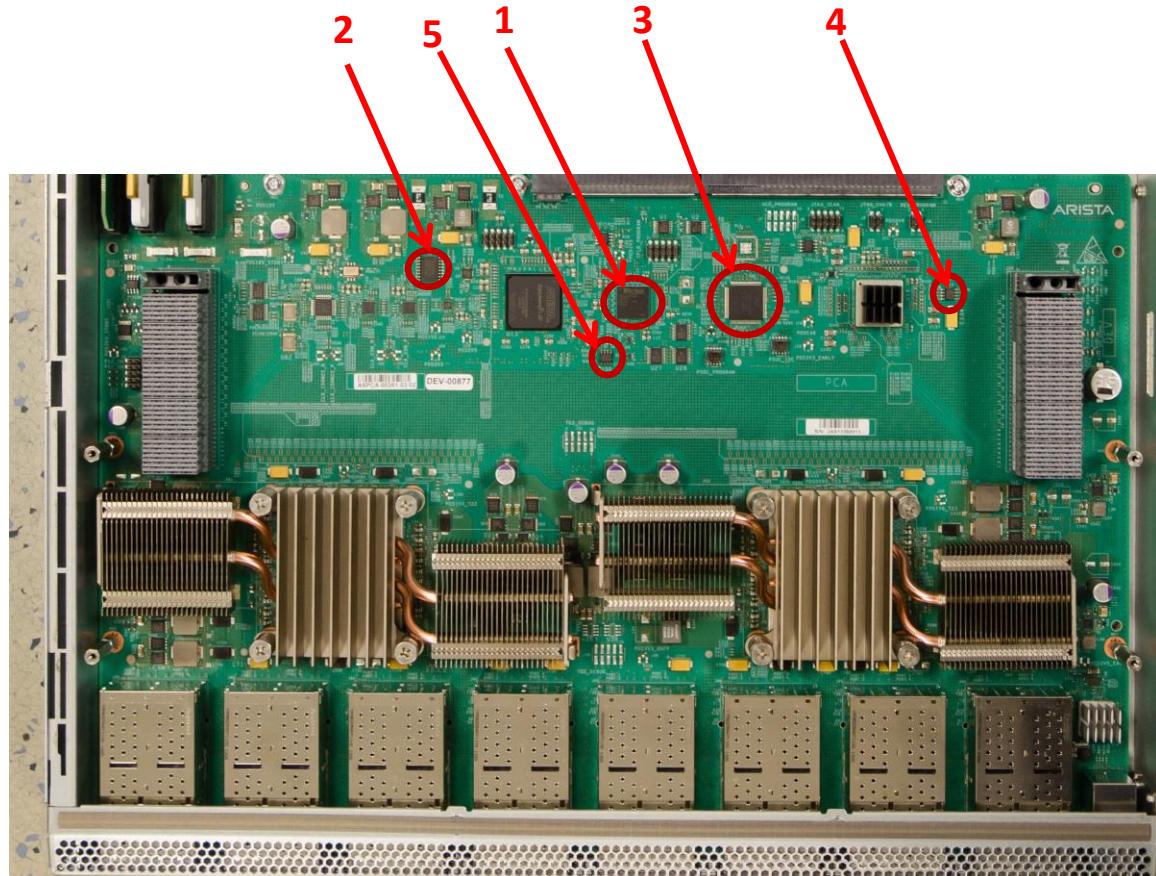


Figure 71: DCS-7250QX-64 Lower Switch Card Non-Volatile Memory Locations

Table 72: Lower Switch Card Volatile Memory

Location	Type	Application	Removable	User Data	Size
1	Switch ASIC	Temporary packet storage	No	Yes	12 MB
2	Switch ASIC	Temporary packet storage	No	Yes	12 MB
3	FPGA	System configurations	No	No	138 MB

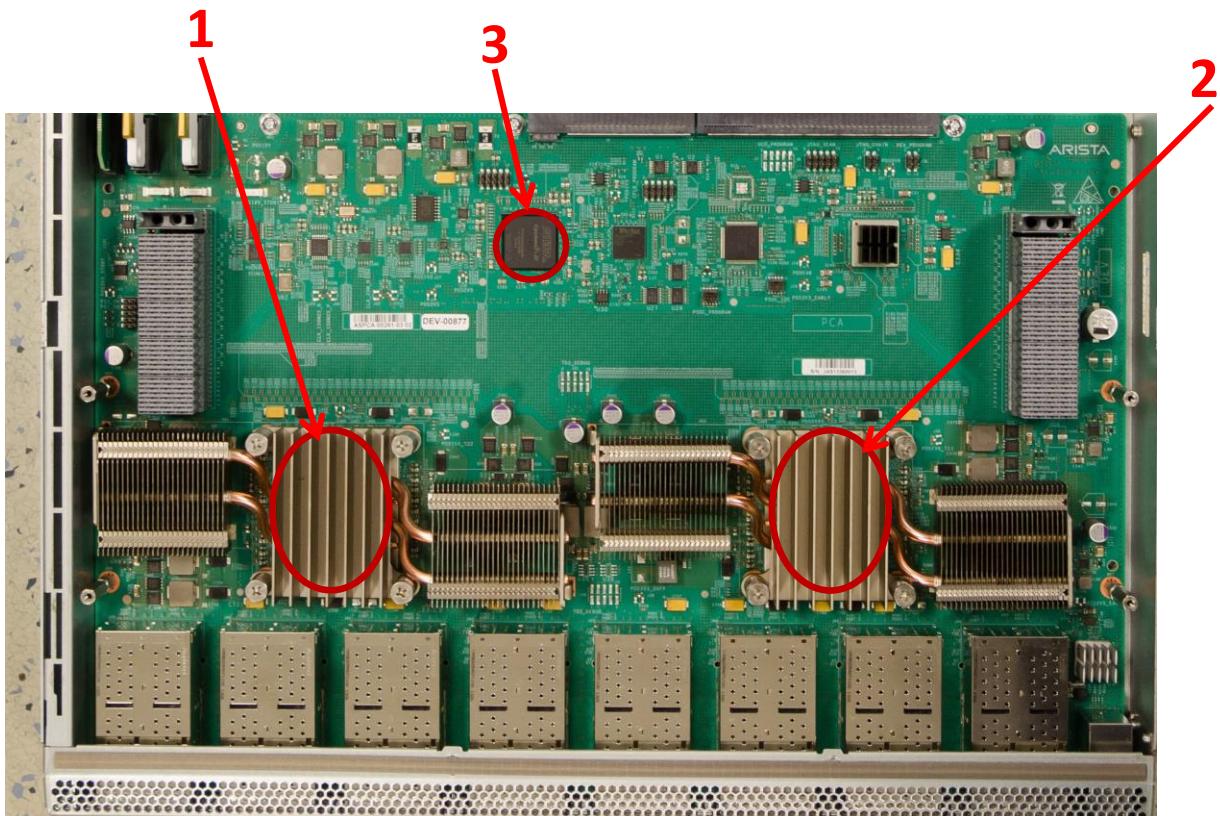


Figure 72: DCS-7250QX-64 Lower Switch Card Volatile Memory Locations

## 7.4 DCS-7050TX-128

### 7.4.1 Upper Switch Card Memory Map

The switch card on upper deck contains only non-volatile memory.

Table 73 – Upper Switch Card Non-volatile Memory

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Board MFG info, serial number	No	No	2k bytes
2	Flash	Power Controller	No	No	NA

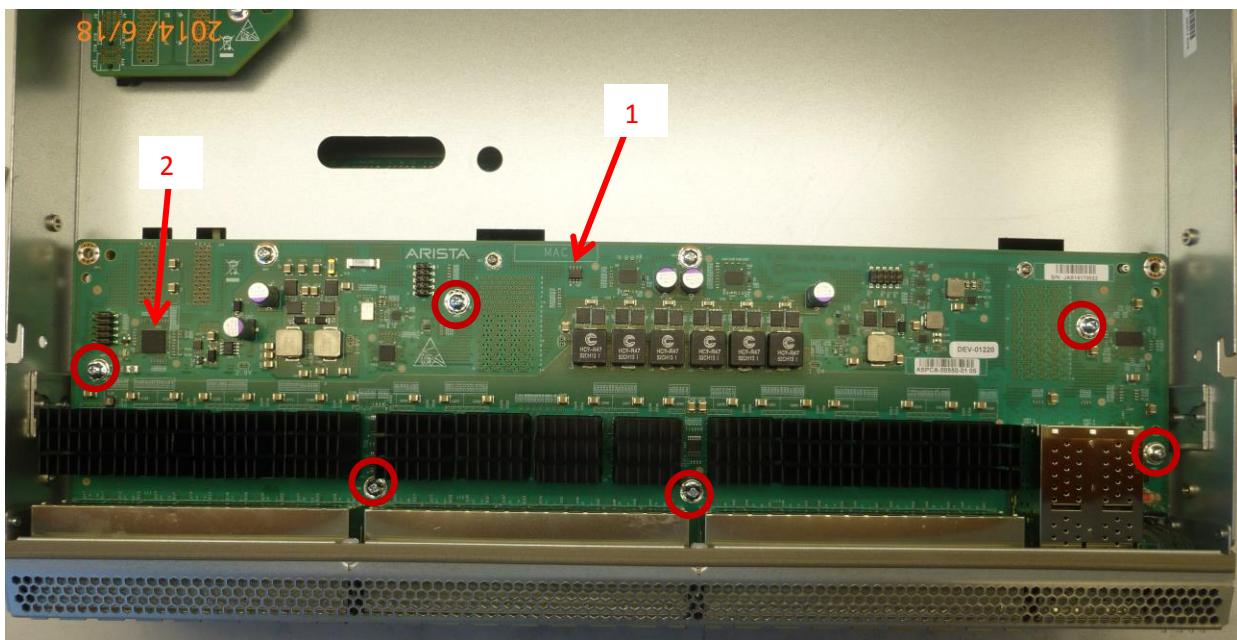


Figure 73: DCS-750TX-128 Upper Switch Card non-volatile Locations

To expose lower deck, follow the steps below:

1. Remove power supplies (PSU)
2. Remove fan modules
3. Remove 6 screws circled in red above
4. Lift up the upper deck

### 7.4.2 Lower Switch Card Memory Map

The switch card on lower deck contains only non-volatile memory. The CPU card on back is listed in Section 7.1.

Table 74 – Lower Switch Card Non-volatile Memory

Location	Type	Application	Removable	User Data	Size
1	EEPROM	Board MFG info, serial number	No	No	2k bytes
2	Built-in Flash	Power Controller	No	No	NA
3	Flash	FPGA configuration	No	No	8M bytes
4	Flash	PHY firmware	No	No	8M bytes
5	Built-in Flash	CPLD configuration	No	No	NA

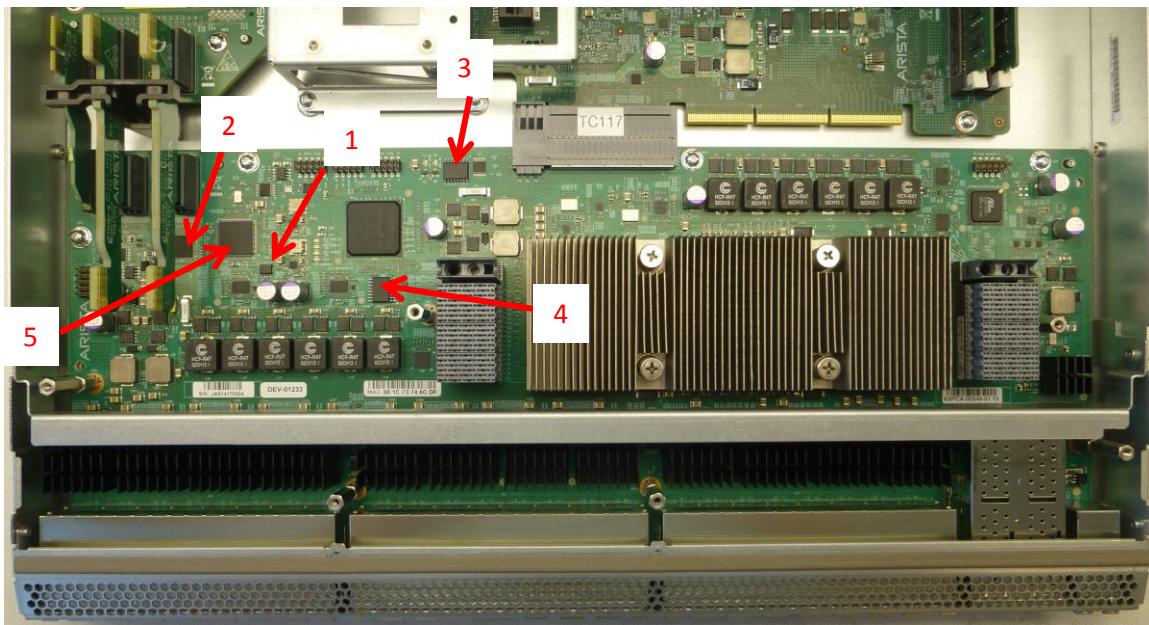


Figure 74: DCS-750TX-128 Lower Switch Card non-volatile Locations

## 8. Removable Optics

The following optional removable optics modules contain a small non-volatile memory. However user data is not stored in this memory nor is it user accessible through normal interfaces. Therefore, no sanitization procedure is required for these items:

10GBASE-SR

10GBASE-SRL

10GBASE-LRM

10GBASE-LR

10GBASE-ER

10GBASE-DWDM

1000BASE-SX

1000BASE-LX

1000BASE-T

Table 75-Optional Removable Optics Modules

Type	Application	Removable	User data	Size
Flash	Manufacturer data	yes	no	Approx. 2KB

